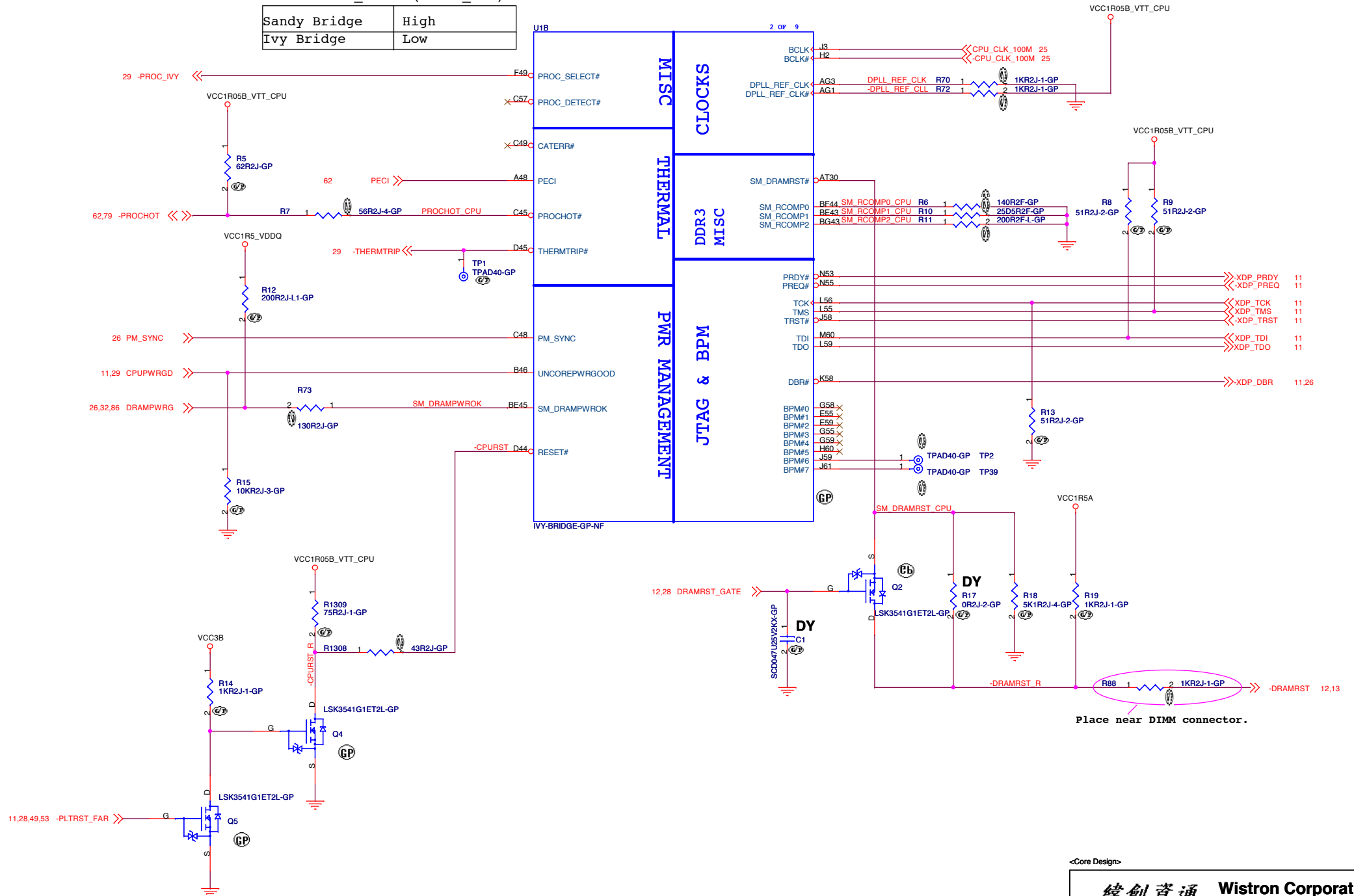


Table -PROC\_SELECT(-PROC\_IVY)

Sandy Bridge	High
Ivy Bridge	Low

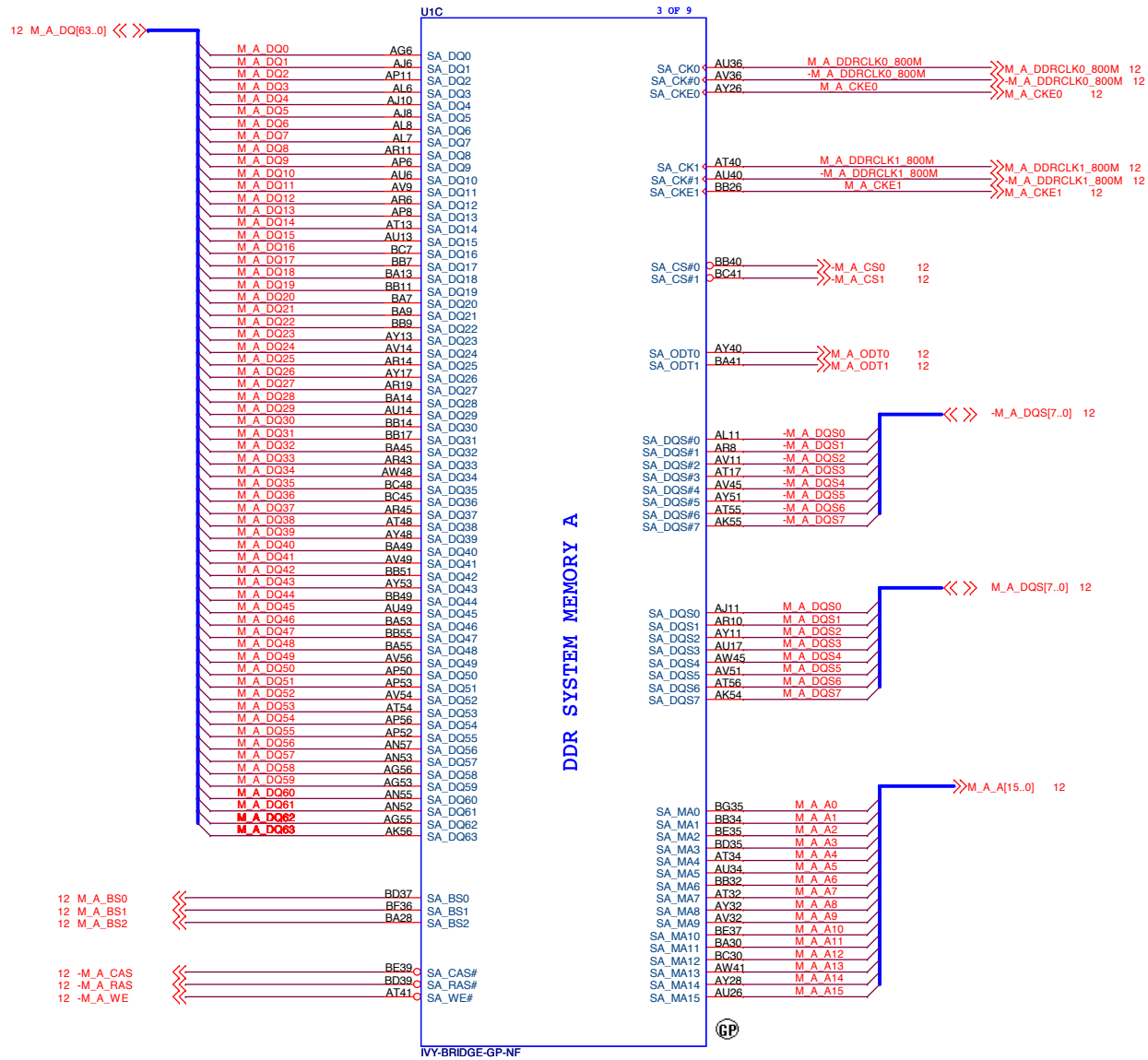


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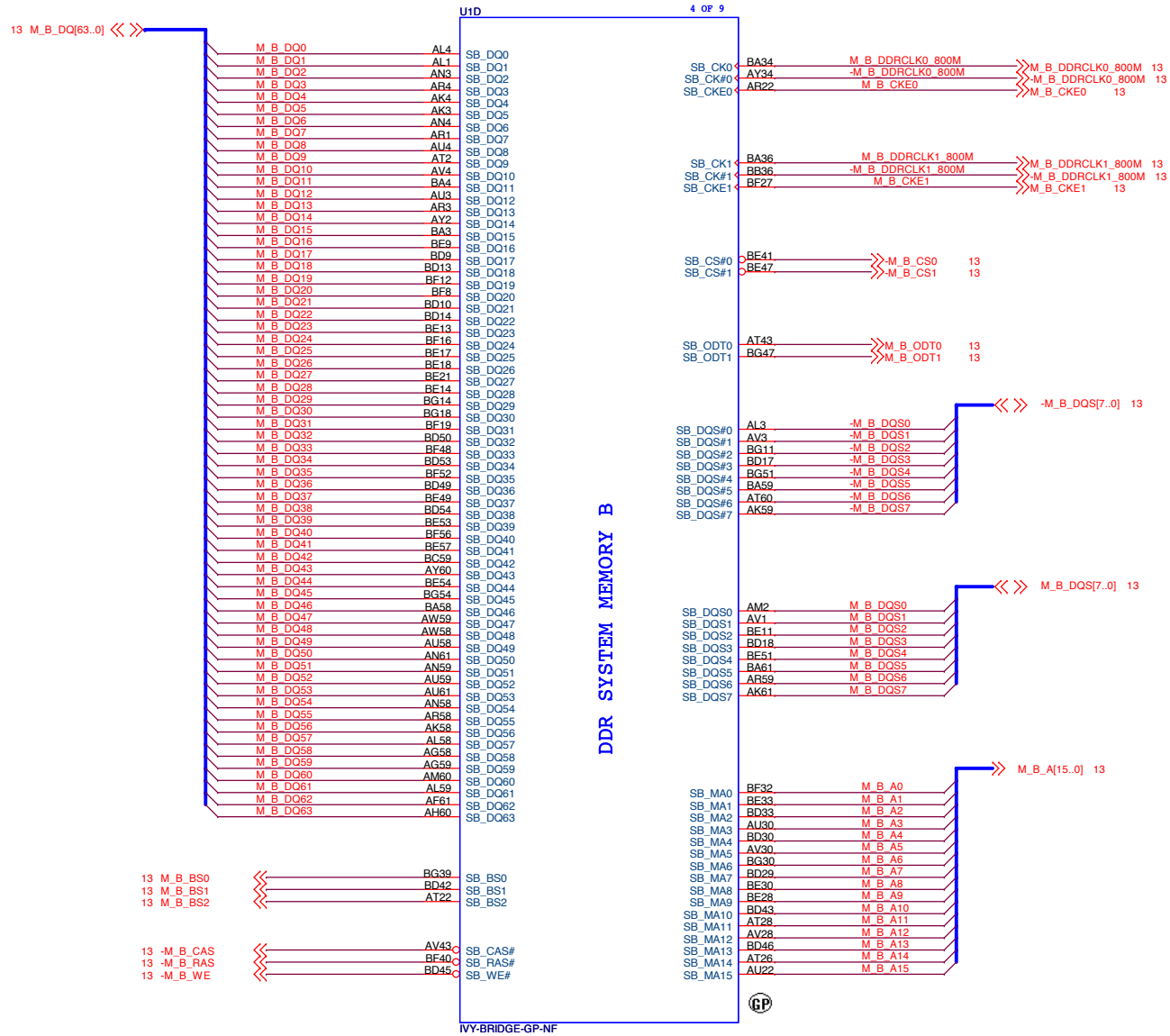
**緯創資通 Wistron Corporation**  
21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

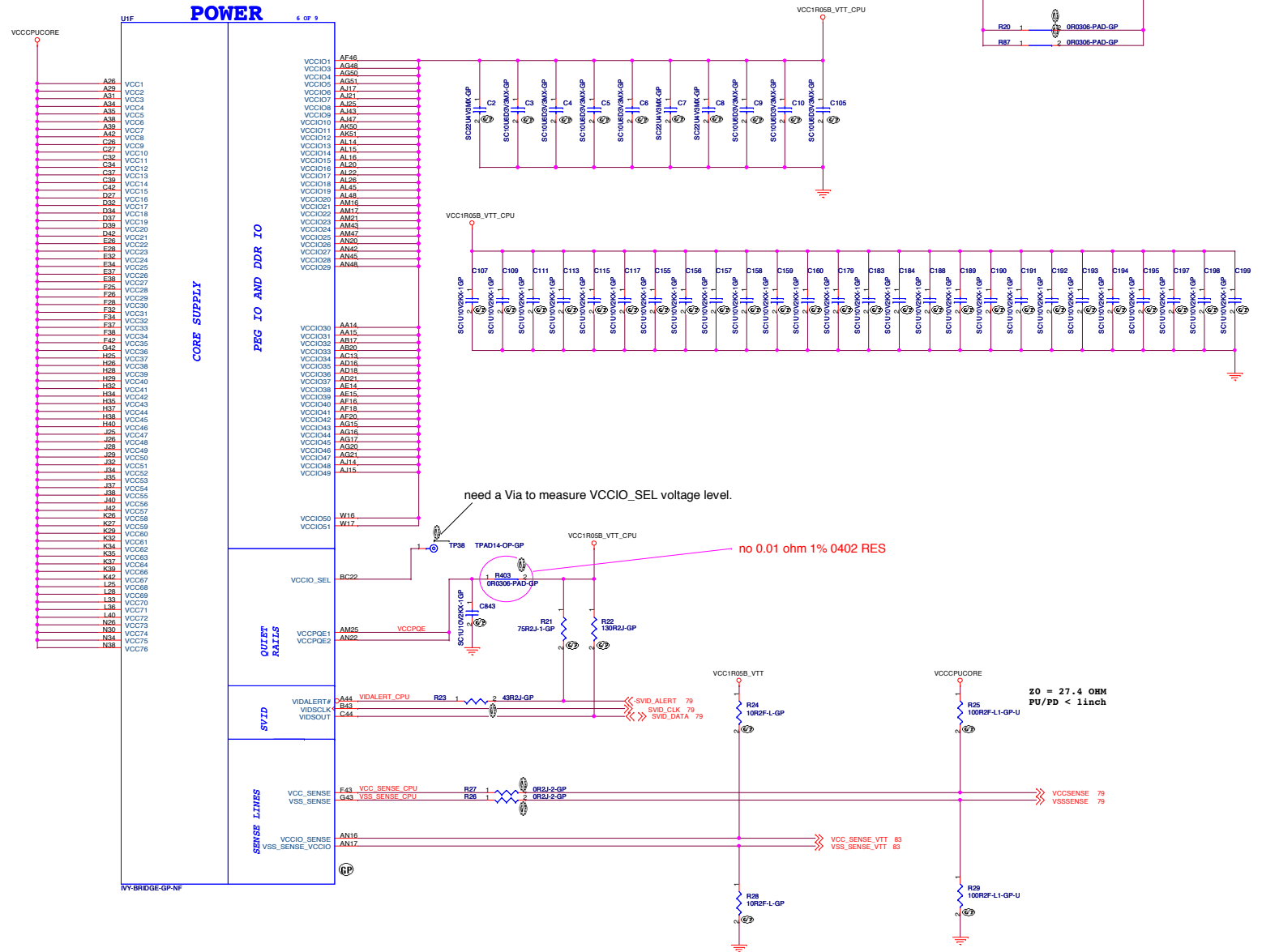
Title **CPU(2/8):CLK/MISC/JTAG**

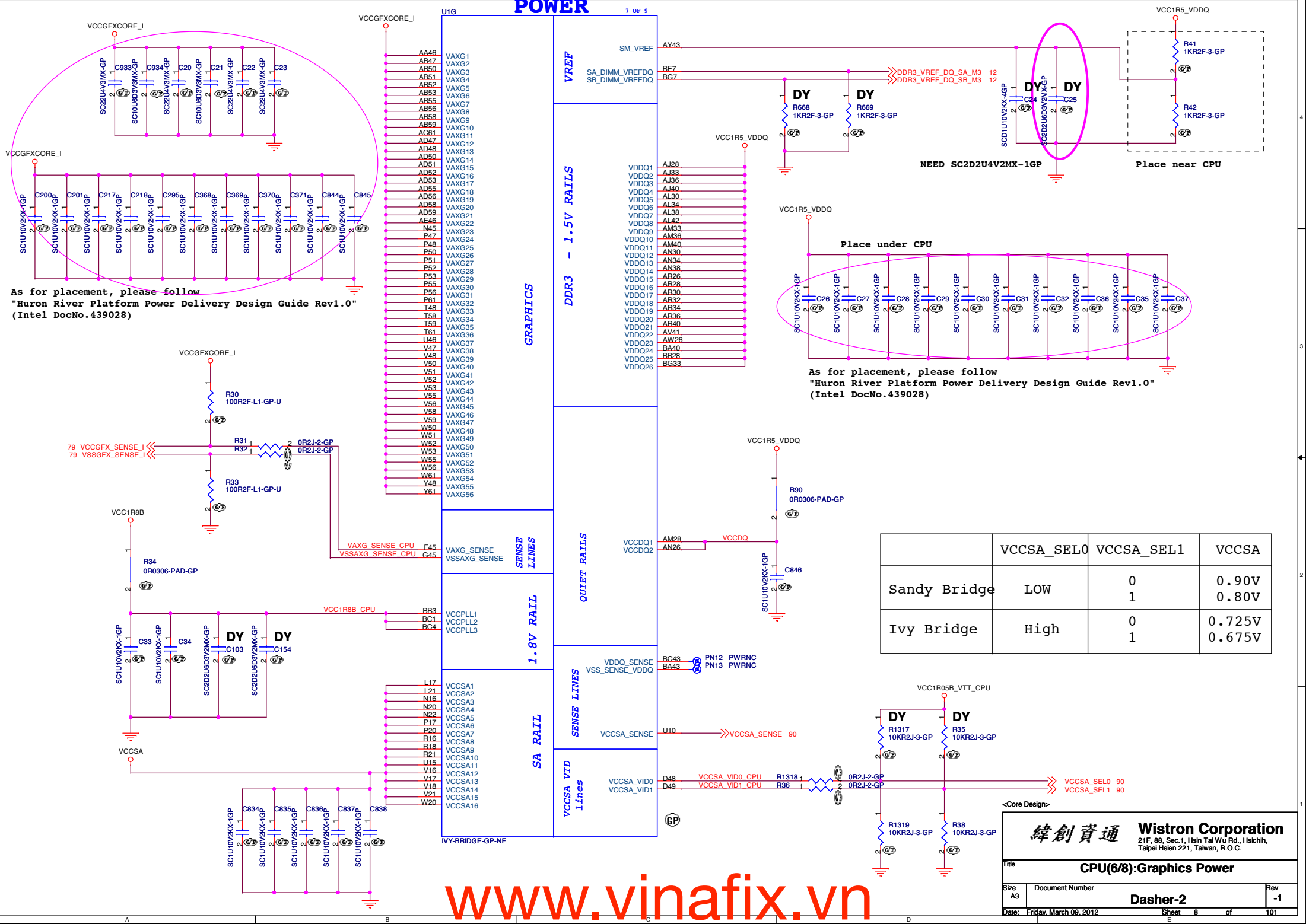
Size A3	Document Number <b>Dasher-2</b>	Rev <b>-1</b>
Date: Friday, March 09, 2012	Sheet 4	of 101



<Core Design>







As for placement, please follow  
"Huron River Platform Power Delivery Design Guide Rev1.0"  
(Intel DocNo.439028)

As for placement, please follow  
"Huron River Platform Power Delivery Design Guide Rev1.0"  
(Intel DocNo.439028)

	VCCSA_SEL0	VCCSA_SEL1	VCCSA
Sandy Bridge	LOW	0 1	0.90V 0.80V
Ivy Bridge	High	0 1	0.725V 0.675V

緯創資通

Wistron Corporation

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CPU(6/8):Graphics Power

Size A3

Document Number

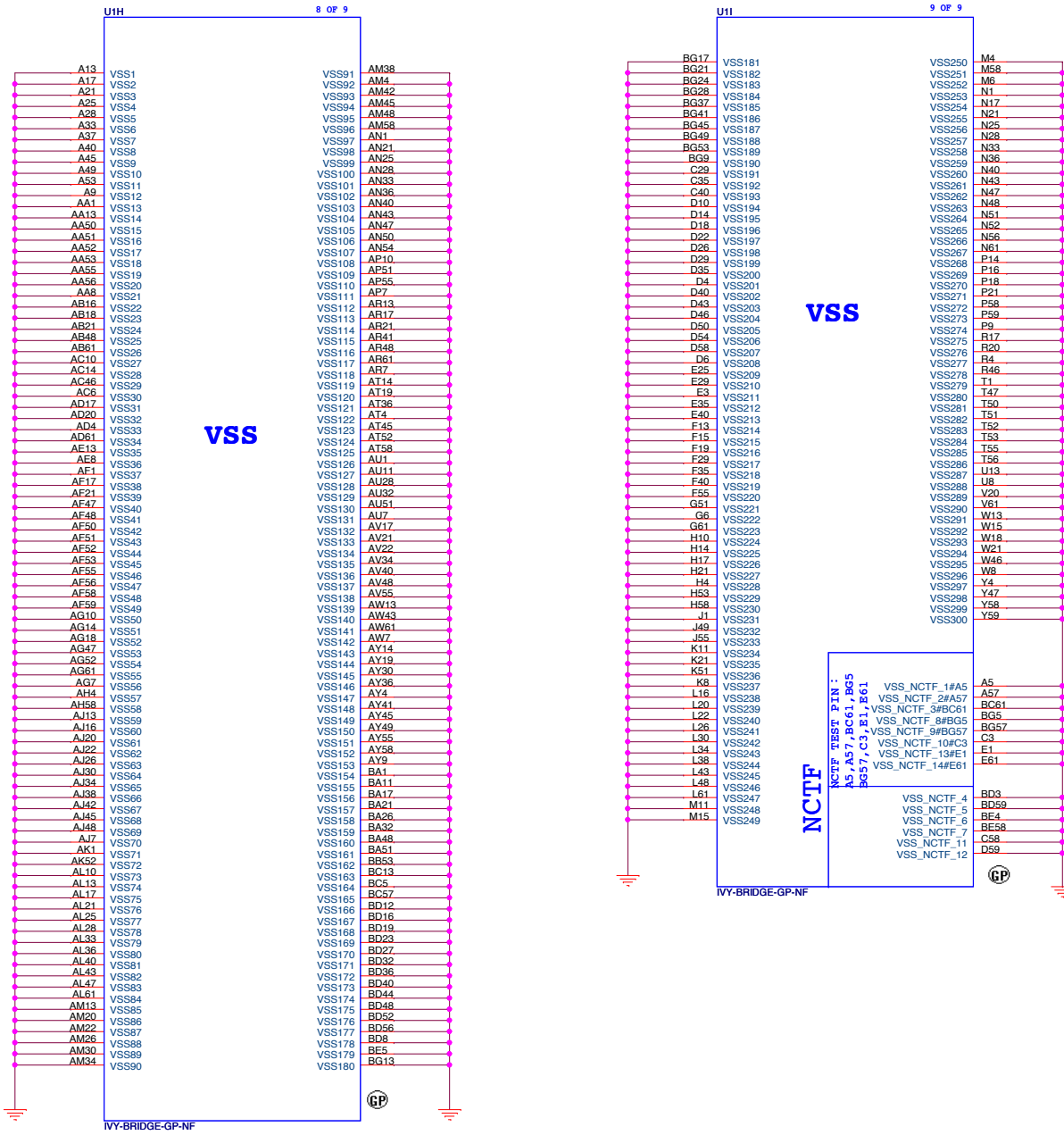
Friday, March 09, 2012

Dasher-2

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Rev -1

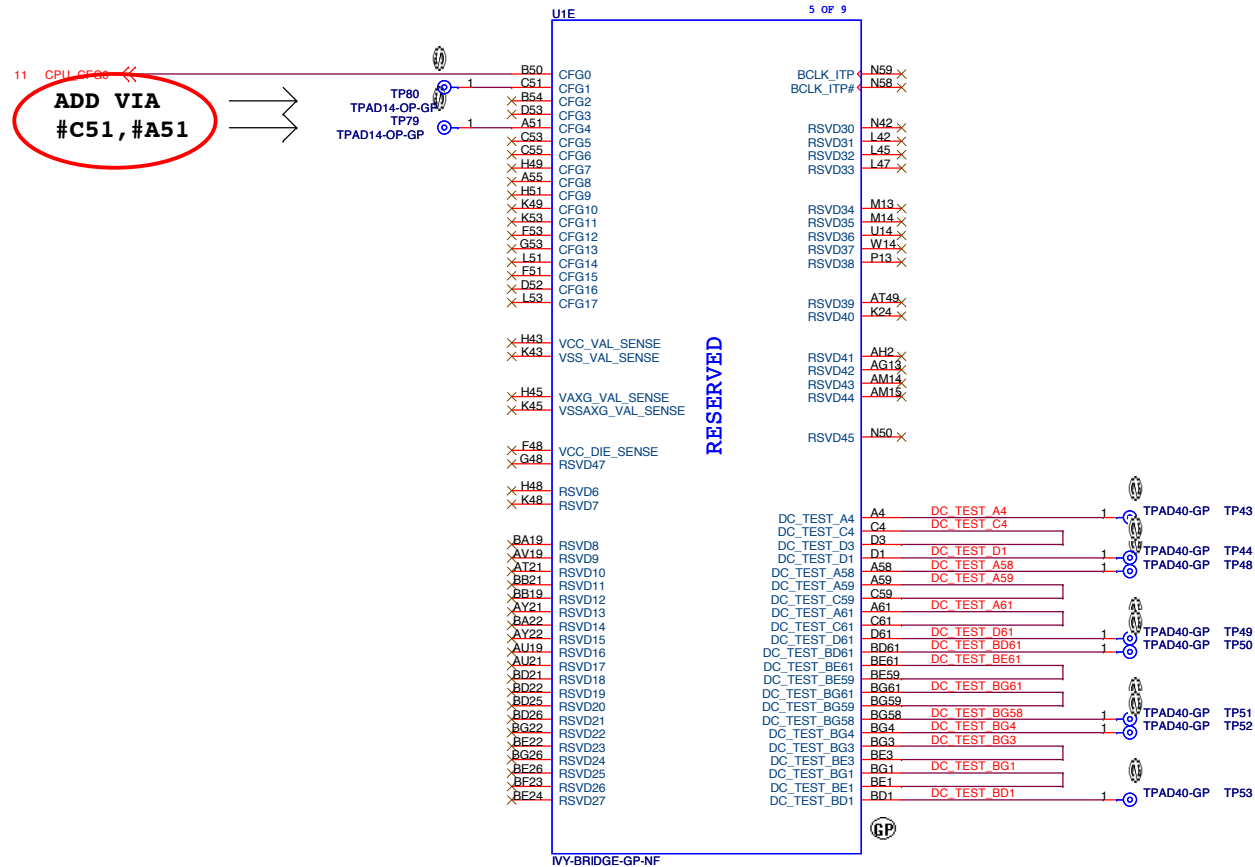




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緯創資通 Wistron Corporation		
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
Title CPU(7/8):GND		
Size Custom	Document Number Dasher-2	Rev -1
Date: Tuesday, February 21, 2012	Sheet 9 of	101

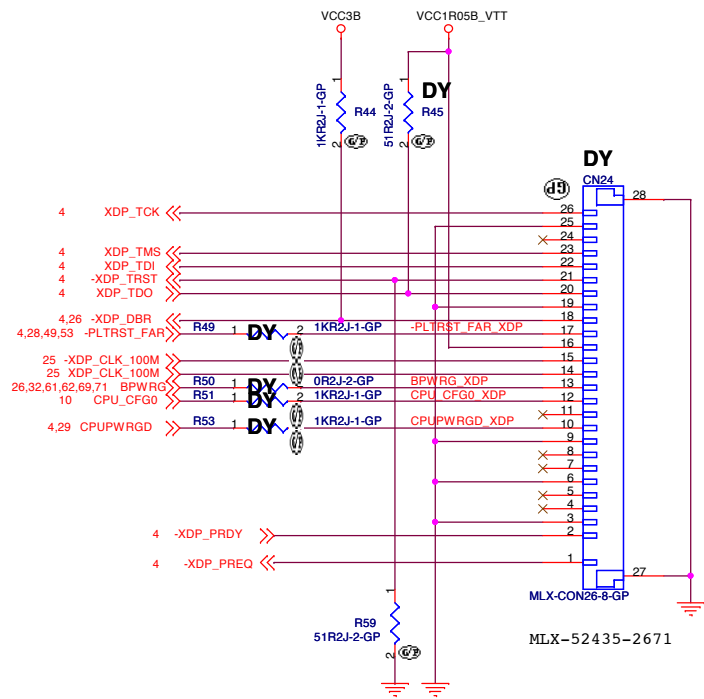
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Title			CPU(8/8):CFG/Reserved
Size	Document Number	Rev	
A3		-1	
Date:	Friday, March 09, 2012	Sheet	10 of 101

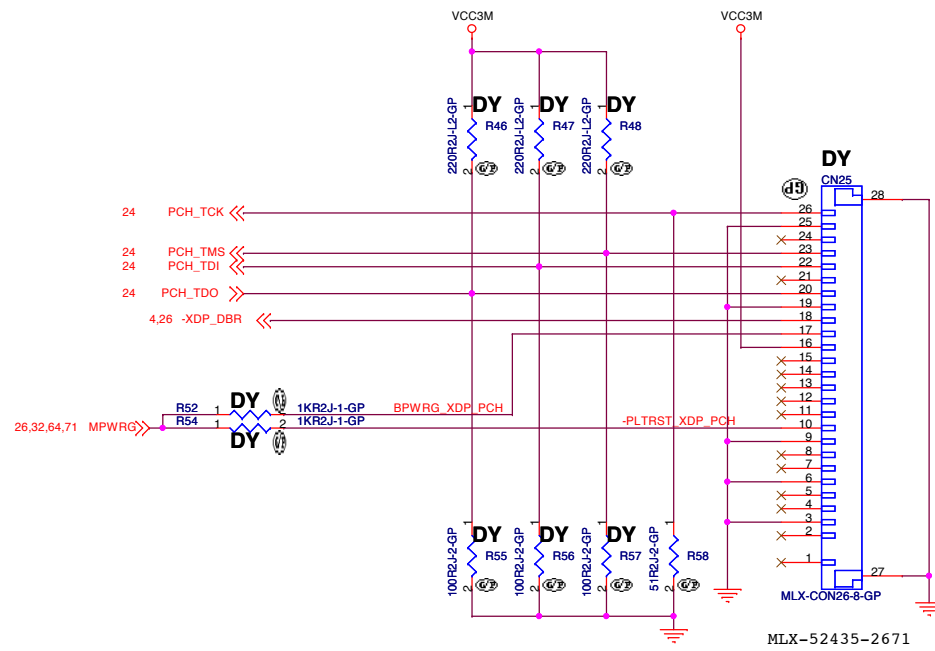


DEBUG Interface for Processor.

**XDP1** NOTE:"ASM" FOR PDV/SDV ONLY

		ENABLE	DISABLE
TDO	R45	ASM	DY
TRST#	R59	ASM	ASM
DBRST#	R44	ASM	ASM
RESET#	R49	ASM	DY
CFG0	R51	ASM	DY
PWRGD	R53	ASM	DY
BPWRG	R50	ASM	DY
	CN24	ASM	DY

FVT Logic



DEBUG Interface for PCH.

**XDP2** NOTE:"ASM" FOR PDV/SDV ONLY

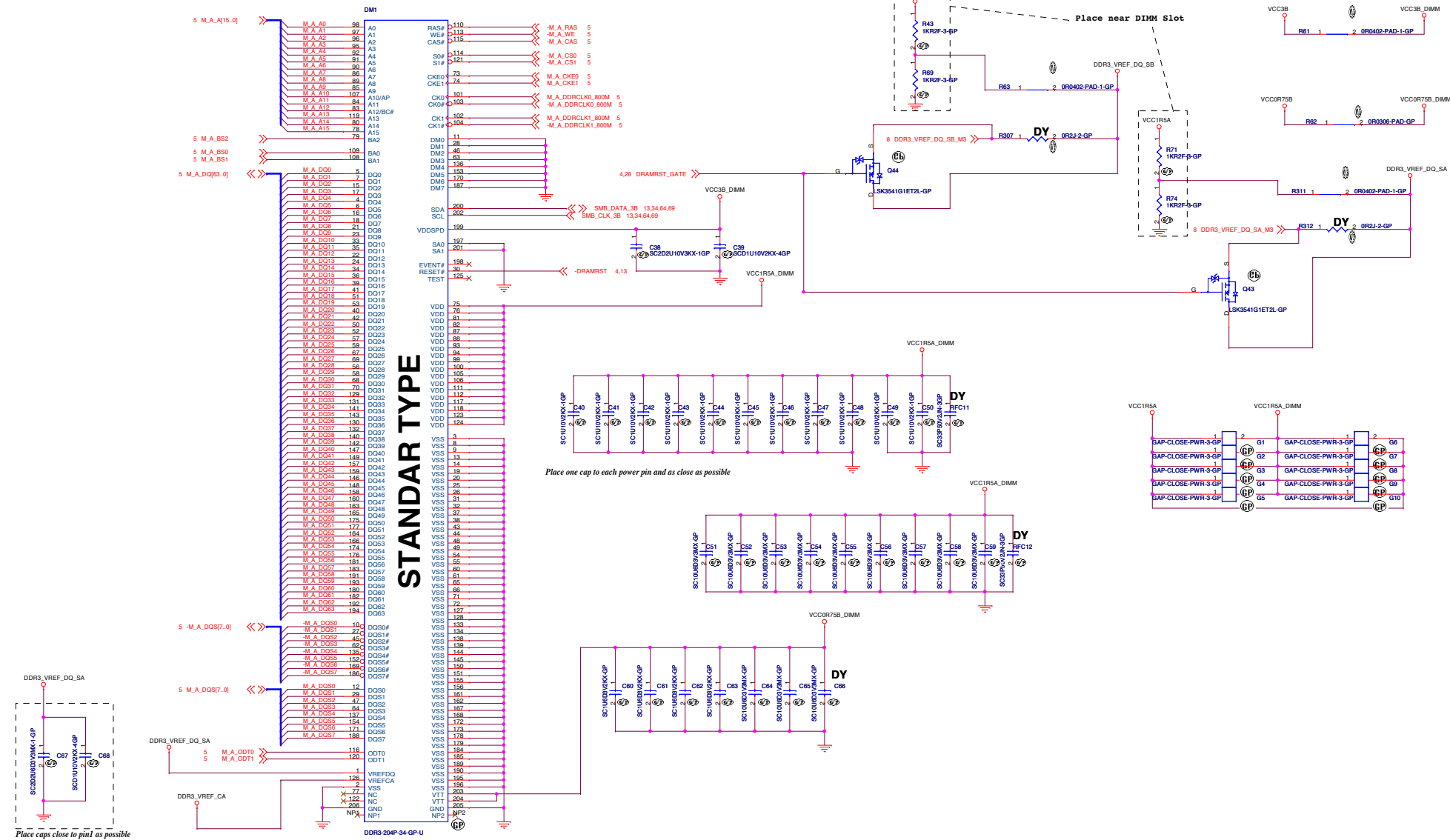
		ENABLE	DISABLE
TDO	R46	220	DY
	R55	100	DY
TMS	R48	220	DY
	R57	100	DY
TDI	R47	220	DY
	R56	100	DY
TCK	R58	51	51
MPWRG	R52	ASM	DY
	R54	ASM	DY
	CN25	ASM	DY

FVT Logic

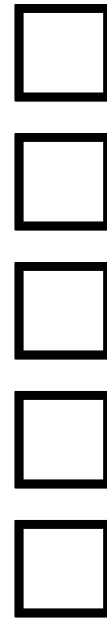
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緯創資通 Wistron Corporation  
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Title	<b>XDP Connector</b>		
Size	Document Number	<b>Dasher-2</b>	Rev
A3			-1
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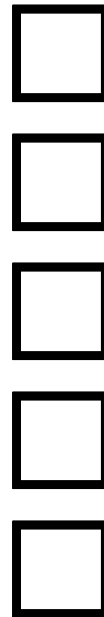




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Size A3	Document Number <b>Dasher-2</b>		Rev <b>-1</b>
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Size	Document Number		Rev
A3	<b>Dasher-2</b>		<b>-1</b>
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Title		Wistron Corporation	
Size		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
A3		-1	
Date: Tuesday, February 21, 2012		Sheet 16 of 101	



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A3	<b>Dasher-2</b>		<b>-1</b>
Date:	Tuesday, February 21, 2012	Sheet	17 of 101

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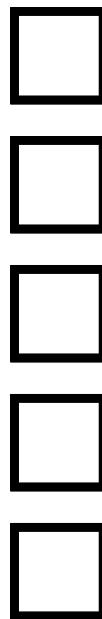
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Size	Document Number		Rev
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Title		<b>緯創資通</b> <b>Wistron Corporation</b> 21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Size		Document Number	
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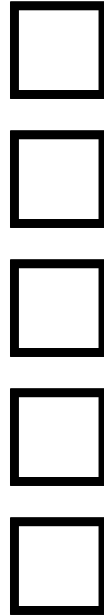
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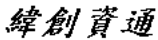
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Size A3	Document Number <b>Dasher-2</b>		Rev <b>-1</b>
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Size		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
A3		-1	
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Size A3	Document Number <b>Dasher-2</b>		Rev <b>-1</b>
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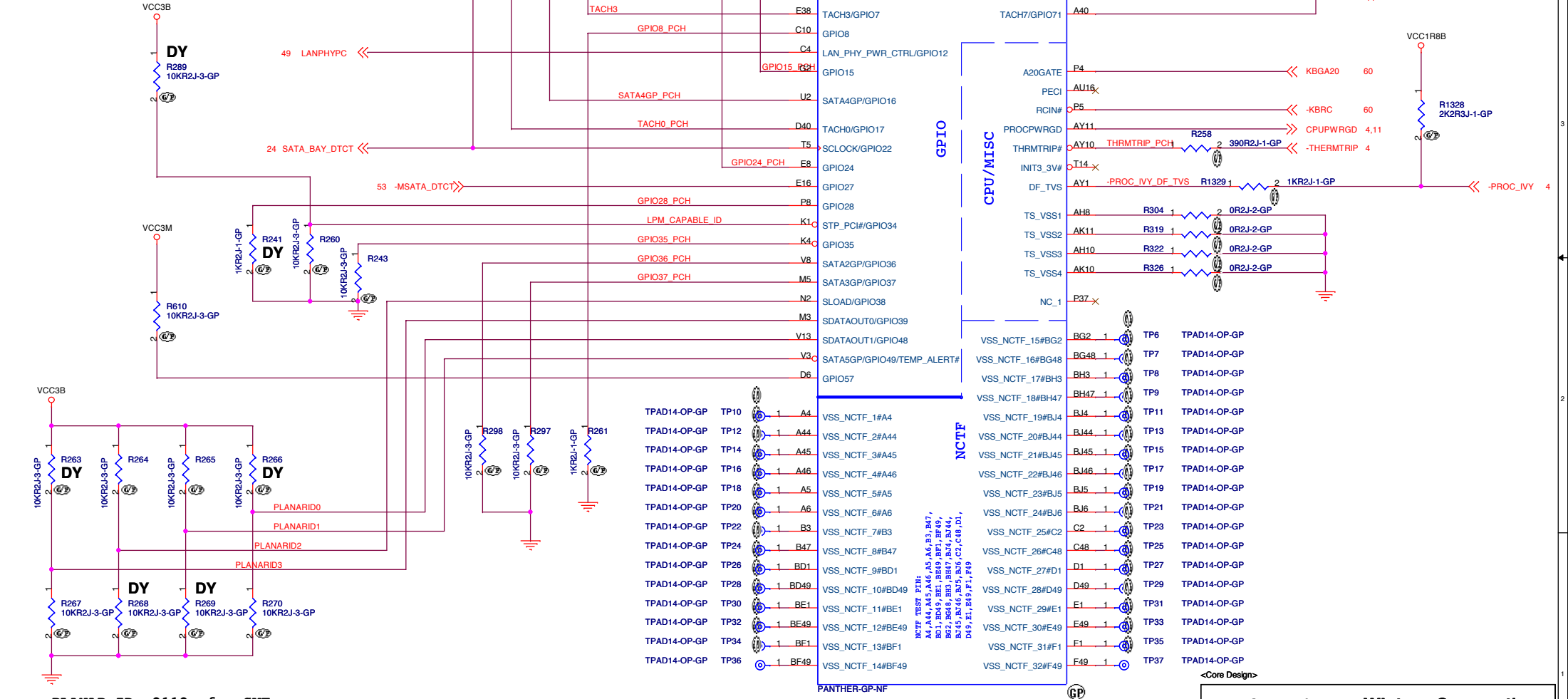




GPIO8	INTEGRATED CLOCKING
HIGH	DISABLE(BTM)
LOW	ENABLE(FCIM)

GPIO15	ME CRYPTO STRAP
HIGH	WITH CONFIDENTIALITY
LOW	NO CONFIDENTIALITY

GPIO34	LPM CAPABILITY
HIGH	LPM CAPABLE PLANAR
LOW	LPM NON-SUPPORT PLANAR

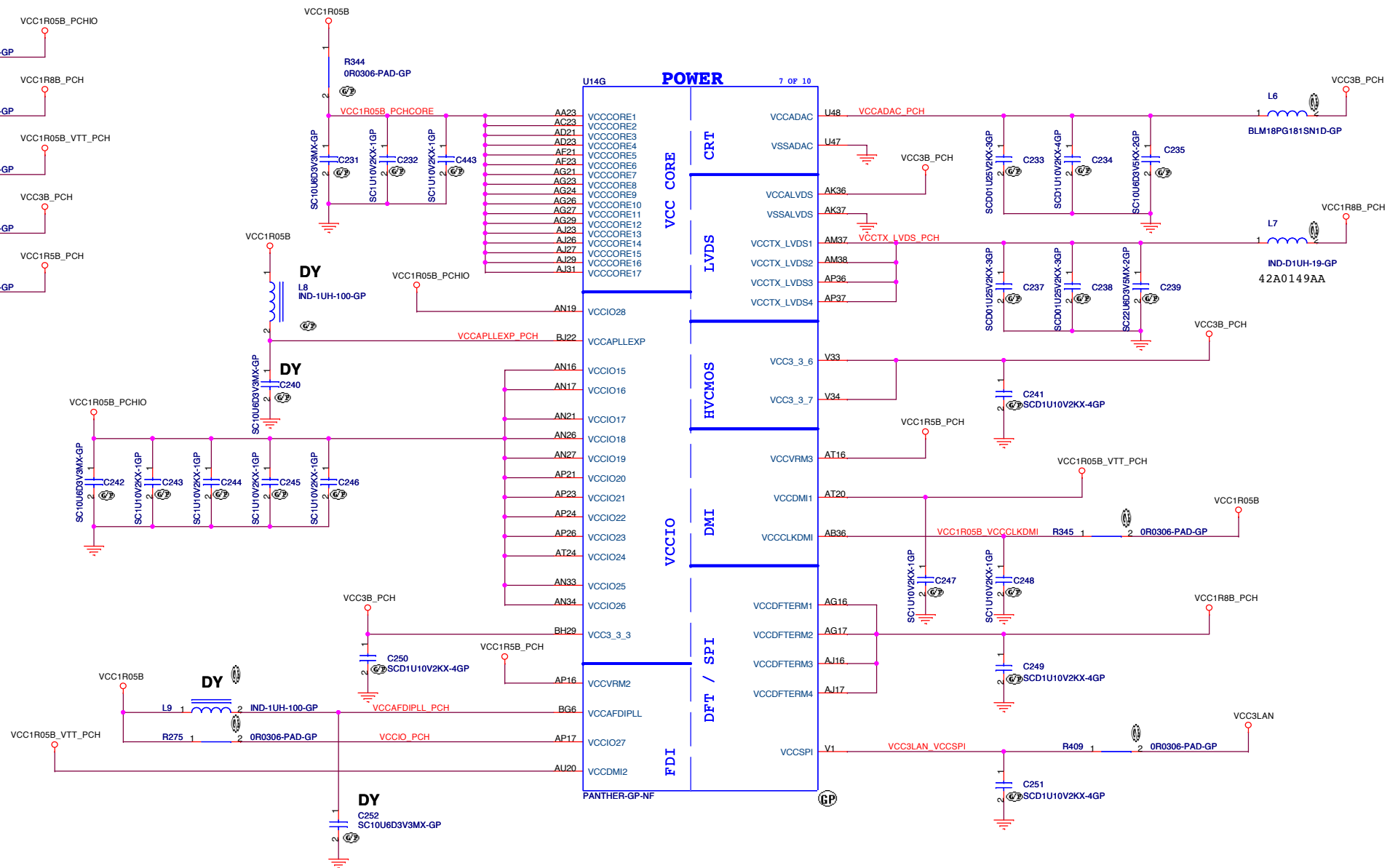


PLANAR ID: 0110 for SVT

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Wistron Corporation  
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Title **PCH(6/9):GPIO/NCTF/RSVD**

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**Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title **PCH(7/9):Power**

Size A3 Document Number

**Dasher-2**

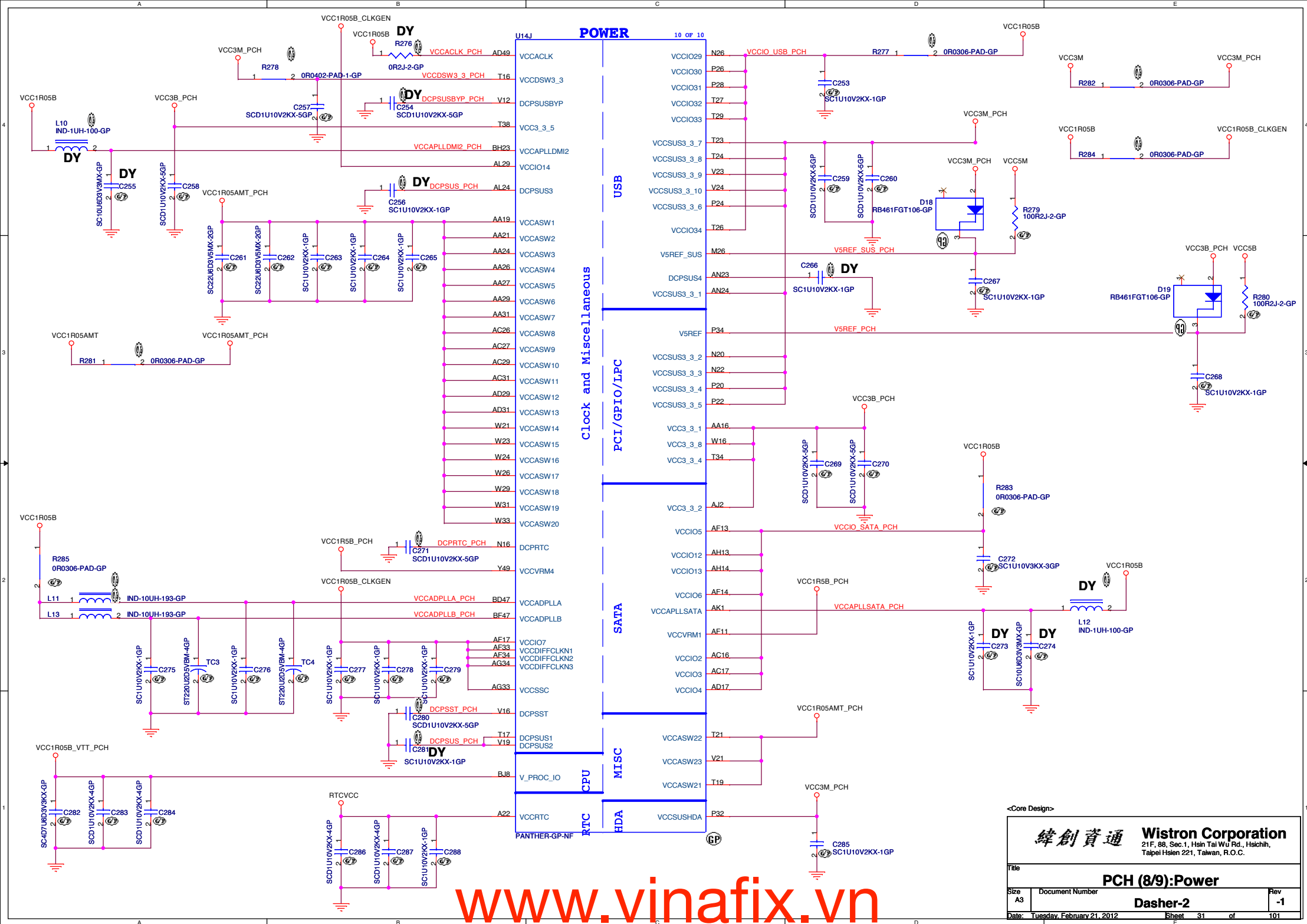
Rev

**-1**

Date: Tuesday, February 21, 2012

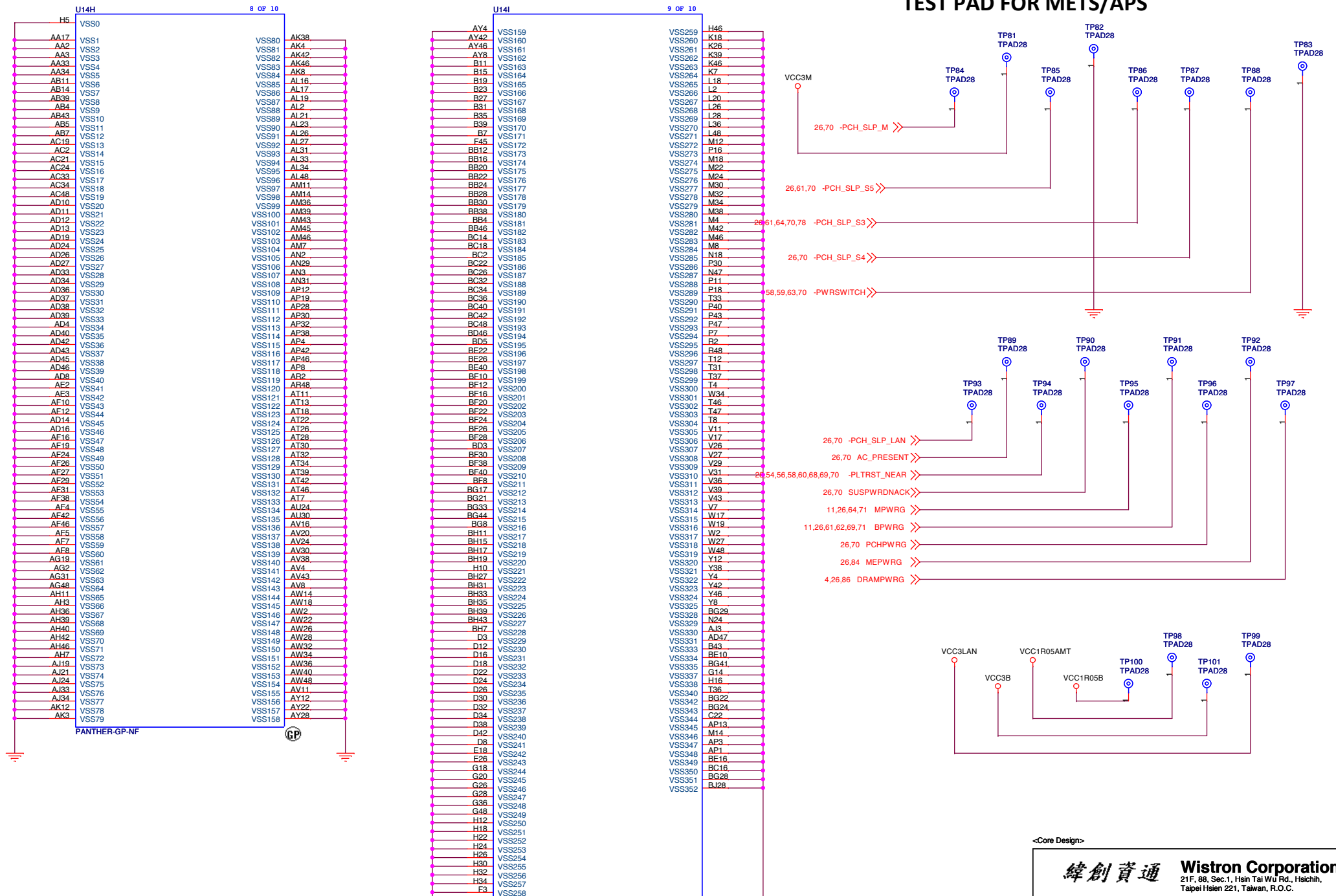
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## TEST PAD FOR METS/APS



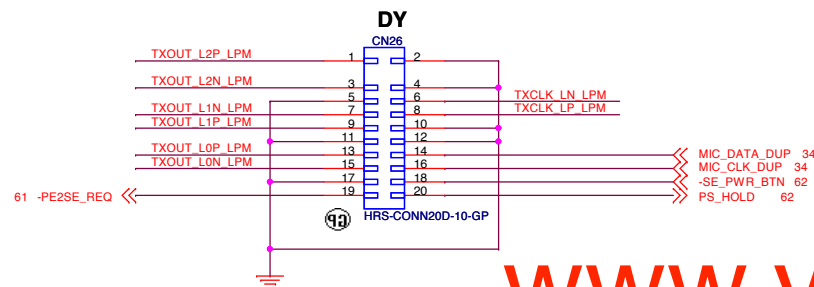
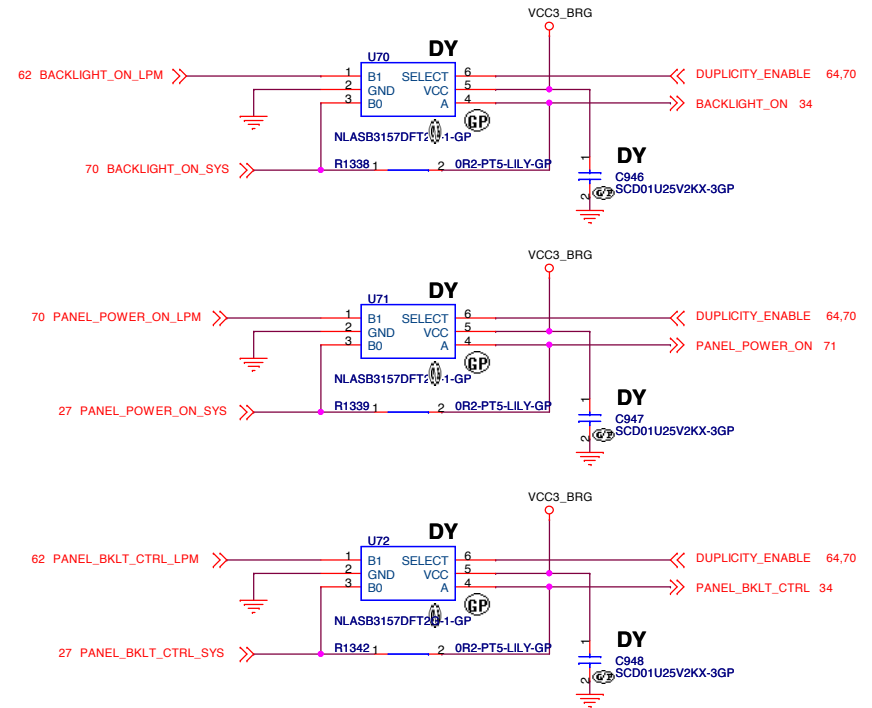
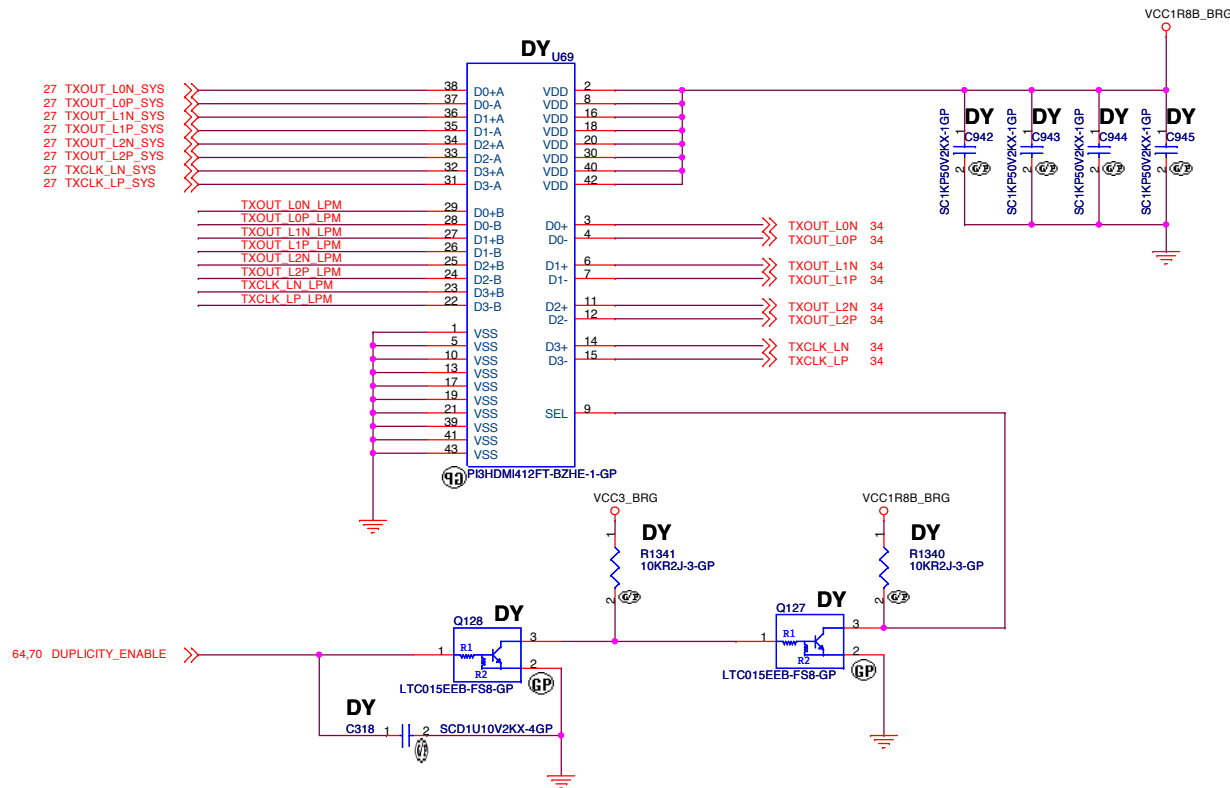
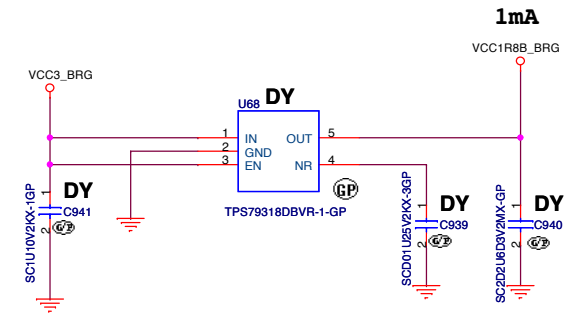
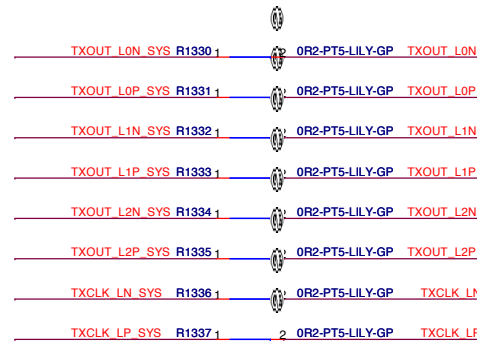
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Taipei Hsien 221, Taiwan, R.O.C.

Title			
<b>PCH (9/9):GND</b>			
Size A3	Document Number		Rev
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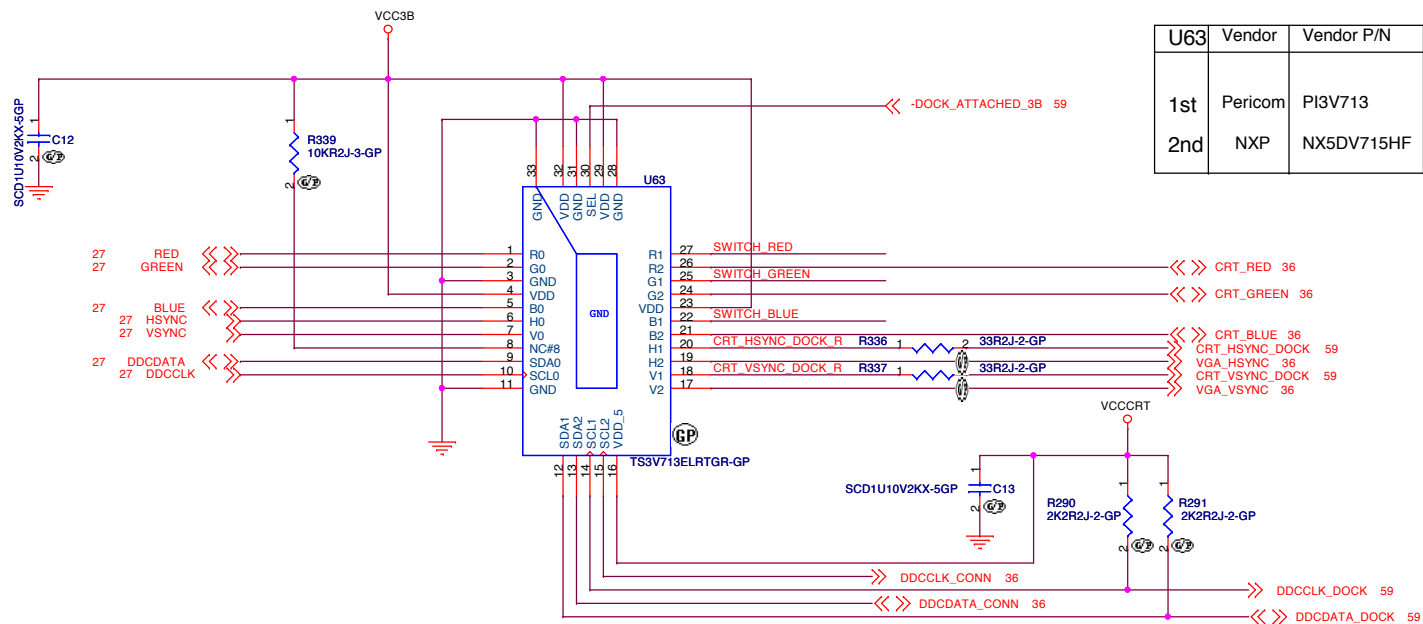


supplier	Vendor P/N	Wistron P/N
Pericom	PIHDMI412-BZHE	71.03412.B0G
TI	TS3DV421RUAR	71.03421.003

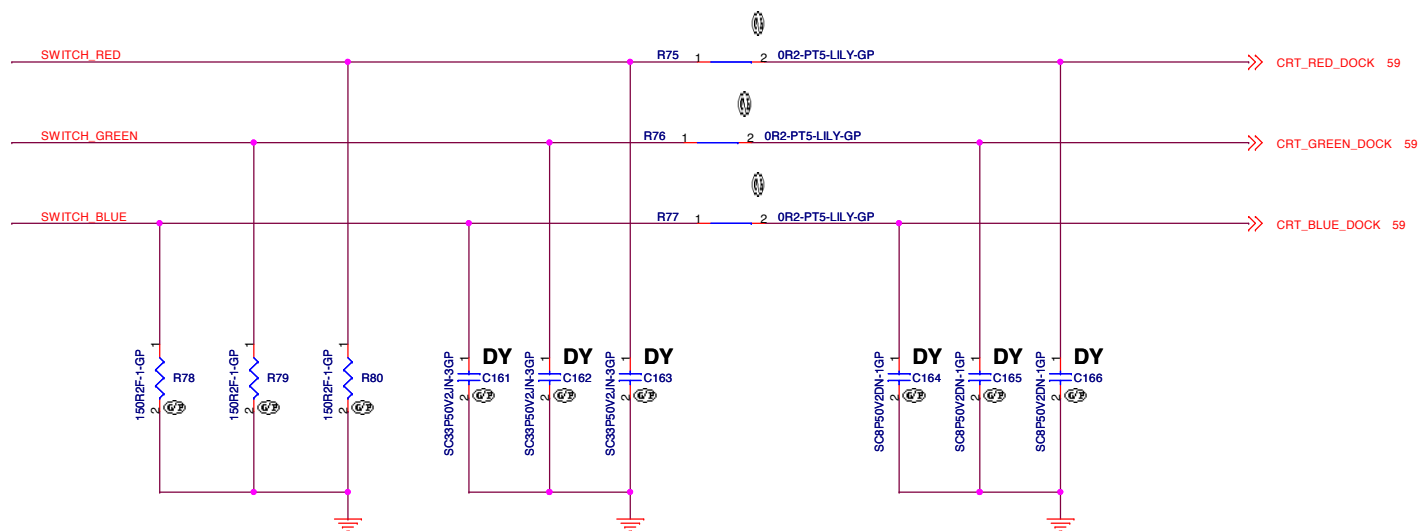


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U63	Vendor	Vendor P/N	Wistron P/N
1st	Pericom	PI3V713	73.3V713.A0I
2nd	NXP	NX5DV715HF	73.05715.003



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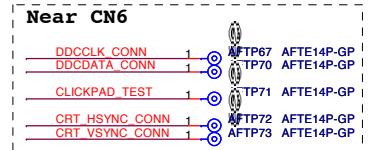
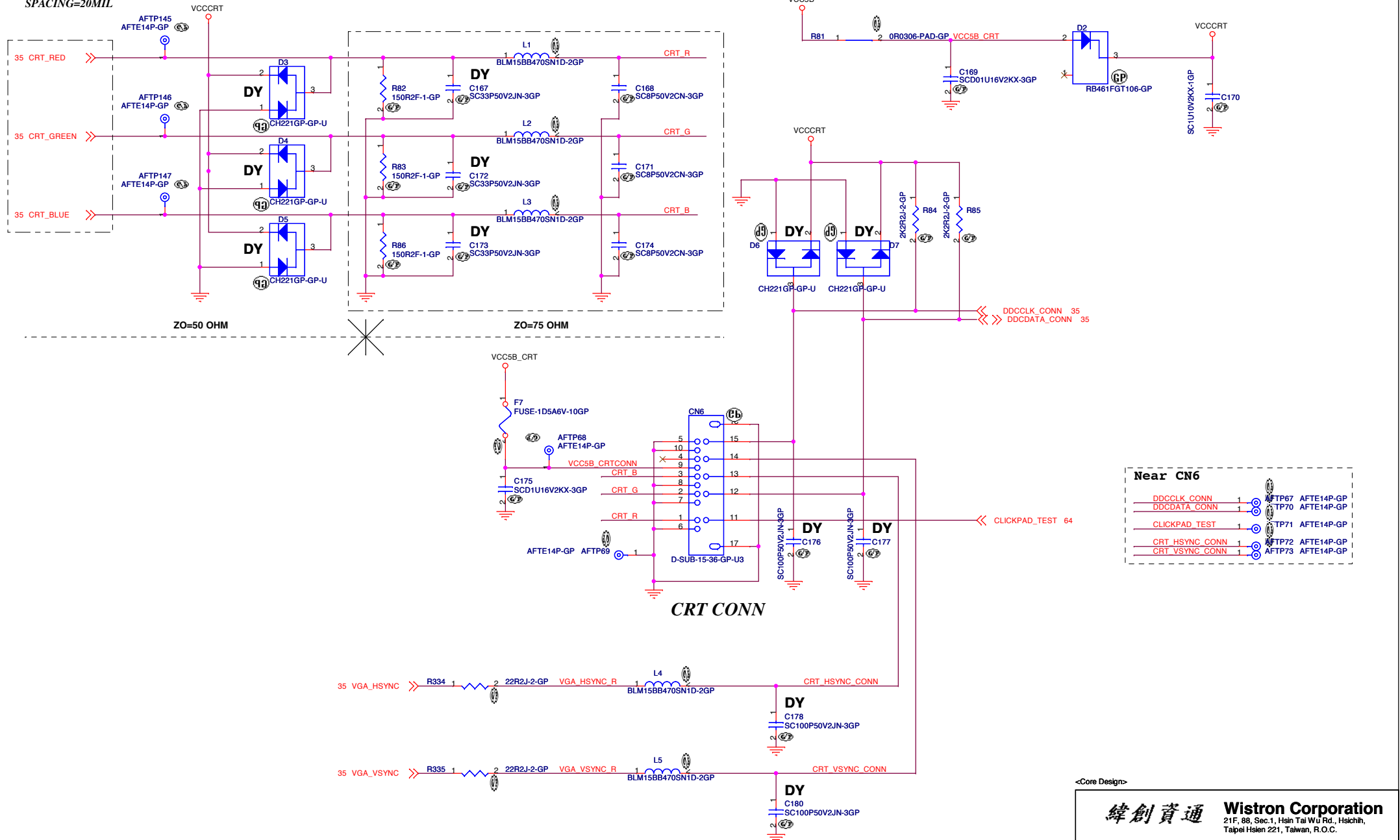
CRT SELECTOR			
Size A3	Document Number Dasher-2	Rev -1	
Date: Friday, March 09, 2012	Sheet 35	of 101	

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# GND GUARDING

EACH SIGNAL WIDTH DEPENDS ON ZO(TRACE IMPEDANCE)

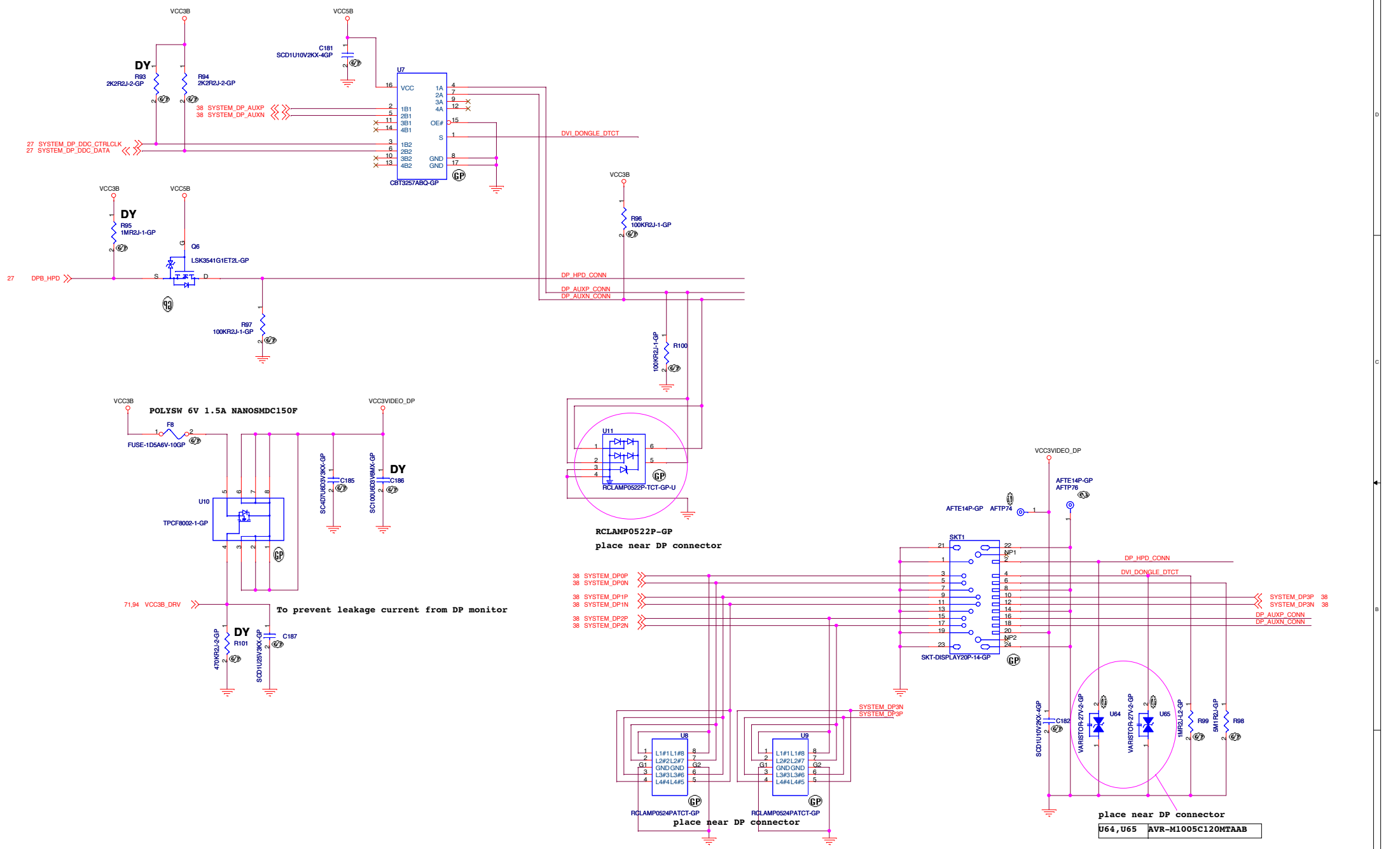
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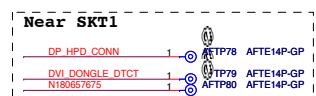
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Ext CRT Interface			
Size A3	Document Number	Dasher-2	Rev -1
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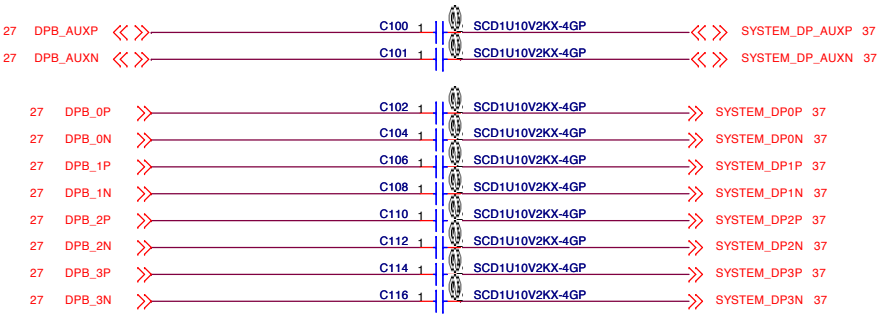
**緯創資通 Wistron Corporation**  
 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichin,  
 Taipei Hsin 221, Taiwan, R.O.C.

File: **Display Port Connector**

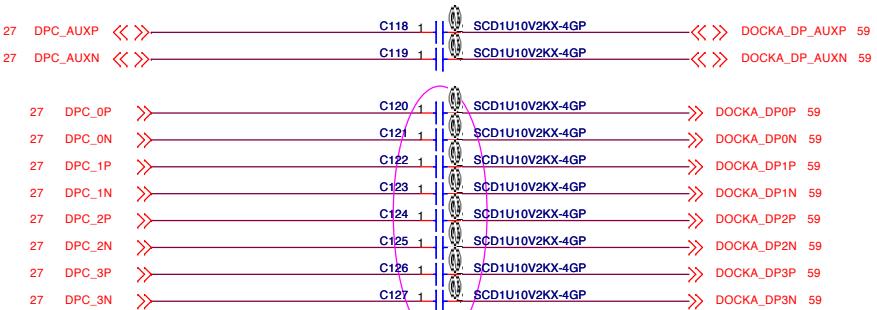
Size: Custom Document Number: **Dasher-2** Rev: -1

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System DP Connector

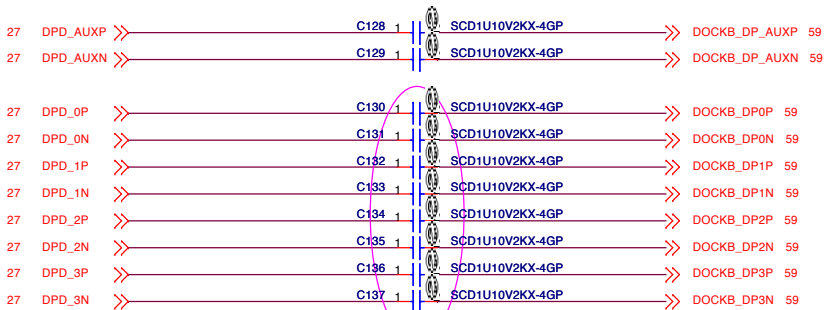


Docking DP Connector A



Place Near Docking Connector

Docking DP Connector B



Place Near Docking Connector

<Core Design>

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Title

Display Port AC Coupling

Size

Document Number

Rev

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Date: Friday, March 09, 2012

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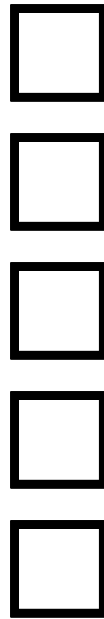
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Taipei Hsien 221, Taiwan, R.O.C.

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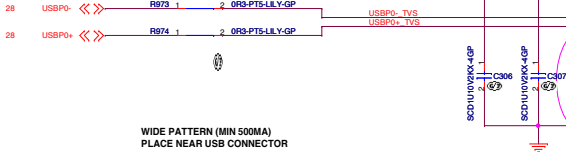
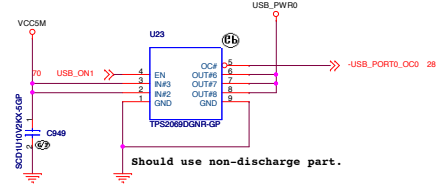




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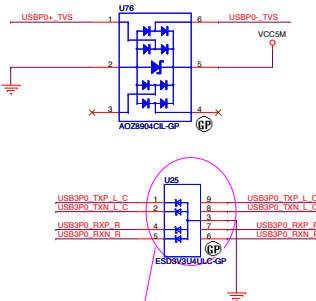
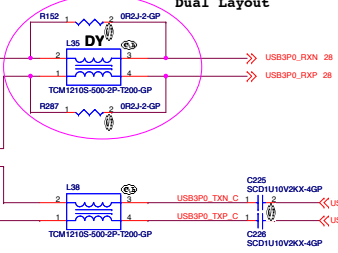
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Size		21F, 88, Sec. 1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
A3		-1	
Date: Tuesday, February 21, 2012		Sheet 41 of 101	

USB3.0 port0			
U23,U18			
USB3.0 Enable	TI	TPS2069DGN	74.02069.079
	GMT	G548A1F51U	74.00548.A79

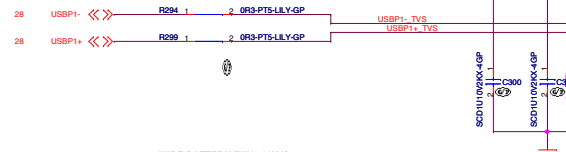
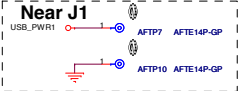
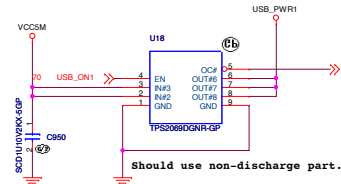


L35,L38,L41,L42		
1st:TDK	TCM1210S-500-2P	68.01210.20E
2nd:Murata	DLP118A350HL2L	68.11350.201

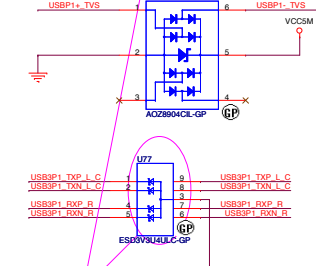
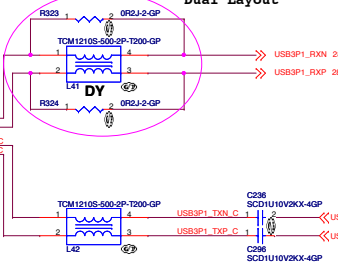
USB3.0 CONNECTOR



USB3.0 port1

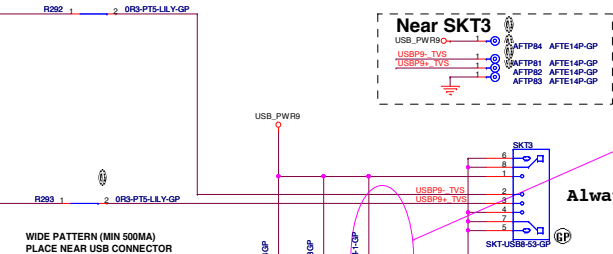
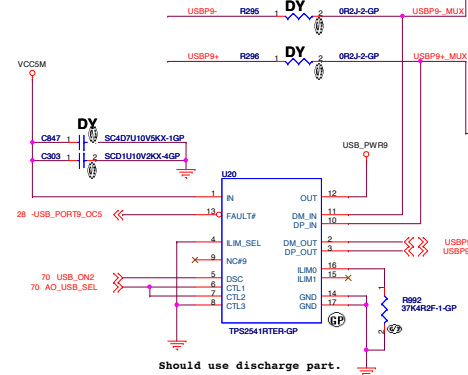


USB3.0 CONNECTOR



U25, U77			
1st:Infineon	ESD3V3U4ULC	83.3V3U4.0A0	
2nd:NXP	IP4294CZ10-TBR	83.04294.0A0	
3rd:AMC	AZ1045-04F DFN2510P 10E	83.01045.AA0	

USB port9(Always On USB)

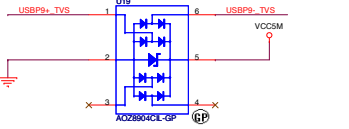


C299, C303,TCS		
1st	NEC Tokin	TEPSLB20J157M 77.C1571.09L
2nd	SANYO	6TPE150HA2B 77.21571.111

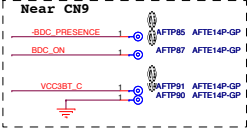
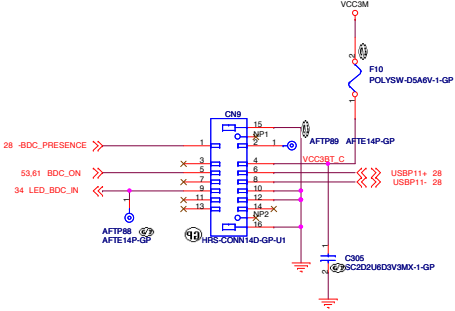
Always on port

Always On USB Part List (U20)		
	~SDV	FVT~
TI	TPS2541RTER (74.02541.073)	TPS2541RTER(74.02541.073)
		TPS2541A(74.02541. A73)

U76, U22, U19			
1st:AOS	AOZ8904CIL	83.08904.0AE	
2nd:Little Fuse	SP3002-04HTG	84.03200.0AF	



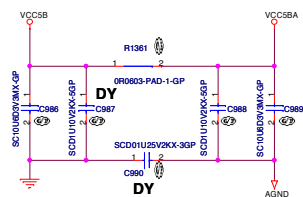
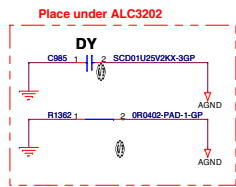
Bluetooth Module



PSIT Config.

	PSIT-1	PSIT-2	PSIT-3	PSIT-4	PSIT-5	PSIT-6
L35,L41	No ASM	No ASM	No ASM	No ASM	No ASM	No ASM
L38,L42	TDK	No ASM	No ASM	TDK	No ASM	Murata
U25,U77	Infineon	AMC	AMC	Infineon	AMC	AMC
R152,287,323,324	ASM	ASM	ASM	ASM	ASM	ASM
R320,321,325,328	No ASM	ASM	ASM	No ASM	ASM	No ASM

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21F, 8B, Sec.1, Hsin Tai Wu Rd., Neichin, Taipei Hsien 221, Taiwan, R.O.C.			
USB Connector			
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<Core Design>

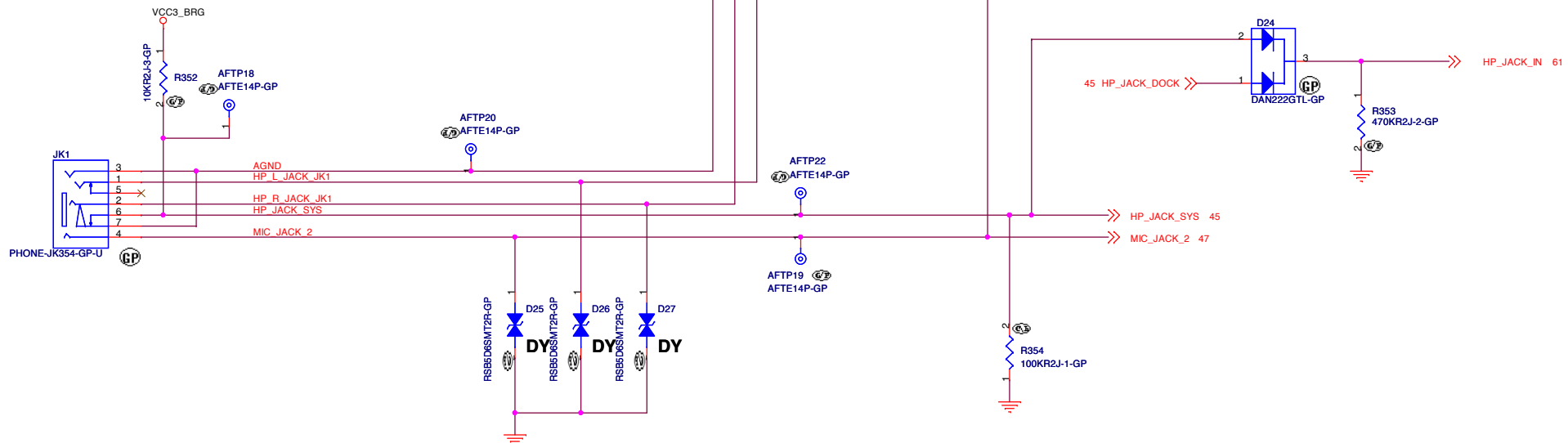
緯創資通 Wistron Corporation  
21F, 8B, Sec. 1, Hsin Tai Wu Rd., Hsinchu,  
Taipei Hsin 221, Taiwan, R.O.C.

File		
AUDIO ALC3202		
Size	Document Number	Rev
A2	Dasher-2	+1
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# NEAR HEADPHONE CONN

WIDE PATTERN

WIDE AND SHORT PATTERN

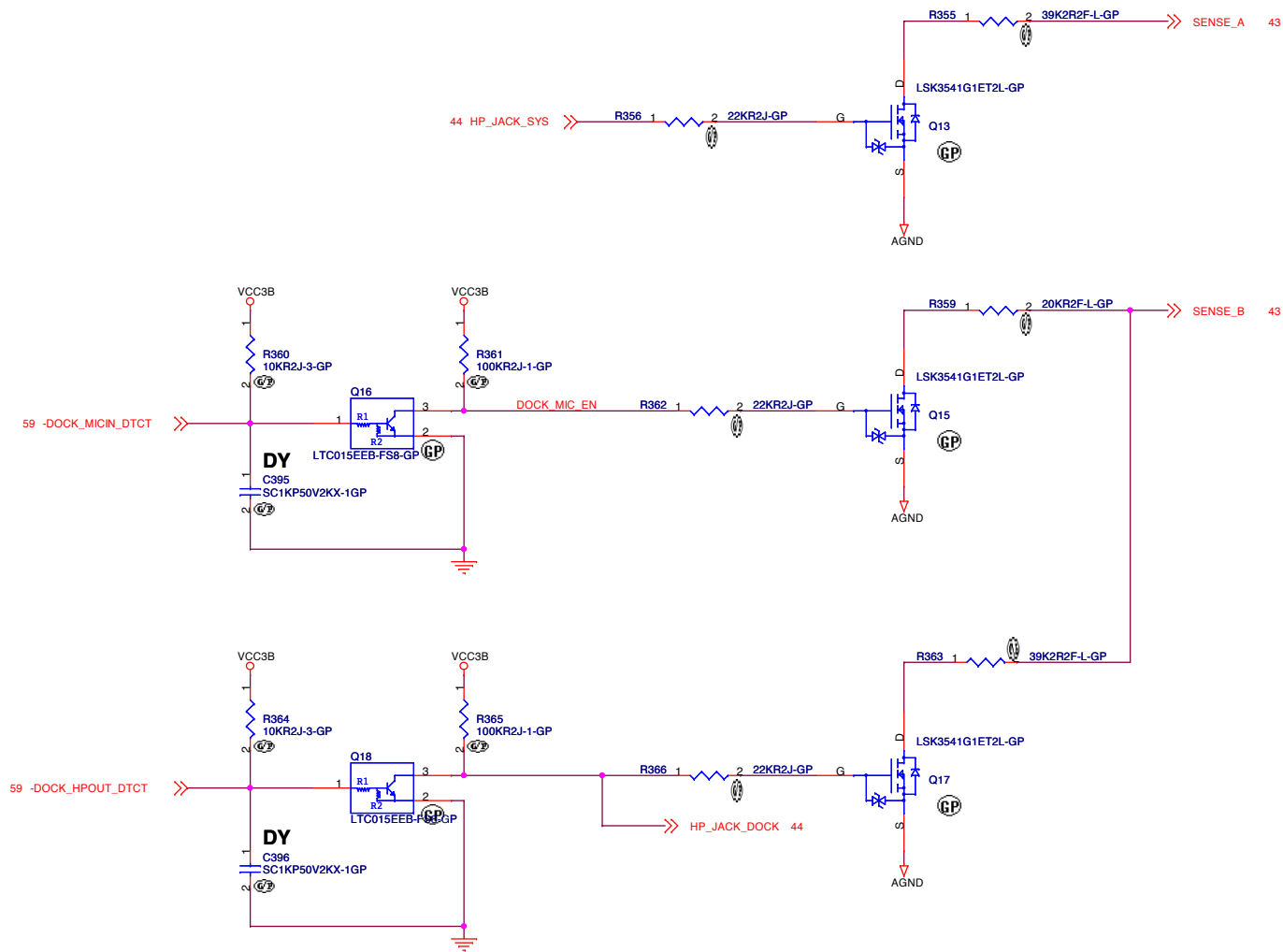


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緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

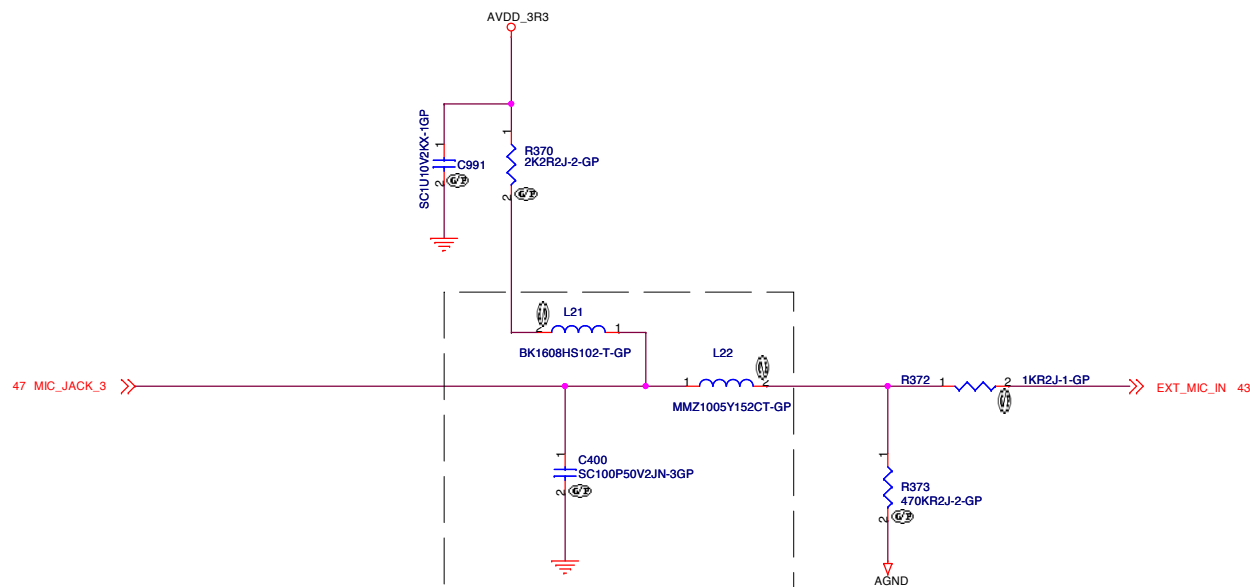
Title		
Audio Connector		
Size	Document Number	Rev
A3	Dasher-2	-1
Date:	Friday, March 09, 2012	Sheet 44 of 101

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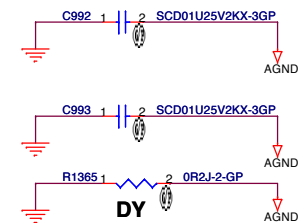
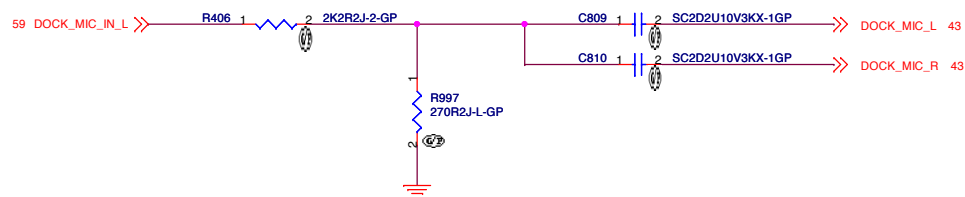


<Core Design>

<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title		<b>Audio Jack Sense</b>	
Size A3	Document Number	<b>Dasher-2</b>	Rev -1
Date: Friday, March 09, 2012		Sheet 45 of 101	



### NEAR EXT MIC CONN



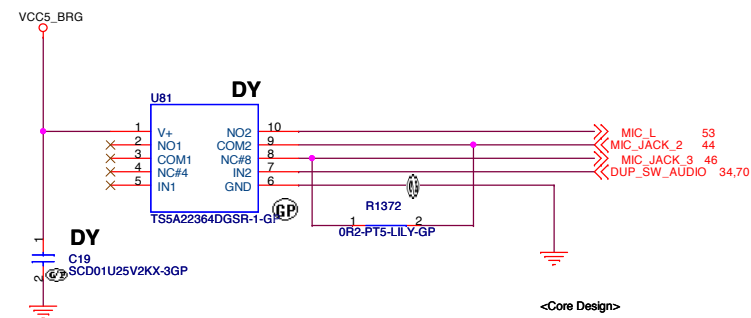
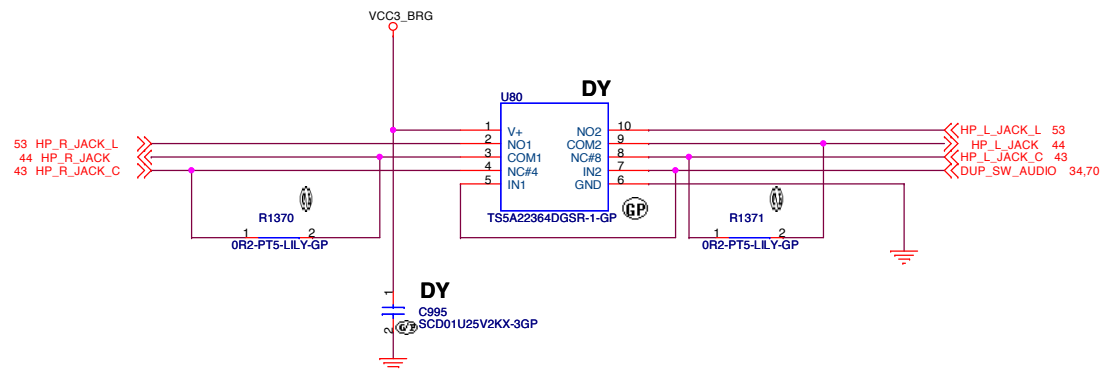
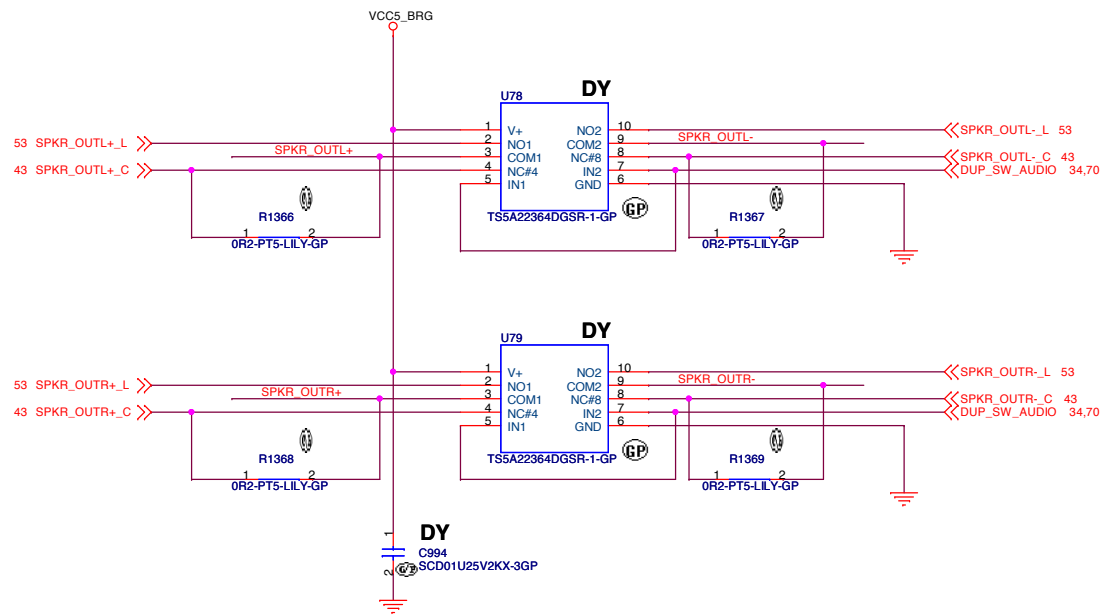
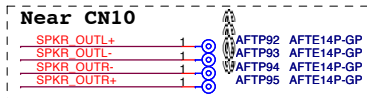
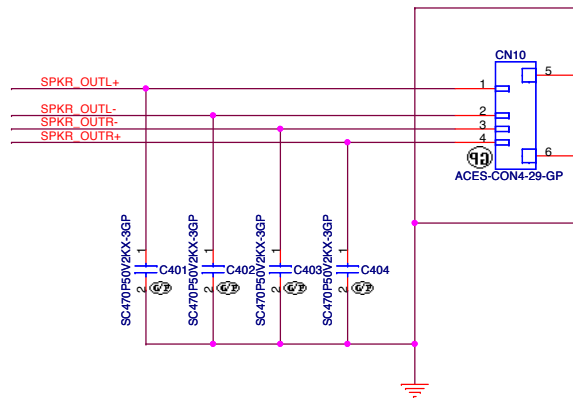
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Taipei Hsien 221, Taiwan, R.O.C.

Title **Audio Ext MIC I/F**

Size A3 Document Number **Dasher-2** Rev **-1**

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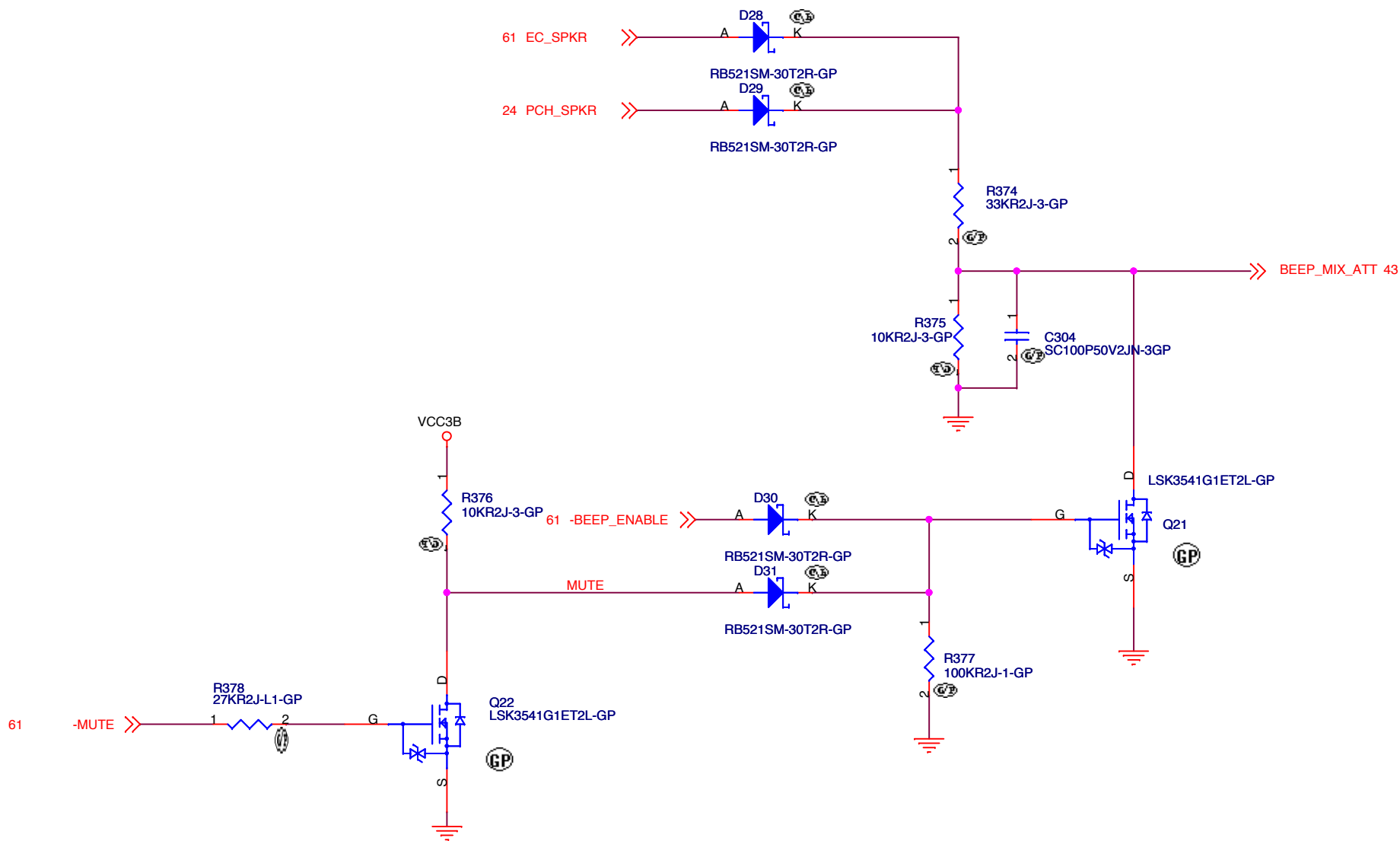
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**緯創資通 Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title **AUDIO SPEAKER**

Size A3	Document Number <b>Dasher-2</b>	Rev <b>-1</b>
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<Core Design>

緯創資通

**Wistron Corporation**

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Taipei Hsien 221, Taiwan, R.O.C.

Title

**Audio BEEP**

Size  
A4

Document Number

**Dasher-2**

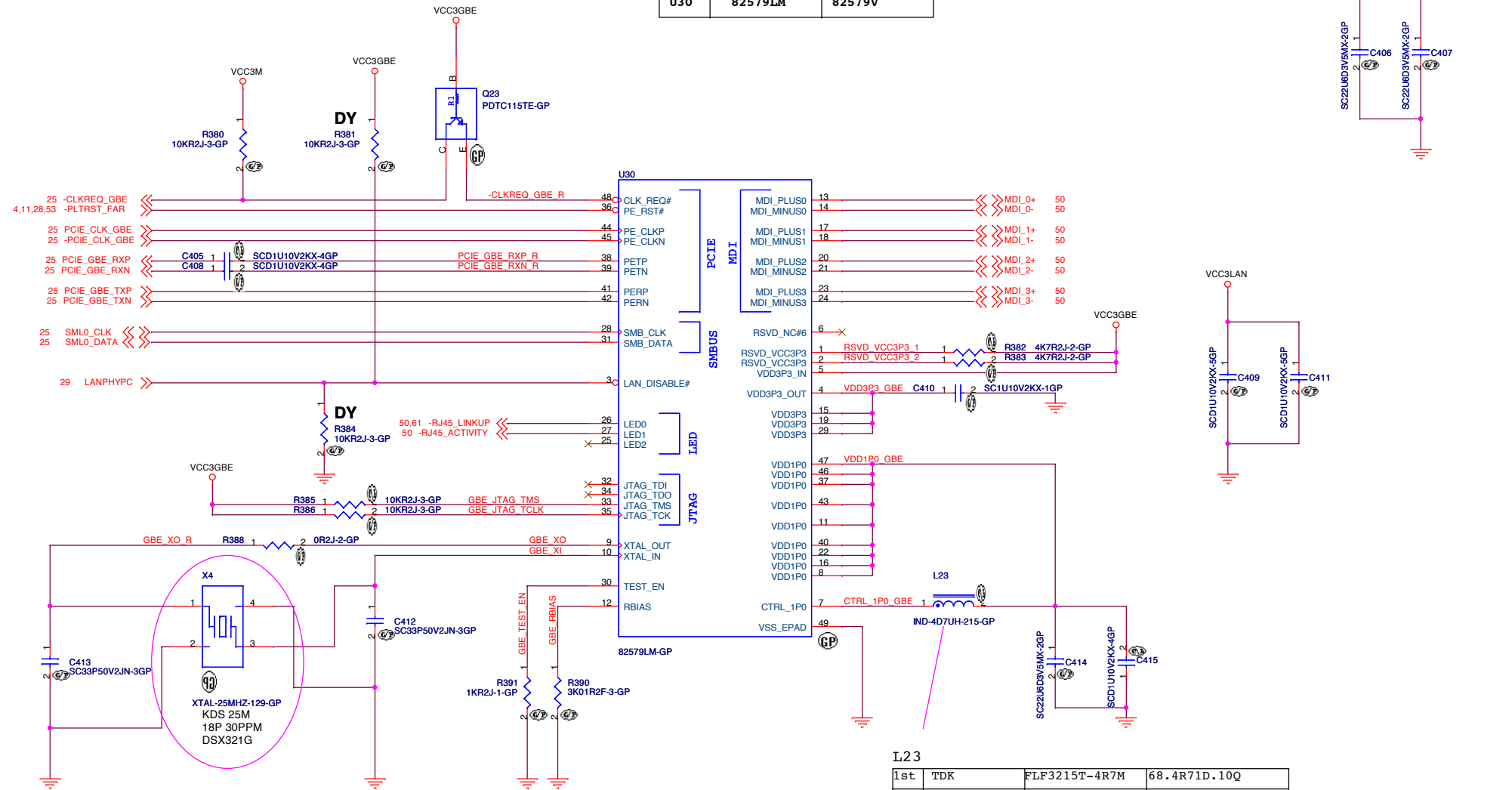
Rev  
-1

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AMT	YES	NO
U30	82579LM	82579V



X4

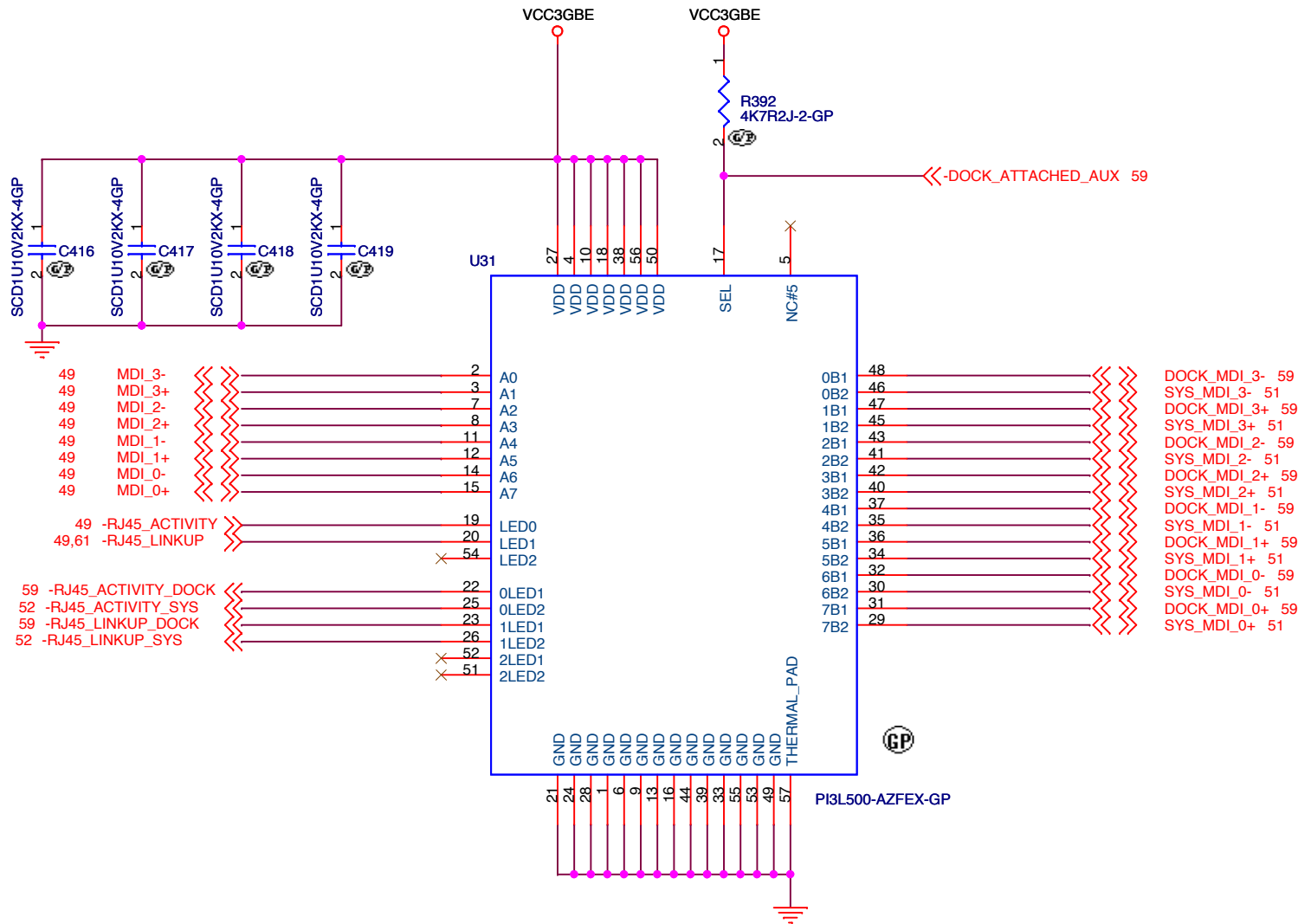
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2nd	H.ELE	HSX321S 25M 18P 30PPM	82.30020.B21
3rd	TXC	7V25020001 25M 18P 30PPM	41U6141AA

L23

1st	TDK	FLF3215T-4R7M	68.4R71D.10Q
2nd	Murata	LQH32PN4R7NN0	68.4R750.20H
3rd	Taiyo Yuden	BRL3225T4R7M	68.4R71E.10I

<Core Design>

緯創資通 Wistron Corporation			
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.			
Title			
GBE Lewisville			
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		Vendor P/N	Wistron P/N
1st	Pericom	PI3L500AZFEX	73.3L500.003
2nd	TI	TS3L500AERHUR	73.3L500.A0V
3rd	ST	STMUX1800LQTR	73.01800.003

<Core Design>

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

Title

**GBE LAN Switch**

Size  
A4

Document Number

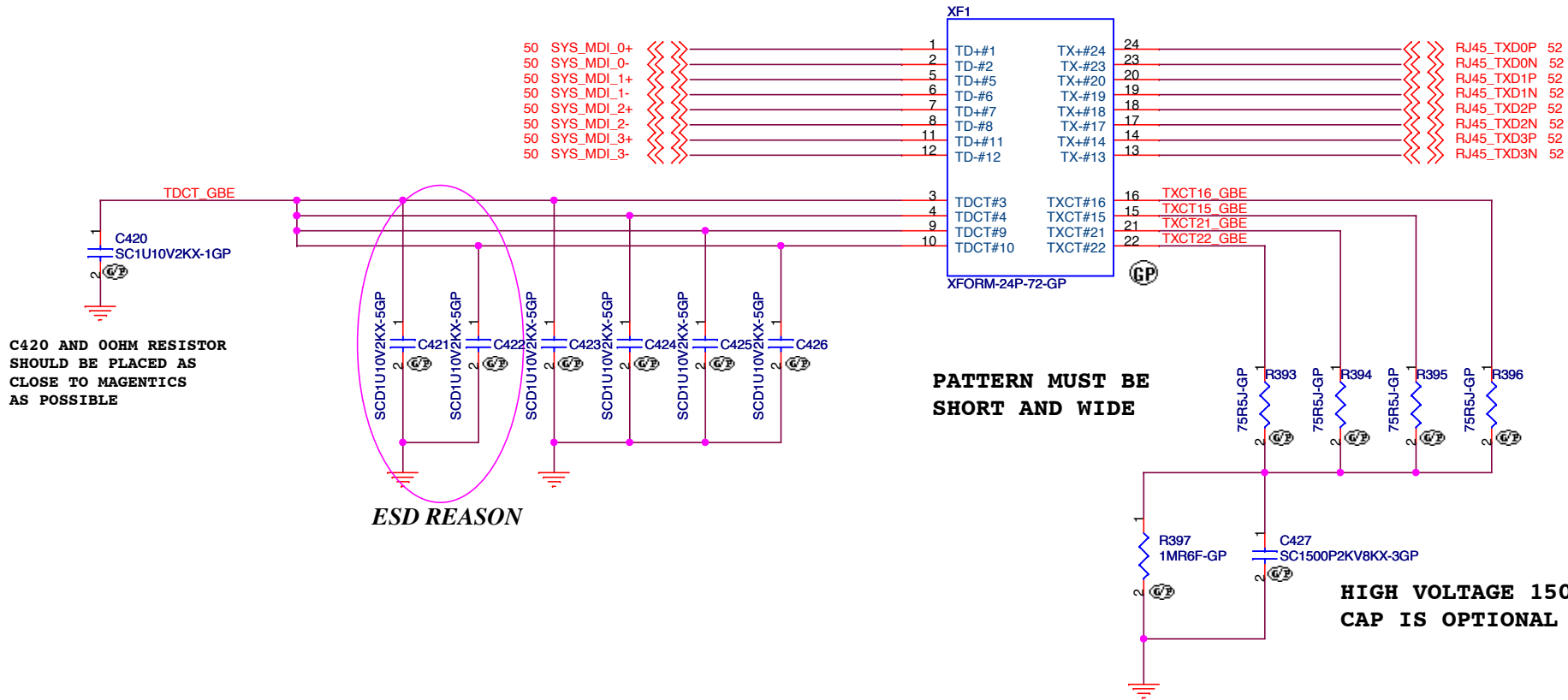
**Dasher-2**

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-1

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<Core Design>

緯創資通

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21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**GBE MAGNETICS**

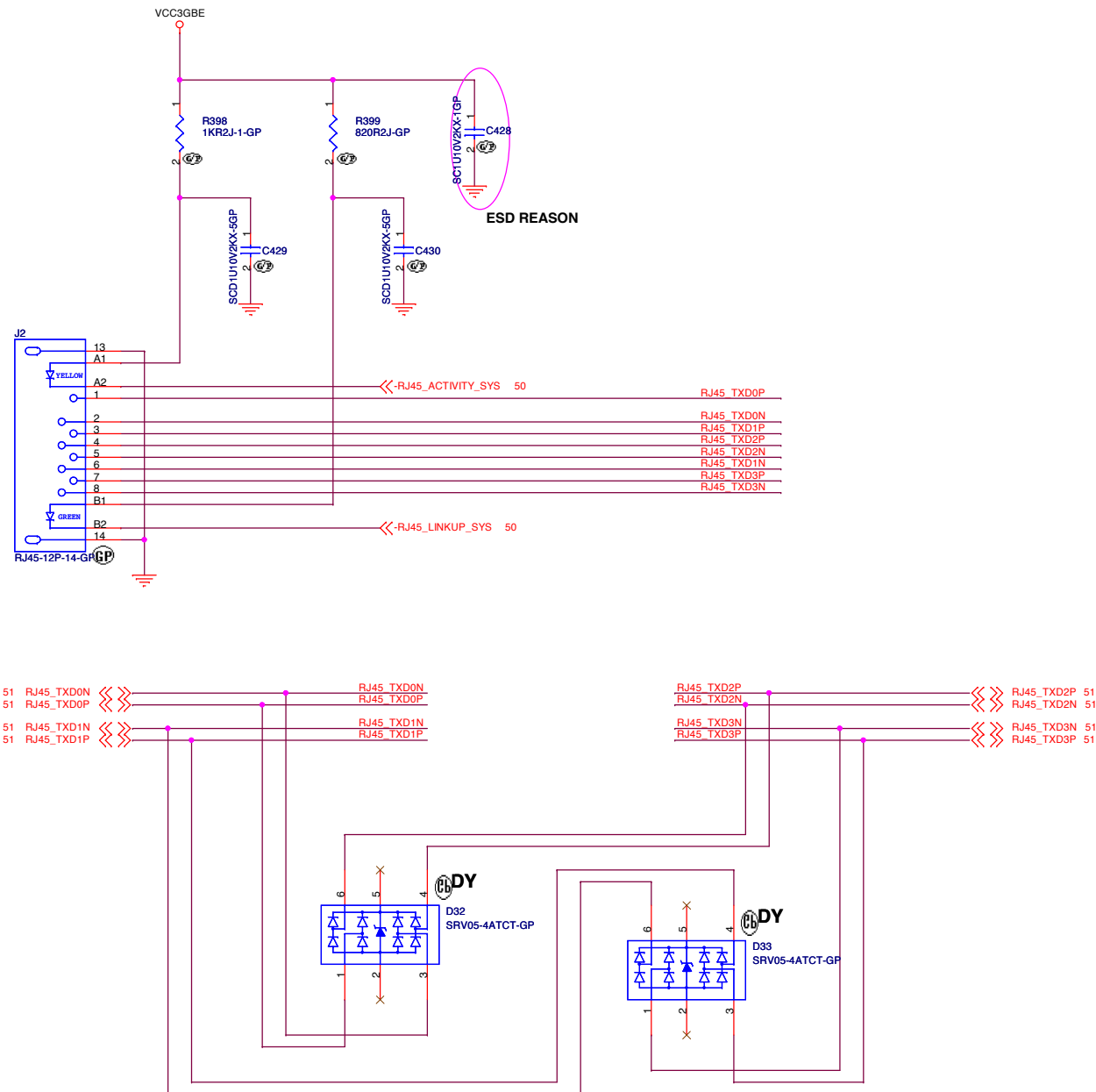
Size Custom Document Number

**Dasher-2**

Rev -1

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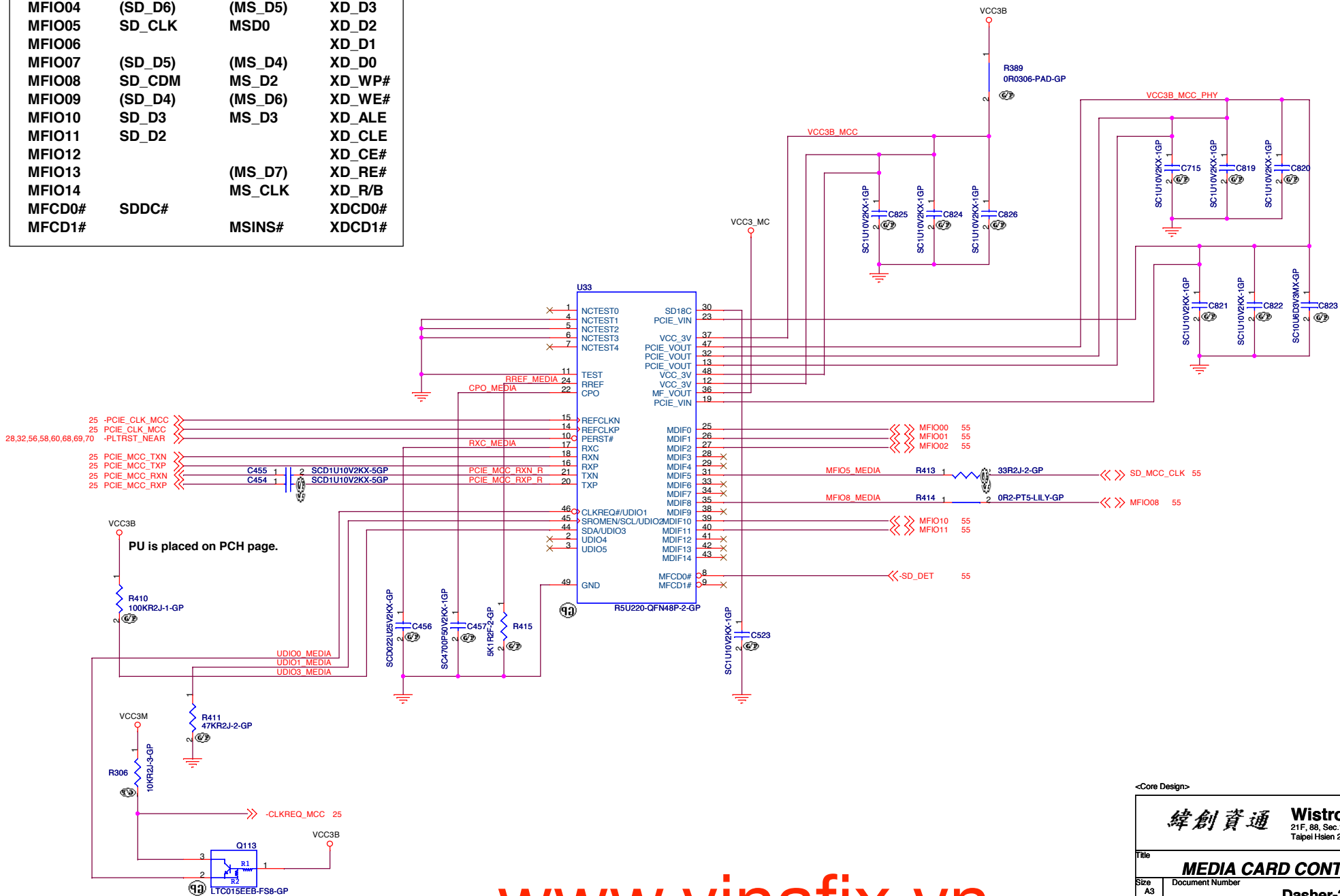
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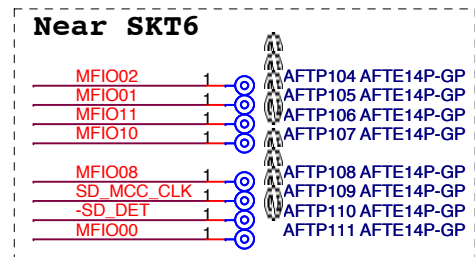
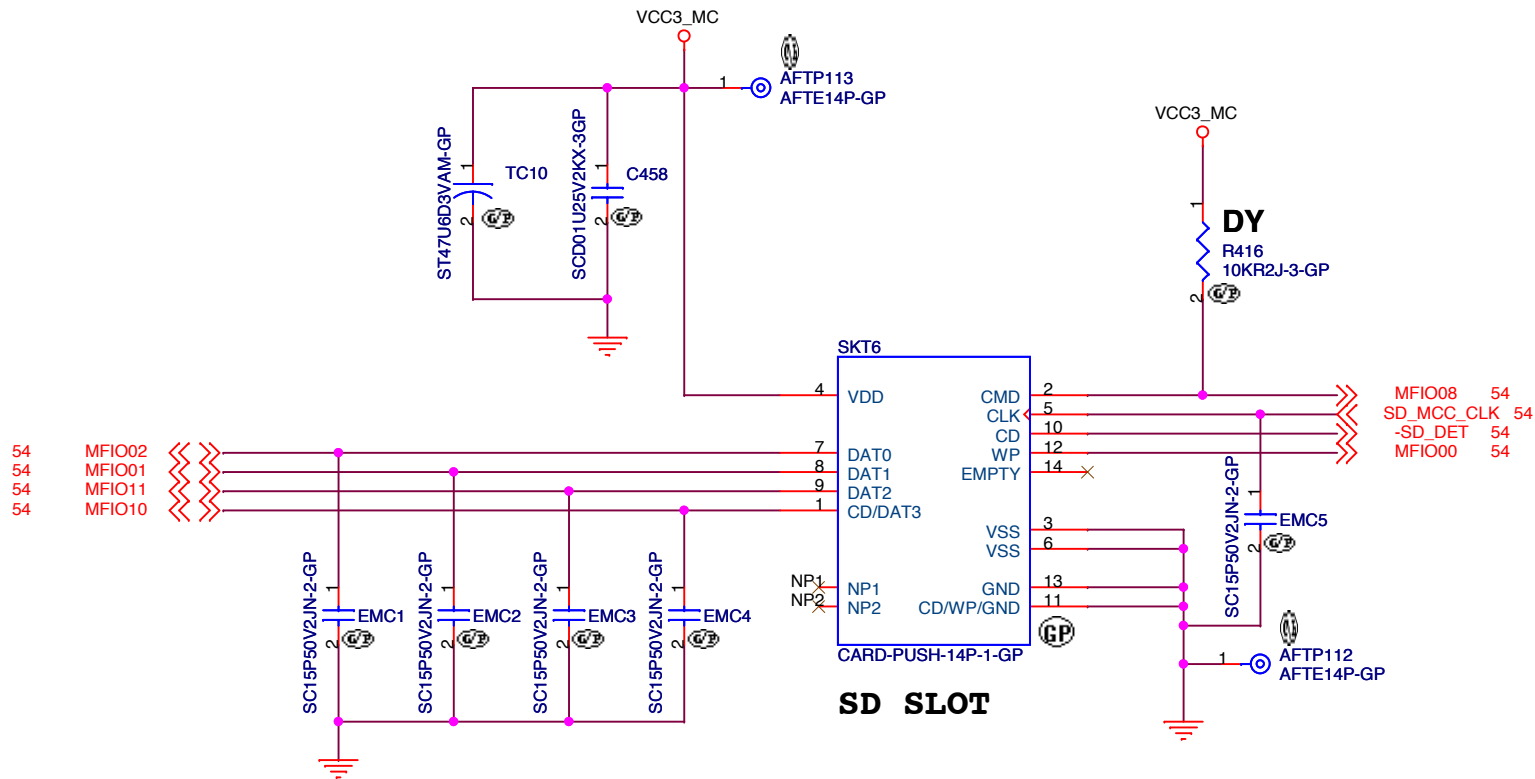
**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title			
RJ45 CONNECTOR			
Size	Document Number	Rev	
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MEDIA I/F	SD/MMC	MEMORYSTICK	XD
MFIO00	SDWP#	MSBS	XD_D7
MFIO01	SD_D1		XD_D6
MFIO02	SD_D0	MS_D1	XD_D5
MFIO03	(SD_D7)		XD_D4
MFIO04	(SD_D6)	(MS_D5)	XD_D3
MFIO05	SD_CLK	MSD0	XD_D2
MFIO06			XD_D1
MFIO07	(SD_D5)	(MS_D4)	XD_D0
MFIO08	SD_CDM	MS_D2	XD_WP#
MFIO09	(SD_D4)	(MS_D6)	XD_WE#
MFIO10	SD_D3	MS_D3	XD_ALE
MFIO11	SD_D2		XD_CLE
MFIO12			XD_CE#
MFIO13		(MS_D7)	XD_RE#
MFIO14		MS_CLK	XD_R/B
MFCD0#	SDDC#		XDCD0#
MFCD1#		MSINS#	XDCD1#





<Core Design>

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

Title

**Media Card Slot**

Size  
A4

Document Number

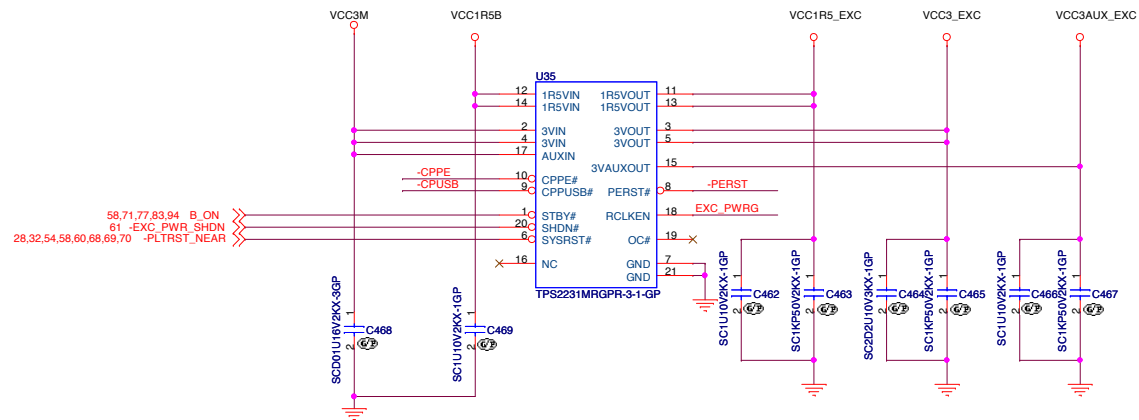
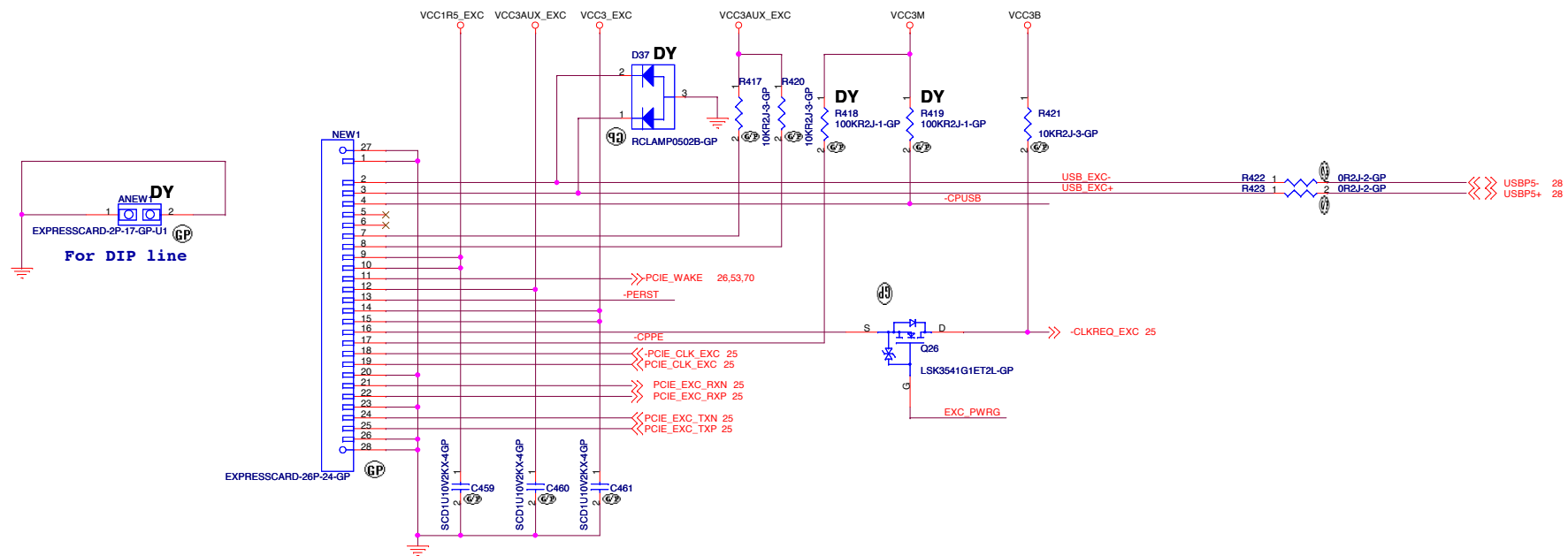
**Dasher-2**

Rev  
-1

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U35	TI	TPS2231MRGP-3	45K0234BA
	Rohm	BD4156MUV-GTR	74.04156.A73
	Rohm	BD4157MUV-GE2	74.04157.A73

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緯創資通 Wistron Corporation  
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Taipei Hsien 221, Taiwan, R.O.C.

Title Express Card Connector

Size Custom Document Number Dasher-2 Rev -1

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**Wistron Corporation**

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Taipei Hsien 221, Taiwan, R.O.C.

Title

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Size  
A

Document Number

**Dasher-2**

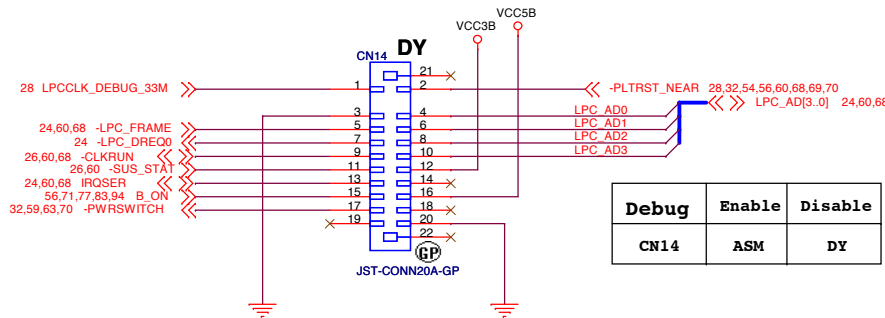
Rev  
**-1**

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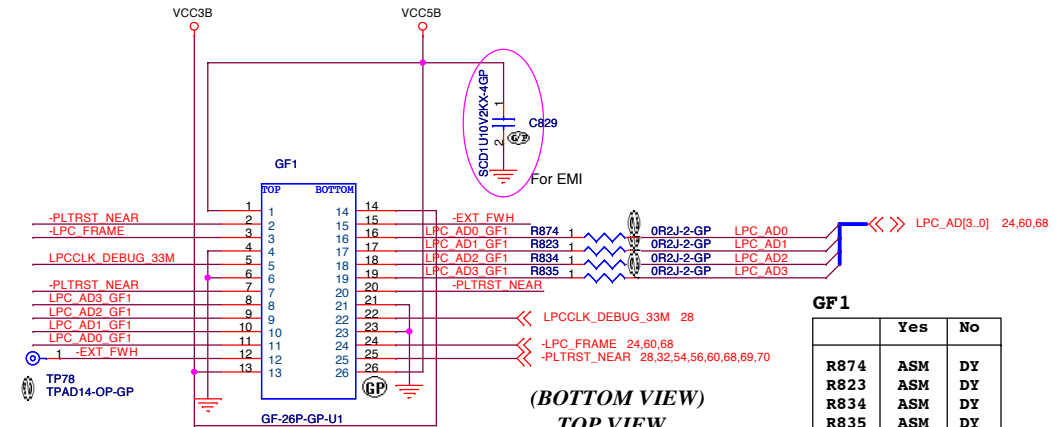
## Debug card connector



Put "easy-to -access" place

4MB				
SO8	Marconix	MX25L3206EM2I-12G	72.25320.C01	
	Winbond	W25Q032BVSSIG	72.25Q32.A01	
	Eon	EN25QH32-104HIP	72.02532.B01	
	Numonyx	N25Q032A13ESEC0	72.25Q32.C01	
8MB				
SO8	Marconix	MX25L6406EM2I-12G	72.25640.D01	
	Winbond	W25Q064CVSSIG	72.25Q64.B01	
	Eon	EN25QH64-104HIP	72.02564.001	
	Numonyx	N25Q064A13ESEC0	72.25Q64.G01	
	Winbond	W25Q64FVSSIG	72.25Q64.F01	

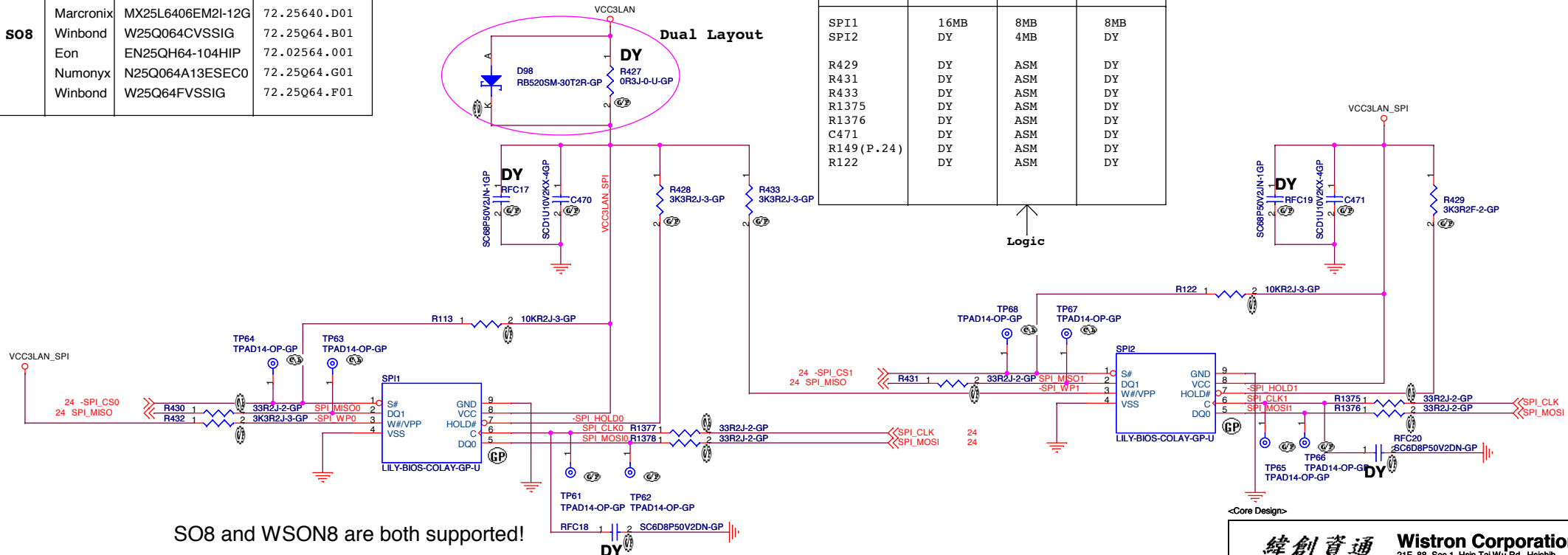
## Golden Finger for Debug Board



Configuration Table

	CONFIG-1 16MB	CONFIG-2 12MB	CONFIG-3 8MB
SPI1	16MB	8MB	8MB
SPI2	DY	4MB	DY
R429	DY	ASM	DY
R431	DY	ASM	DY
R433	DY	ASM	DY
R1375	DY	ASM	DY
R1376	DY	ASM	DY
C471	DY	ASM	DY
R149 (P.24)	DY	ASM	DY
R122	DY	ASM	DY

Logic



SO8 and WSON8 are both supported!

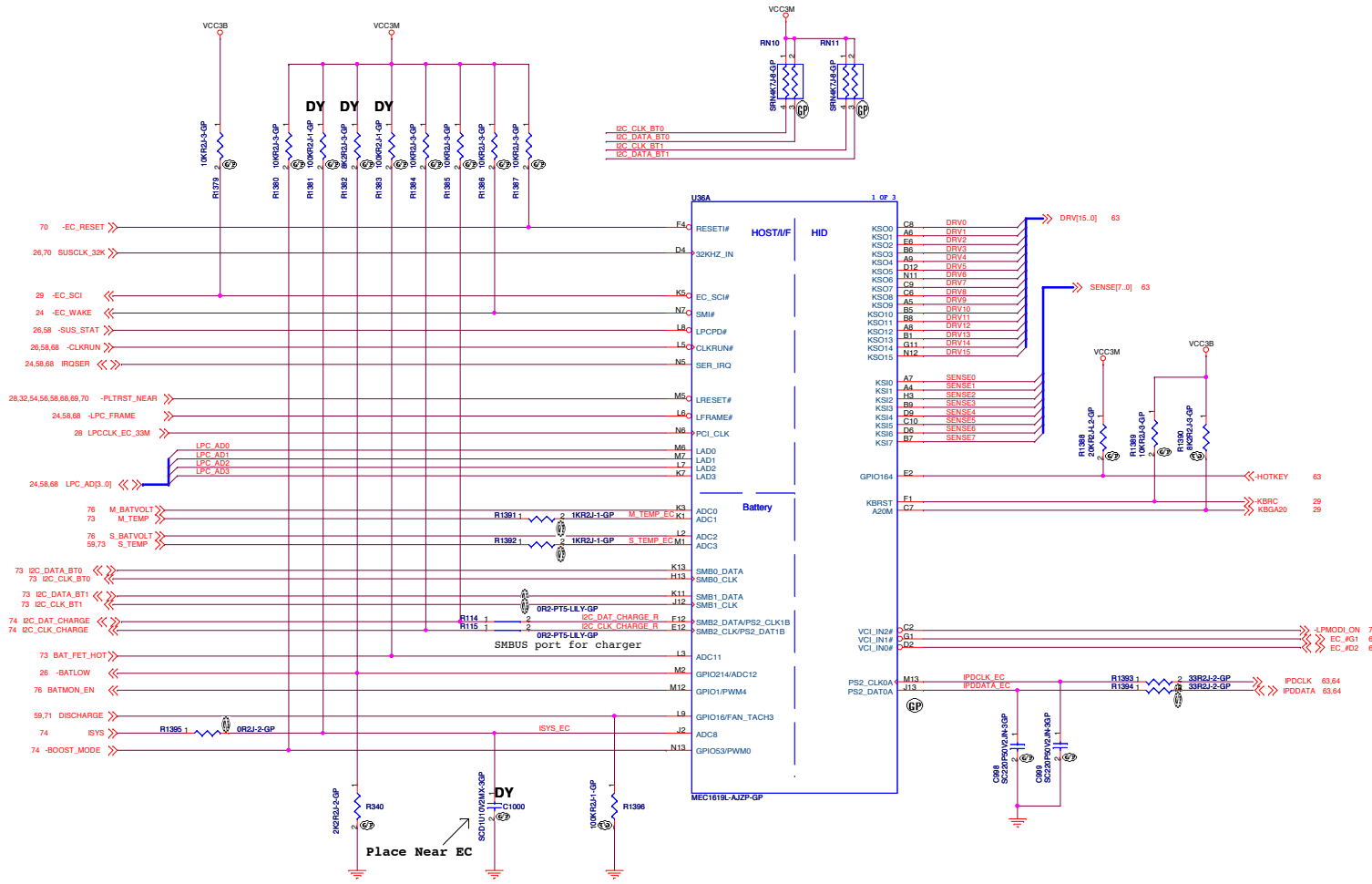
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緯創資通 Wistron Corporation  
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Taipei Hsien 221, Taiwan, R.O.C.

Title	SPI&LPC debug card		
Size	Document Number	Dasher-2	Rev -1
A3			
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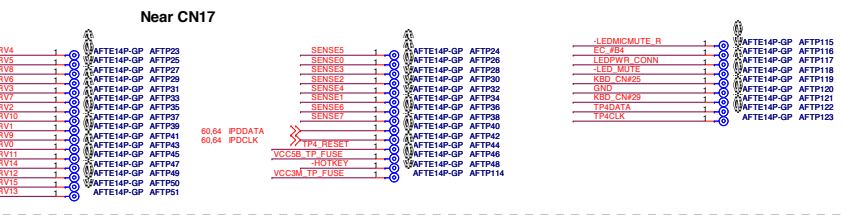


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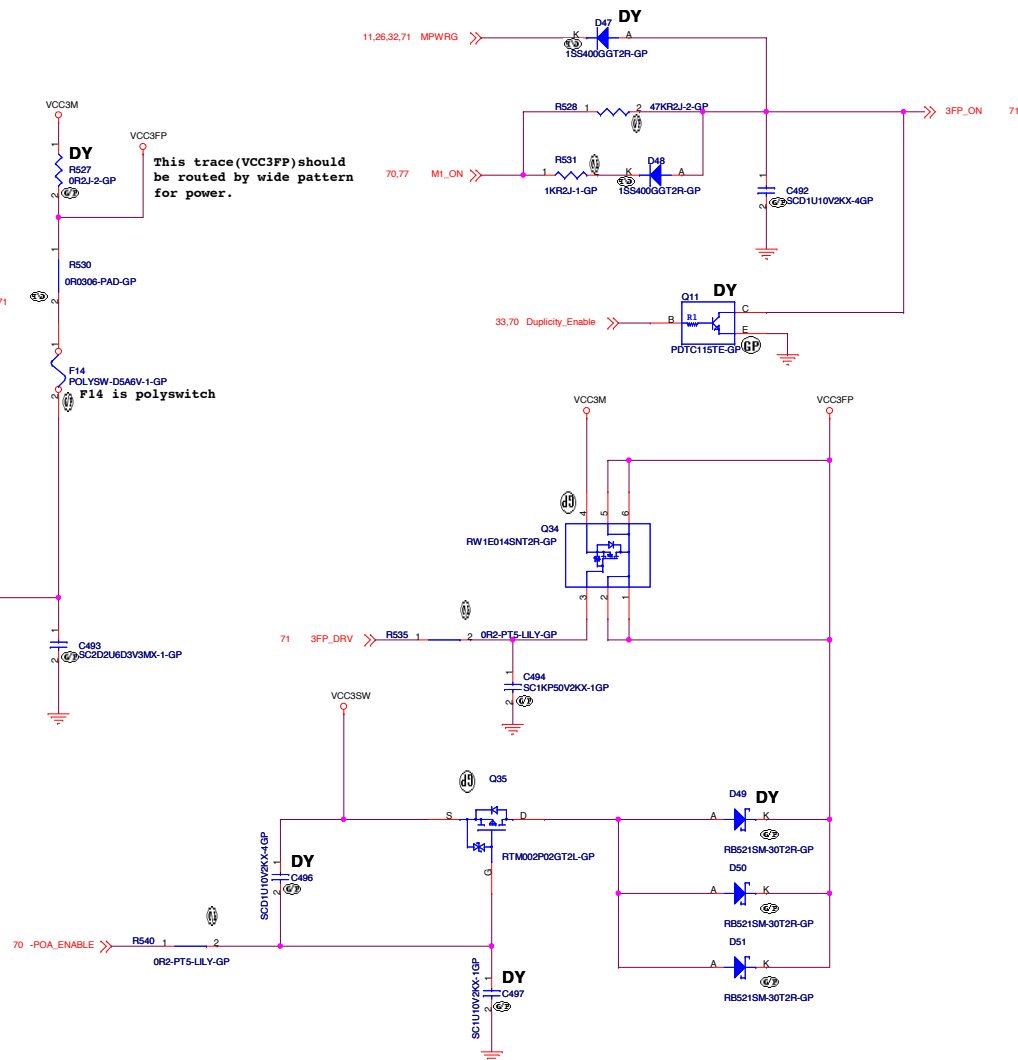
		case1	case2	case3	case4
CS12 KBD w/o BL		v	v	v	
CS12 KBD w BL			v	v	
CS09				v	v
Parts#	parts	case1	case2	case3	case4
R108	10k_5%_0402	DY	DY	ASM	ASM
R104	0_5%_0402	ASM	ASM	DY	DY
R89	100k_5%_0402	ASM	ASM	ASM	ASM
R105	1k_5%_0402	ASM	DY	ASM	ASM
R111	10k_5%_0402	ASM	ASM	ASM	ASM
R110	10k_5%_0402	DY	ASM	ASM	ASM
R92	0_5%_0402	DY	DY	ASM	ASM
R91	390_5%_0402	DY	ASM	ASM	ASM
R107	0_5%_0402	DY	ASM	DY	DY
R103	0_5%_0402	ASM	ASM	DY	DY
R106	0_5%_0402	DY	ASM	ASM	ASM
R112	0_5%_0402	DY	ASM	DY	DY
R109	100k_5%_0402	DY	ASM	ASM	DY
R191	0_5%_0402	ASM	ASM	DY	ASM
R217	0_5%_0402	DY	DY	ASM	DY
R300	0_5%_0402	DY	DY	ASM	ASM
C16	47uF_6.3V_0805	DY	DY	DY	DY
C15	10uF_6.3V_0603	DY	ASM	ASM	DY
C14	10uF_6.3V_0603	DY	ASM	ASM	DY
Q1	LTC015TE	DY	DY	ASM	ASM
Q8	LTA015TE	DY	DY	ASM	DY
Q7	RW1E015RP	DY	DY	ASM	DY
F5	1.5A/6A Fuse	DY	ASM	DY	DY
	1.5A/6V PolySW	DY	DY	ASM	DY
R314	100k_5%_0402	DY	ASM	DY	DY


**KBD Table2**

Pre-DV	case4
SDV	case2
FVT	case2
PreSIT	case2
SIT	case2

← Logic

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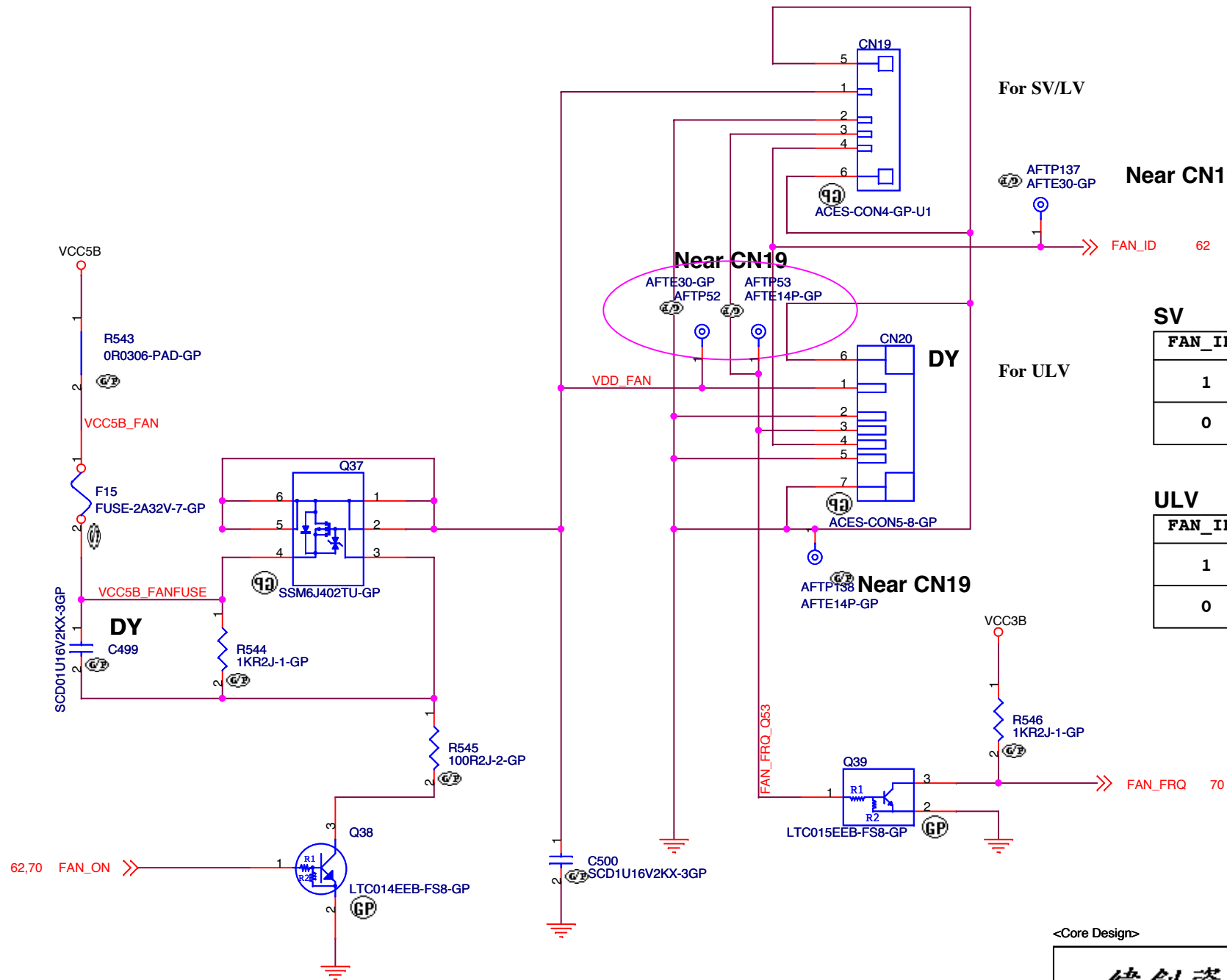
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Title			
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Size	Document Number	Rev	
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Title <b>BLANK</b>			
Size A	Document Number <b>Dasher-2</b>		Rev <b>-1</b>
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For SV/LV

Near CN19

For ULV

SV

FAN_ID	Thermal module
1	DELTA or Toshiba
0	AVC

ULV

FAN_ID	Thermal module
1	Toshiba
0	AVC or DELTA

<Core Design>

緯創資通

**Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title

**FAN Control**

Size  
A4

Document Number

**Dasher-2**

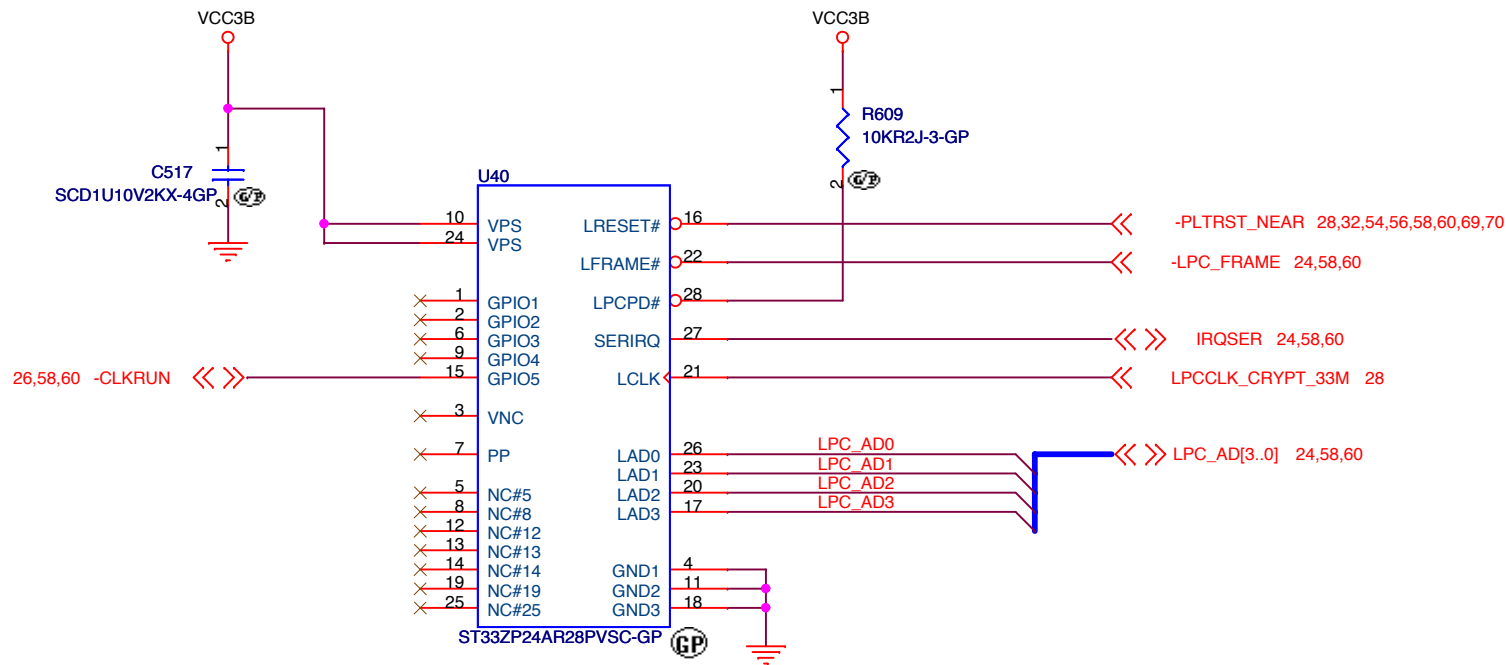
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-1

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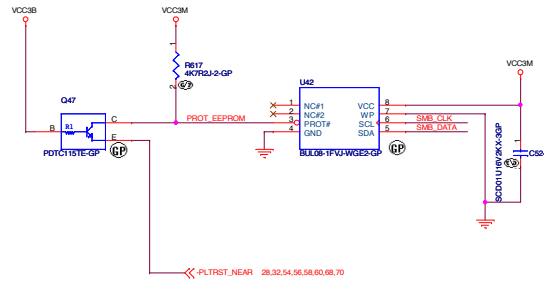


BU	After SDV
ST19NP18ER28PVMO (71.19N18.T0W)	ST33ZP24AR28PVxx xx="OG" for SDV(71.03324.A0W), "RC" for FVT, PreSIT (FW 1.2.C.0)(71.03324.C0W) SC for SIT (not PreSIT) FW 1.2.D.0(71.03324.D0W)

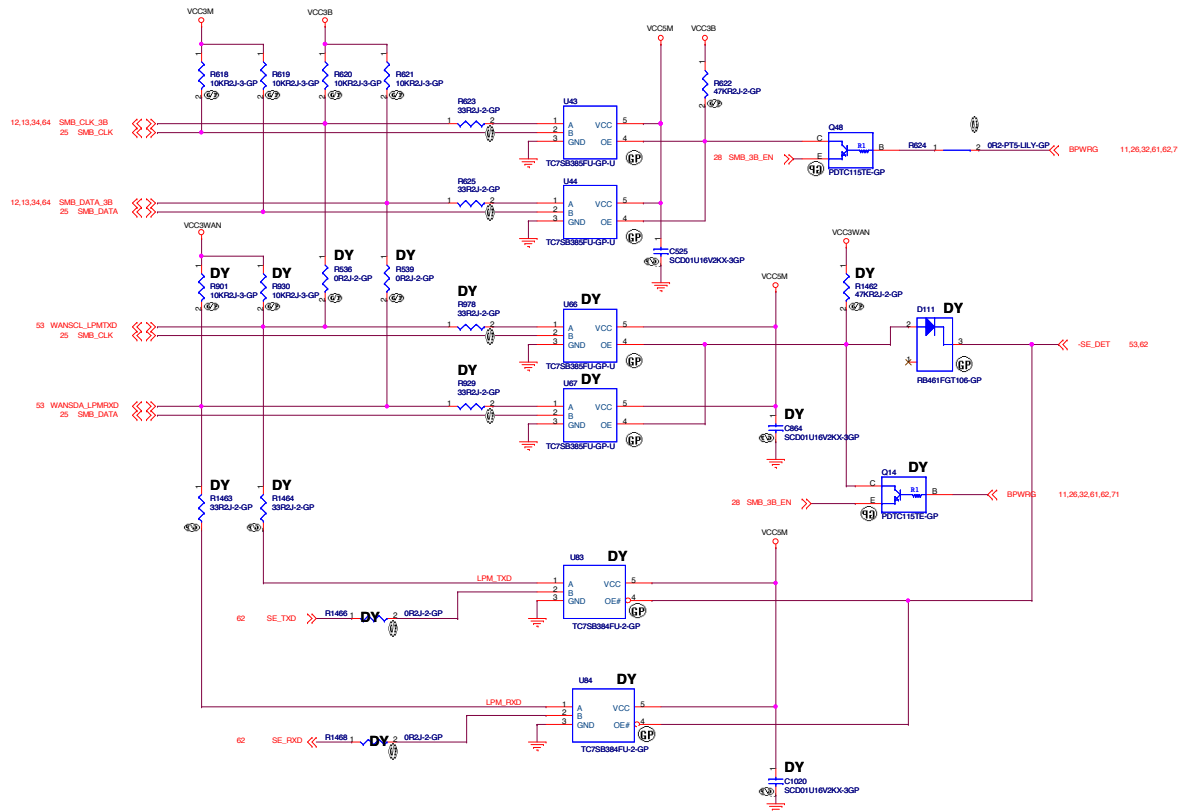
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<div>緯創資通</div>		<div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>	
Title			
TPM			
Size A4	Document Number		Rev -1
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# EEPROM



	Vendor	U42	Part Number
1st	ROHM	BUL08-1FVJ-W	72.BUL08.A0Q
2nd	NXP	PCA24S08ADP	72.24S08.A0Q
3rd	Sanyo	LE26CAP08TT	72.26C08.00R



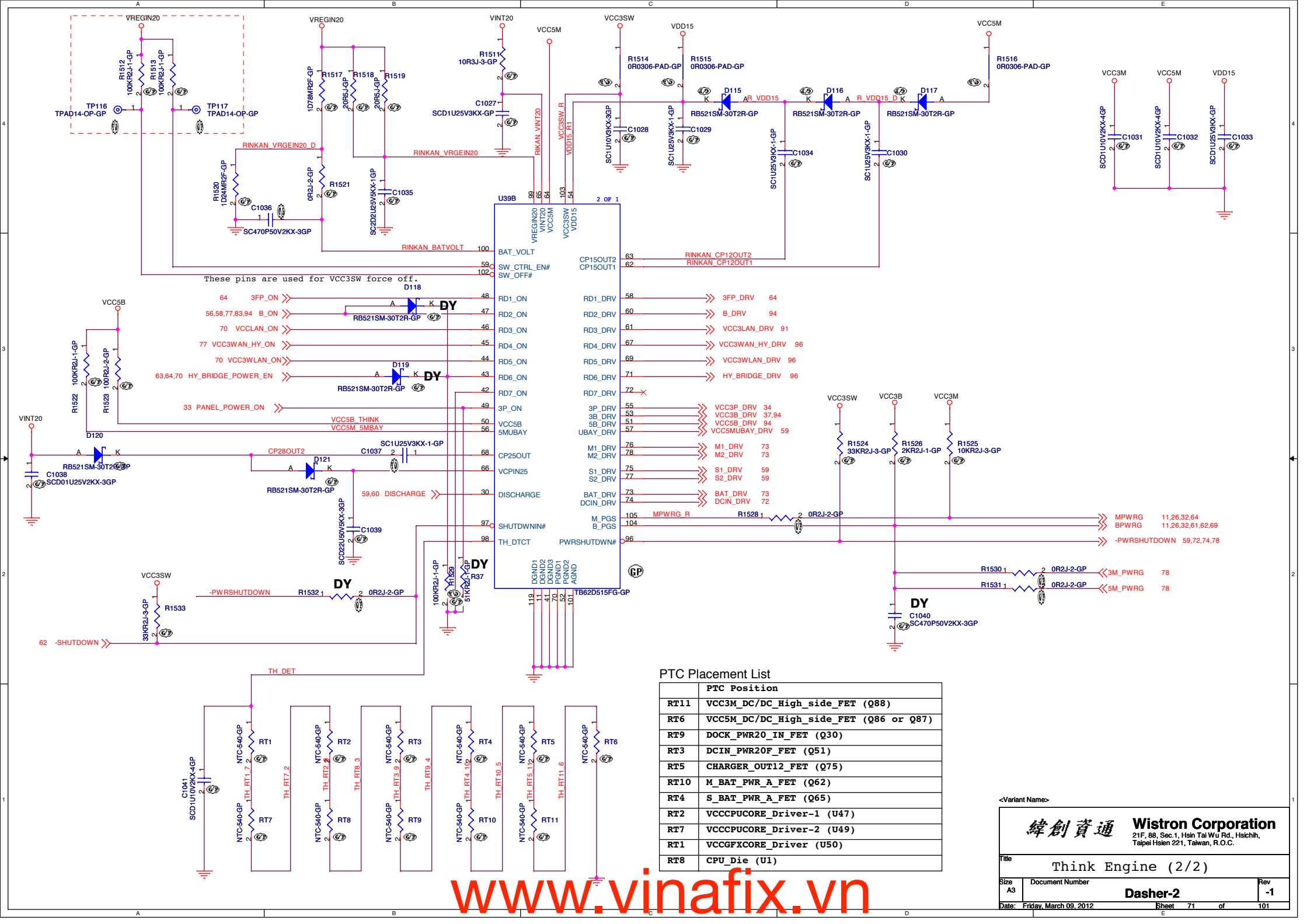
Core Design  
**緯創資通** Wistron Corporation  
 21F, 8B, Sec.1, Hsin Tai Wu Rd., Hsinchu,  
 Taipei Hsin 301, Taiwan, R.O.C.

EEPROM/SMBUS/CM

Rev: 1.0  
 Date: 2011.01.11  
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 -1

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PTC Placement List

	PTC Position
RT11	VCC3M_DC/DC_High_side_FET (Q88)
RT6	VCC5M_DC/DC_High_side_FET (Q86 or Q87)
RT9	DOCK_PWR20_IN_FET (Q30)
RT3	DCIN_PWR20F_FET (Q51)
RT5	CHARGER_OUT12_FET (Q75)
RT10	M_BAT_PWR_A_FET (Q62)
RT4	S_BAT_PWR_A_FET (Q65)
RT2	VCCCPUCORE_Driver-1 (U47)
RT7	VCCCPUCORE_Driver-2 (U49)
RT1	VCCGFXCORE_Driver (U50)
RT8	CPU_Die (U1)

<Variant Name>

緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsein 221, Taiwan, R.O.C.

Title

Think Engine (2/2)

Size

A3

Document Number

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Rev

-1

Date

Friday, March 09, 2012

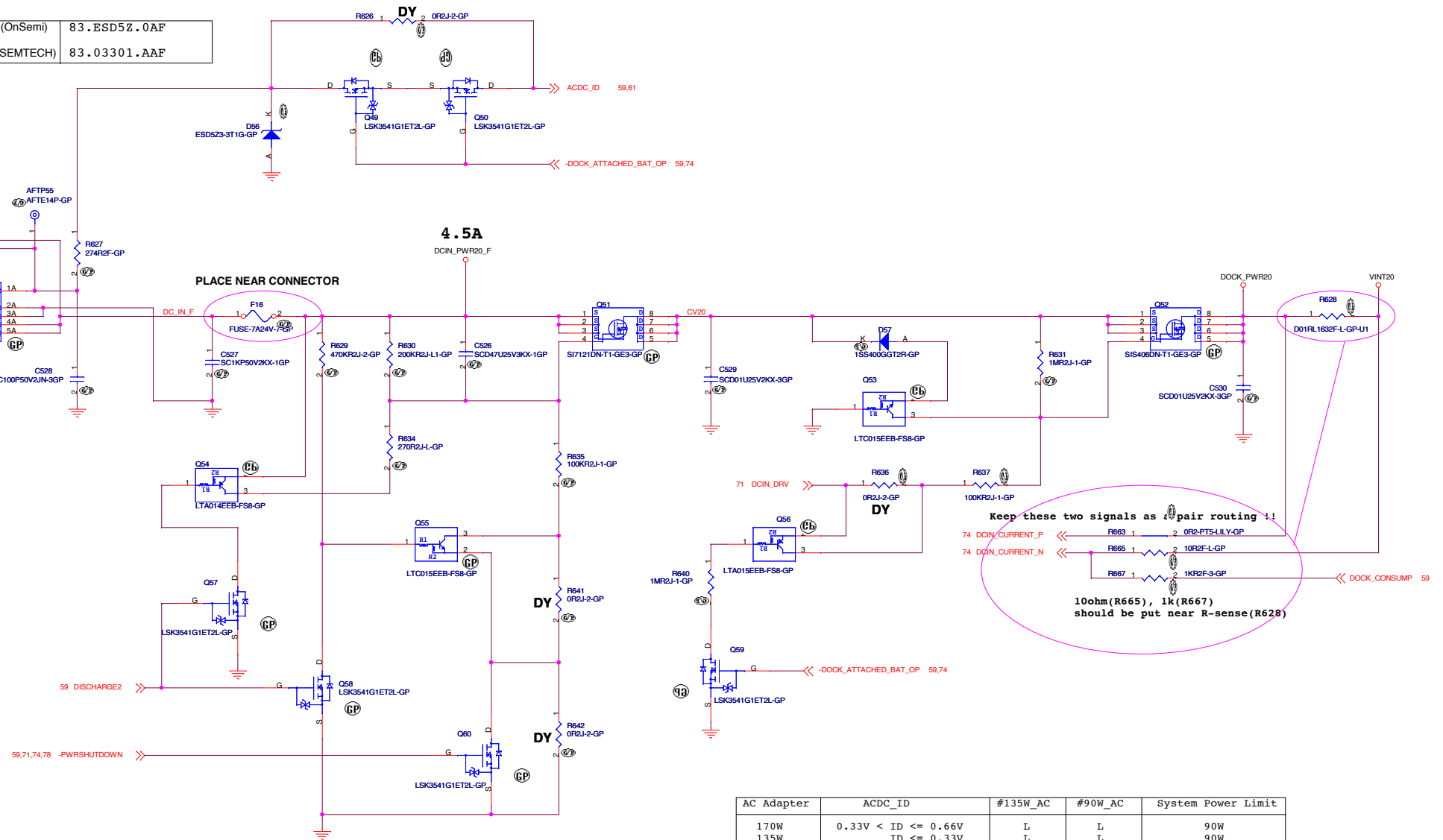
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of

101

ESD5Z3.3T1G(OnSemi)	83.ESD5Z.0AF
uClamp3301H(SEMTECH)	83.03301.AAF



AC Adapter	ACDC_ID	#135W_AC	#90W_AC	System Power Limi
170W	0.33V < ID <= 0.66V	L	L	90W
135W	ID <= 0.33V	L	L	90W
90W	2.64V < ID	H	L	90W
65W	1.32V < ID <= 1.98V	H	H	65W

PEAK SHIFT	YES	NO
R641	NO-ASM	ASM
R629	ASM	NO-ASM
Q58	ASM	NO-ASM
Q55	ASM	NO-ASM

## Logic

Logic

<Core Design>

緯創資通

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Taipei Hsien 221, Taiwan, R.O.C.

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30
Title																													

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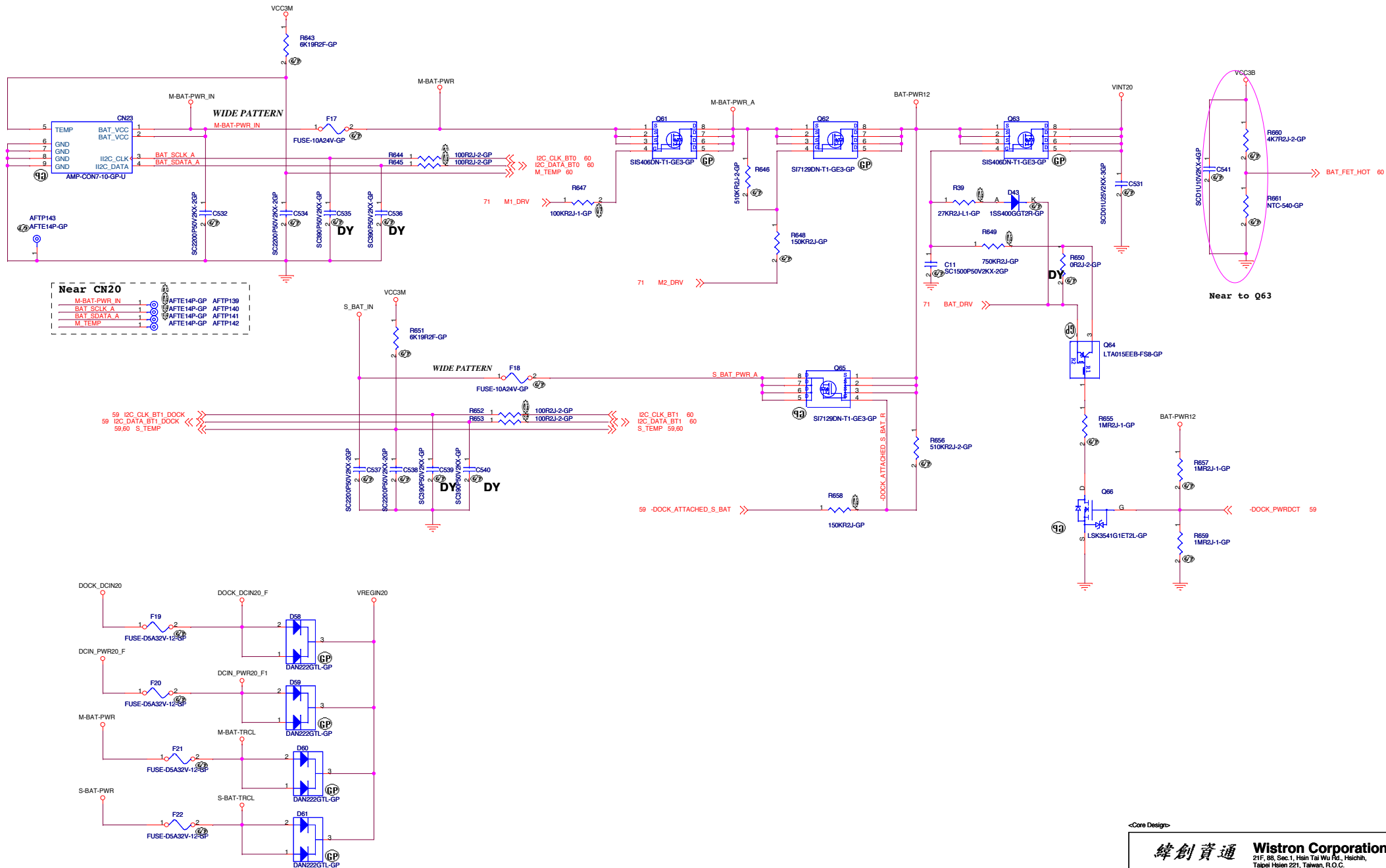
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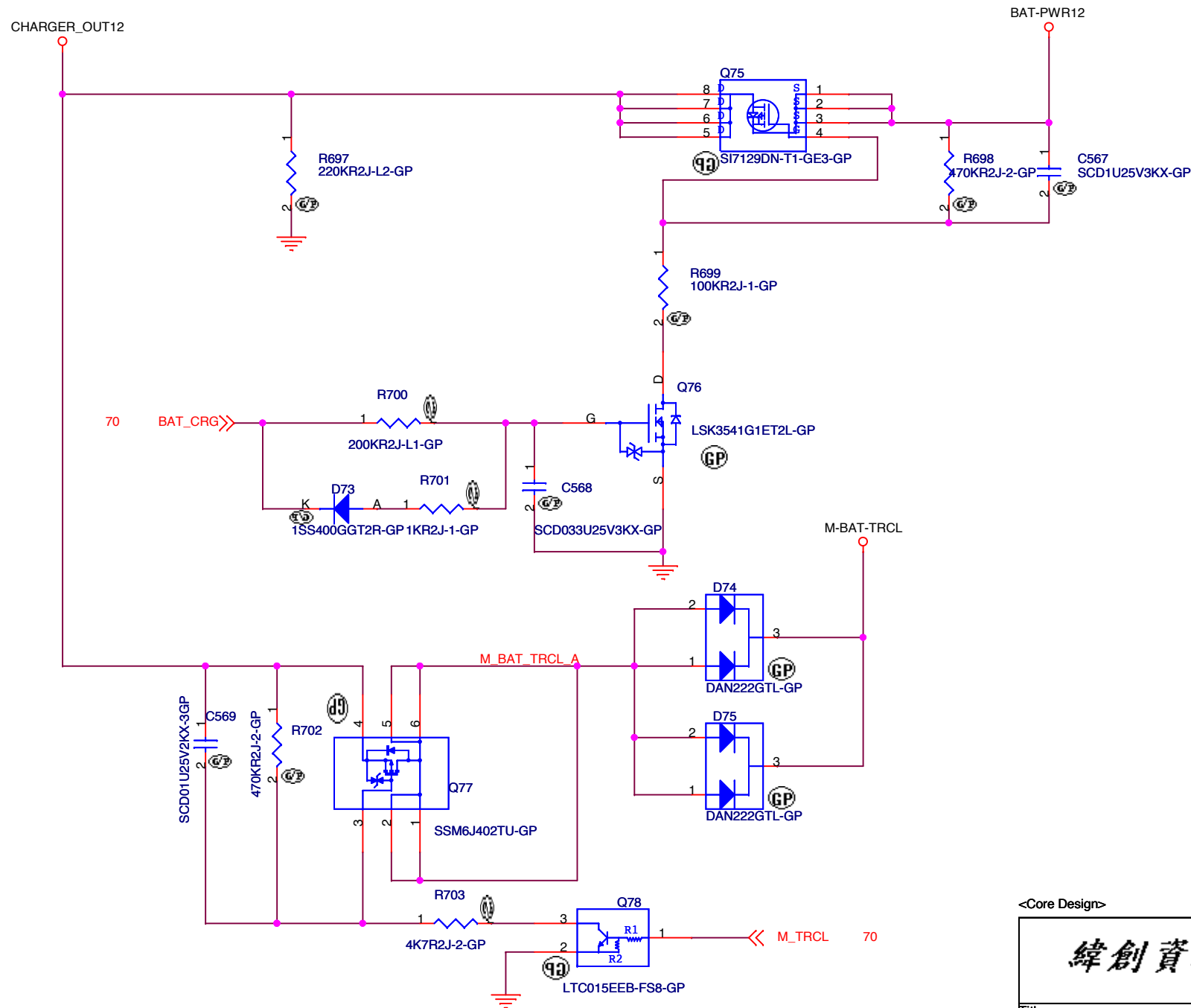
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<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**CHARGER SELECT**

Size  
A4

Document Number

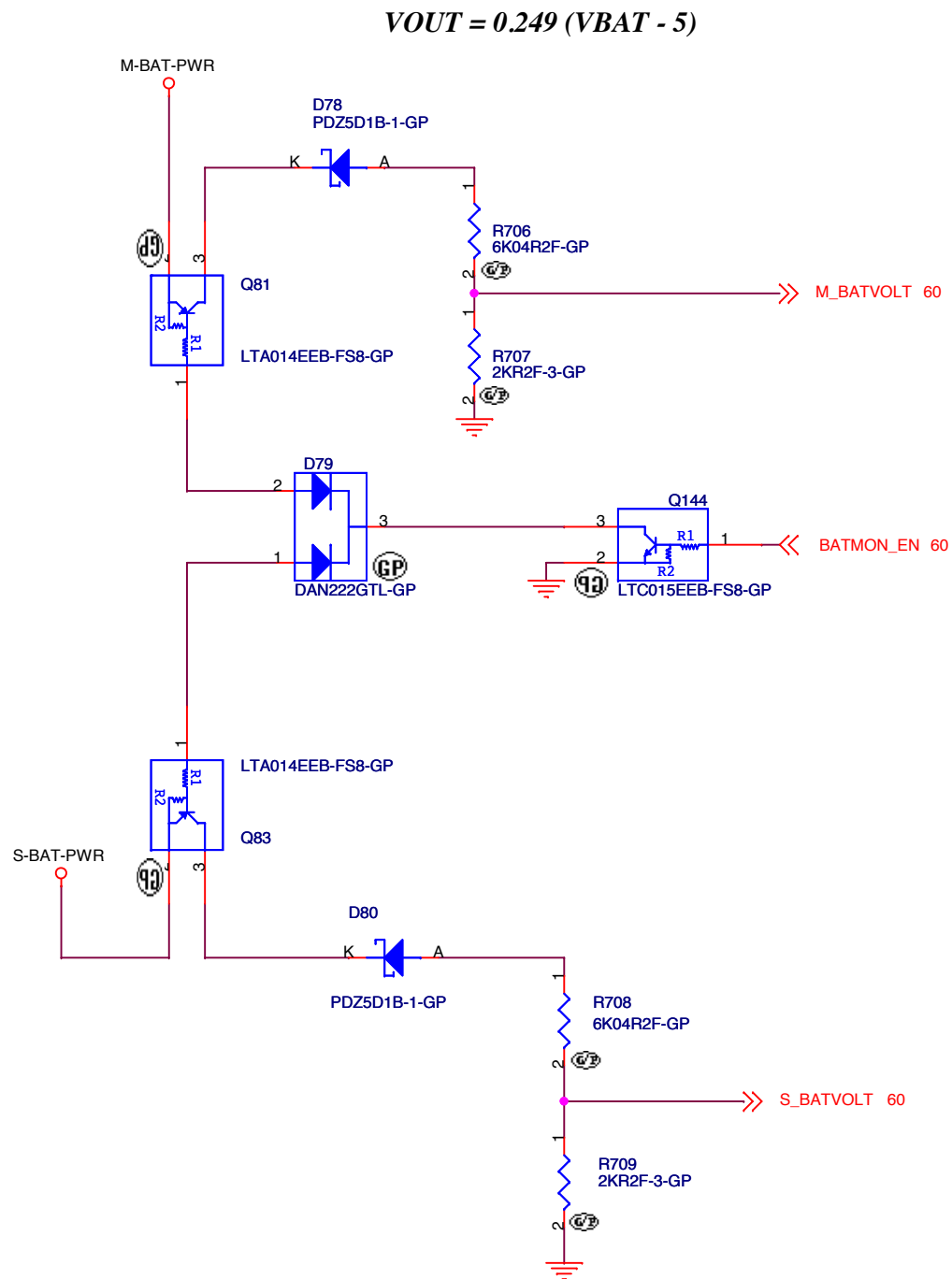
**Dasher-2**

Rev  
-1

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<Core Design>

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**BATTERY MONITOR**

Size  
A4

Document Number

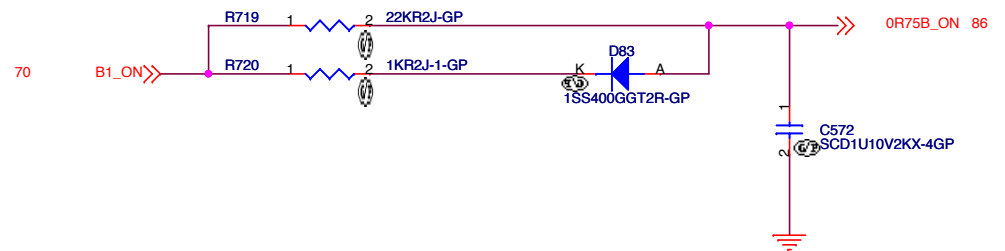
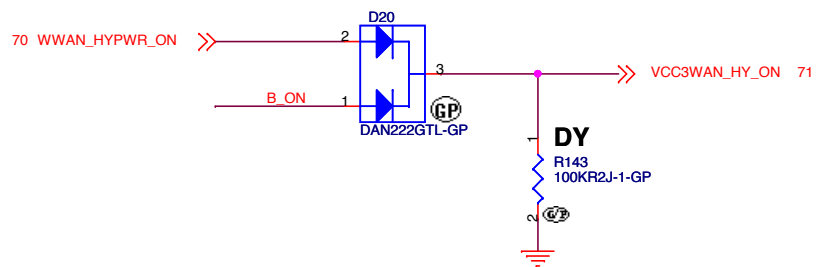
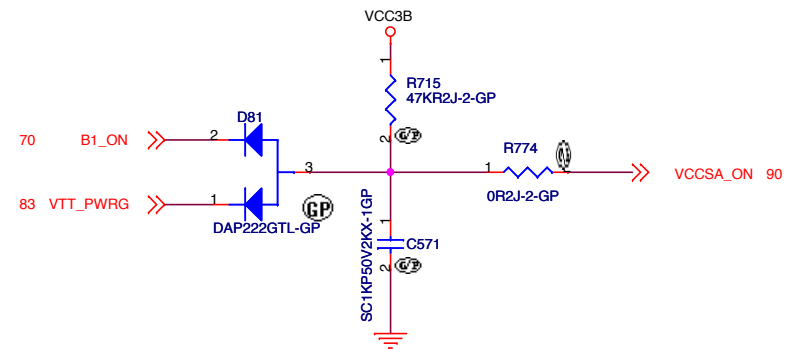
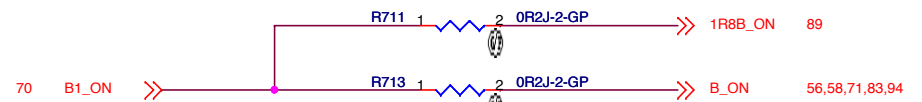
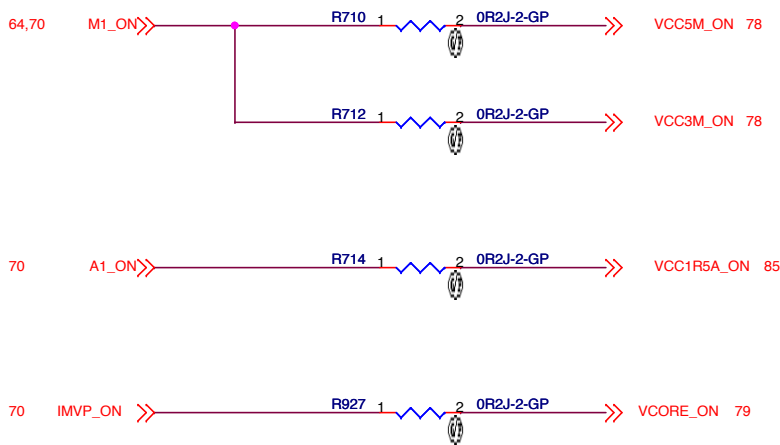
**Dasher-2**

Rev  
-1

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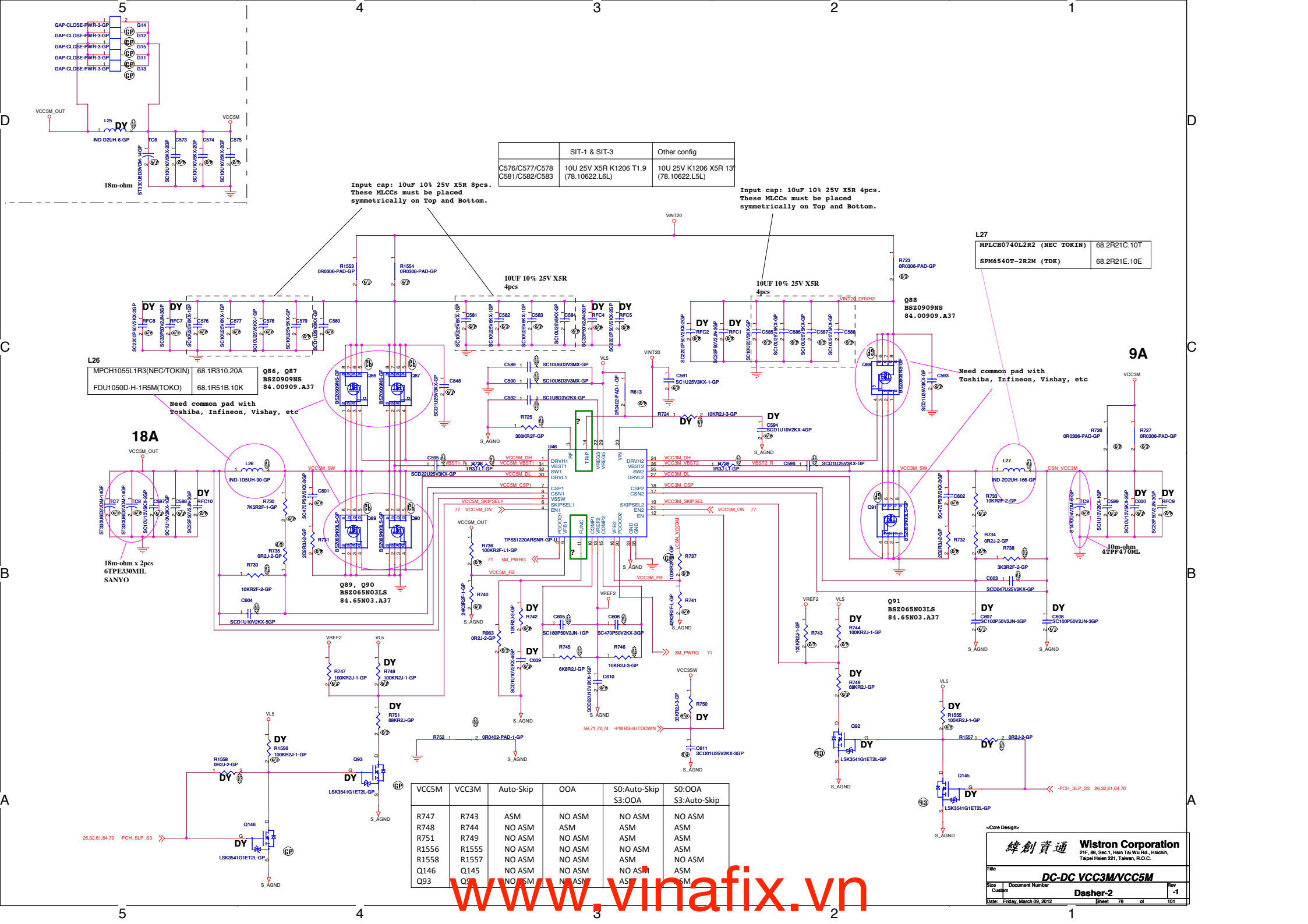


AOAC Duplicity	YES YES	YES NO	NO YES	NO NO
D20 R143	ASM DY	ASM DY	ASM DY	DY ASM

Logic

<Core Design>

<b>緯創資通</b>		<b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>POWER SEQUENCE</b>			
Size Custom	Document Number <b>Dasher-2</b>		Rev <b>-1</b>
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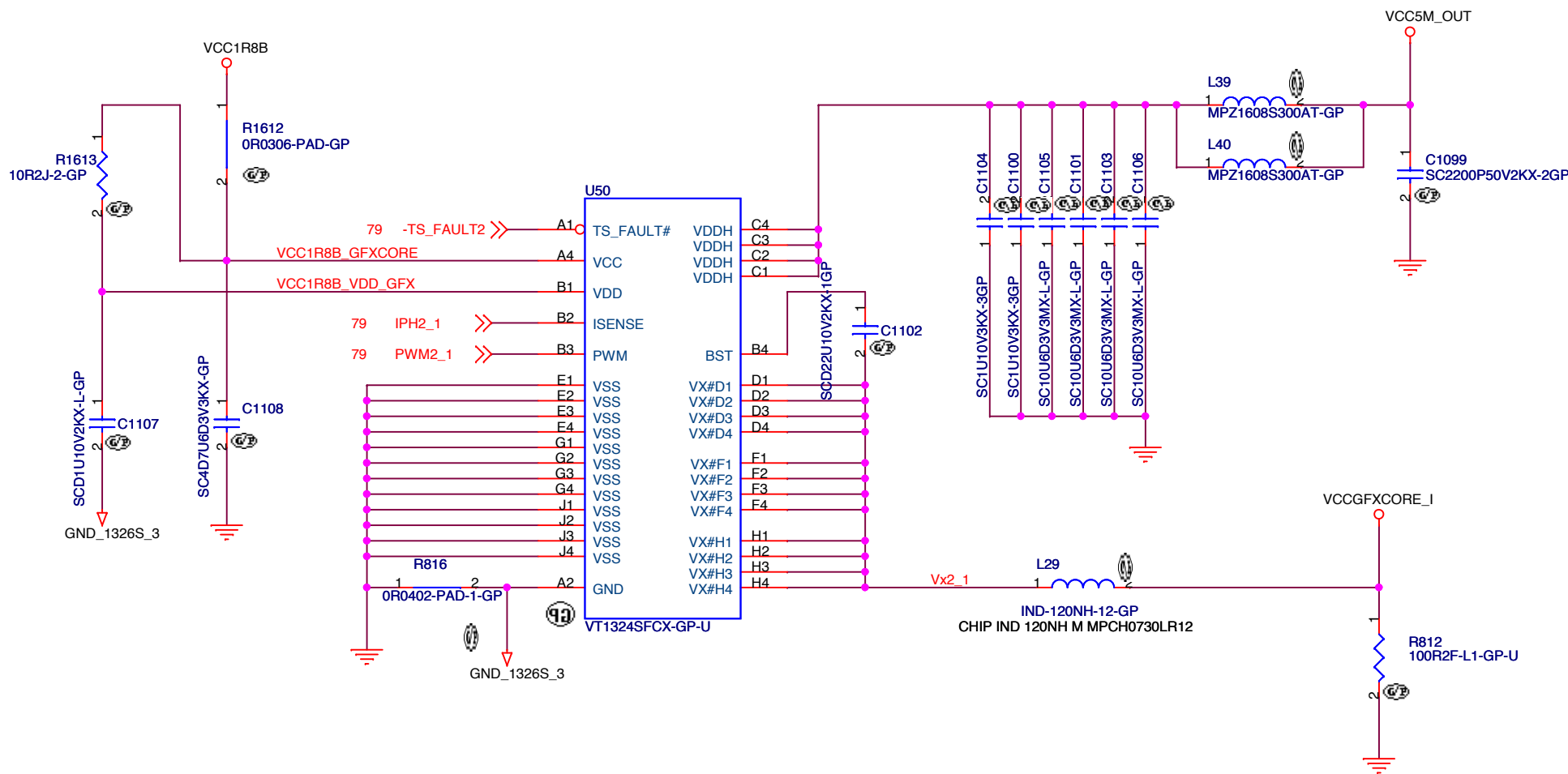


SIT-1 & SIT-3	Other config
C576/C577/C578 C581/C582/C583	10U 25V X5R K1206 T1.9 (78.10622.L6L)
	10U 25V K1206 X5R 13 (78.10622.L5L)

L27	MPLCH0740L2R2 (NEC TOKIN)	68.2R21C.10T
	SPH6540T-2R2M (TDK)	68.2R21E.10E

VCC5M	VCC3M	Auto-Skip	OOA	S0:Auto-Skip S3:OOA	S0:OOA S3:Auto-Skip
R747	R743	ASM	NO ASM	NO ASM	NO ASM
R748	R744	NO ASM	ASM	NO ASM	NO ASM
R751	R749	NO ASM	NO ASM	ASM	ASM
R1556	R1555	NO ASM	NO ASM	NO ASM	ASM
R1558	R1557	NO ASM	NO ASM	ASM	NO ASM
Q146	Q145	NO ASM	NO ASM	NO ASM	ASM
Q93	Q92	NO ASM	NO ASM	ASM	NO ASM





<Variant Name>

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Title **VCCGFXCORE**

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VCCCPUCORE

C087

1 x 22uF\_0603 MLCC for VCCCPUCORE

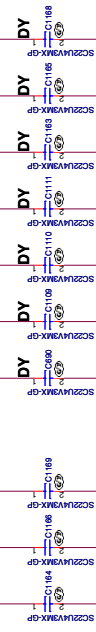
VCCCPUCORE



3 x 10uF + 7 x 4.7uF\_0603 MLCC for VCCCPUCORE

3 x 22uF + 7 x 22uF\_0603 MLCC (No ASM) for VCCCPUCORE

VCCCPUCORE



20 x 22uF + 5 x 4.7uF\_0603 MLCC for VCCCPUCORE

VCCCPUCORE

C087

C088

C089

C090

C091

C092

C093

C094

C095

C096

C097

C098

C099

C100

C101

C102

C103

C104

C105

C106

C107

C108

C109

C110

C111

C112

C113

C114

C115

C116

C117

C118

C119

C120

C121

C122

C123

C124

C125

C126

C127

VCCCPUCORE

C128

C129

C130

C131

C132

C133

C134

C135

C136

C137

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C141

C142

C143

C144

C145

C146

C147

C148

C149

C150

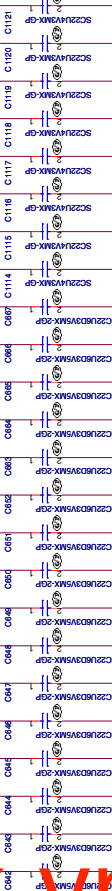
C151

C152

C153

40 x 2.2uF + 3 x 2.2uF (NO ASM)\_0402 MLCC for VCCCPUCORE

VCCCPUCORE



24 x 22uF MLCC for VCCGFXCORE\_1

<Core Design>

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2/F, 48, Sec. 1, Hsin Tai Wu Rd., Hsinshui,  
Taichung 412, Taiwan, R.O.C.

DC-DC VCCCPUCORE

Document Number

Rev. 1

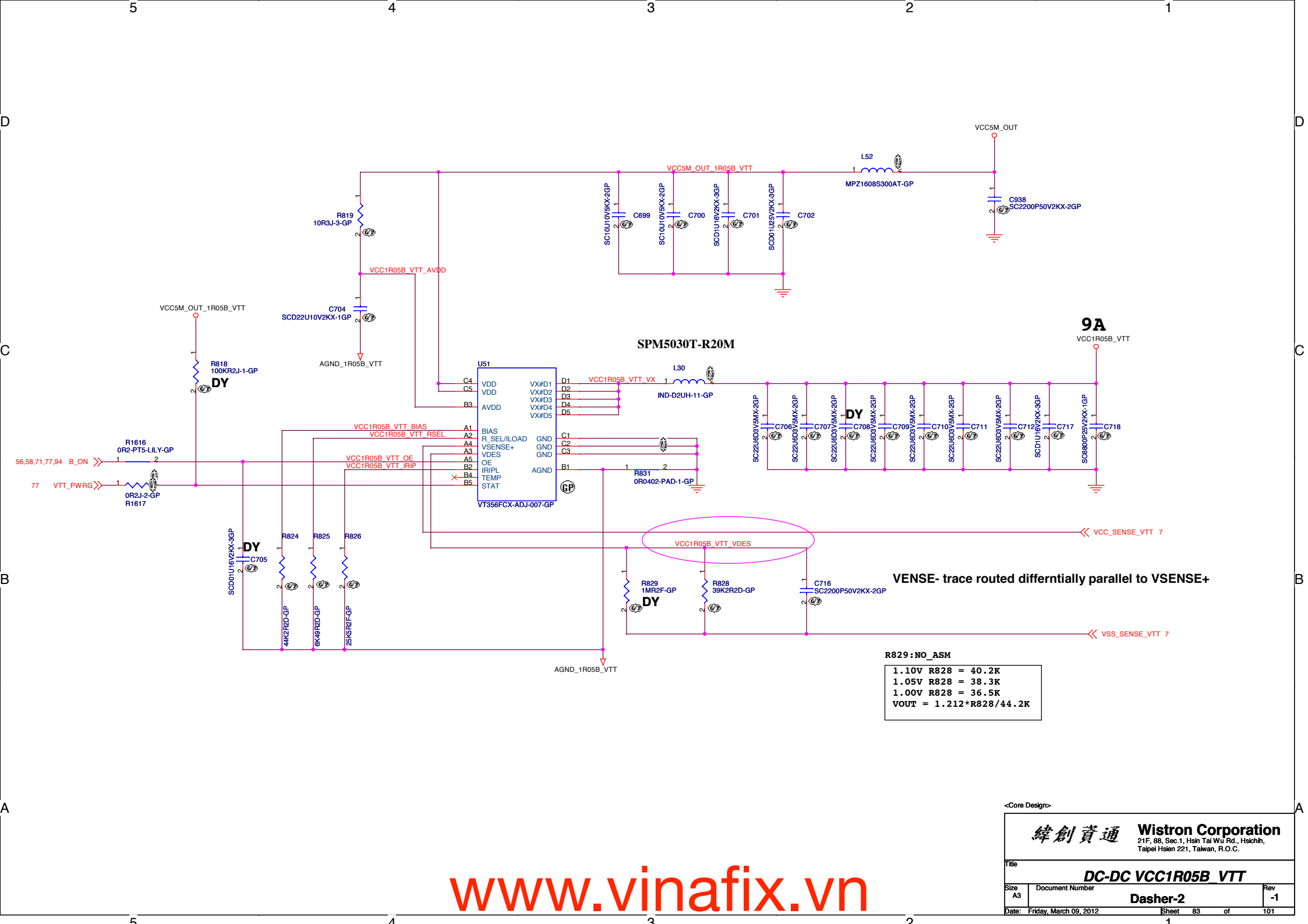
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1

1

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SPM5030T-R20M

9A

VCC1R05B\_VTT

VENSE- trace routed differentially parallel to VSENSE+

R829:NO\_ASM

1.10V R828 = 40.2K  
1.05V R828 = 38.3K  
1.00V R828 = 36.5K  
VOUT = 1.212\*R828/44.2K

<Core Design>

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Title

DC-DC VCC1R05B VTT

Size

A3

Document Number

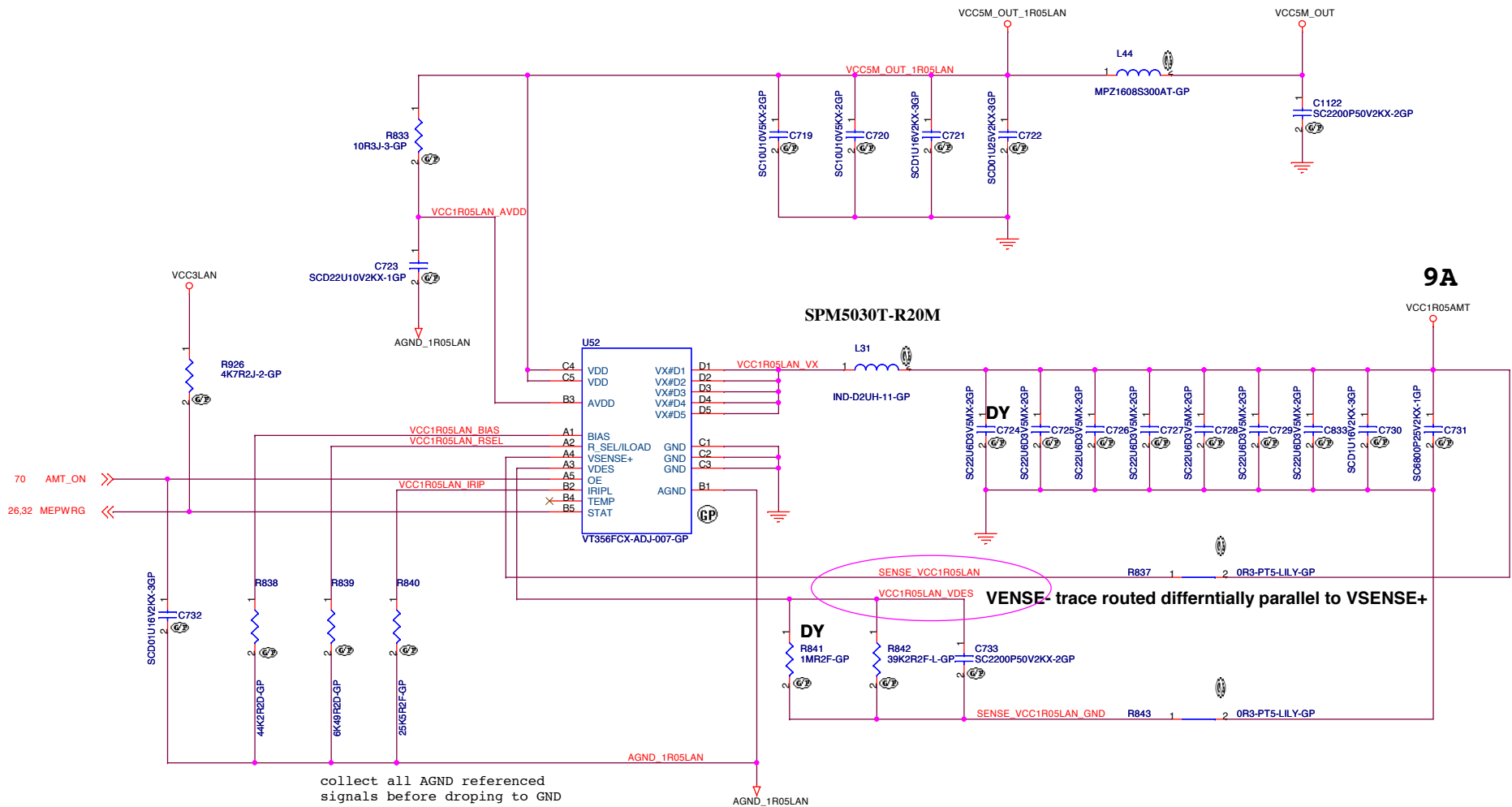
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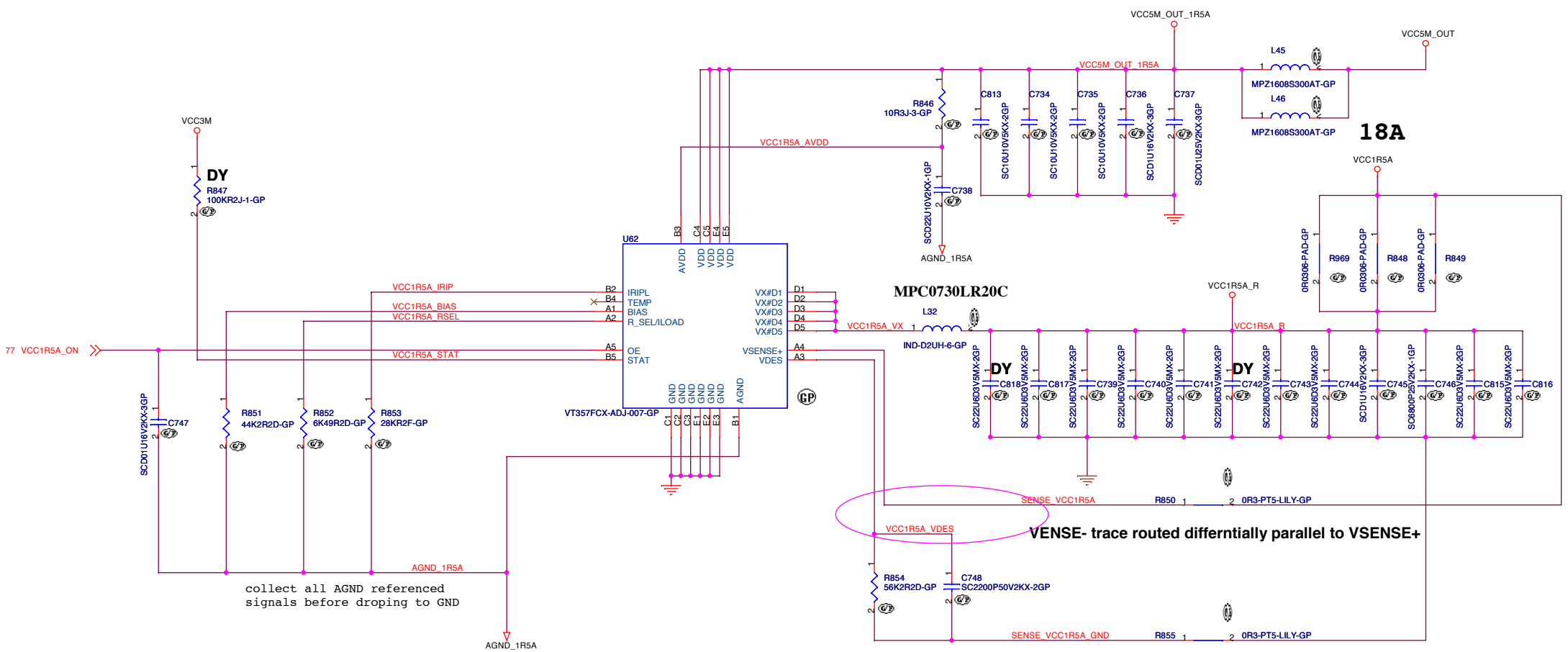
Rev

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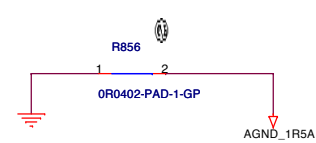
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Don't connect AGND and GND under the chip



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<Variant Name>

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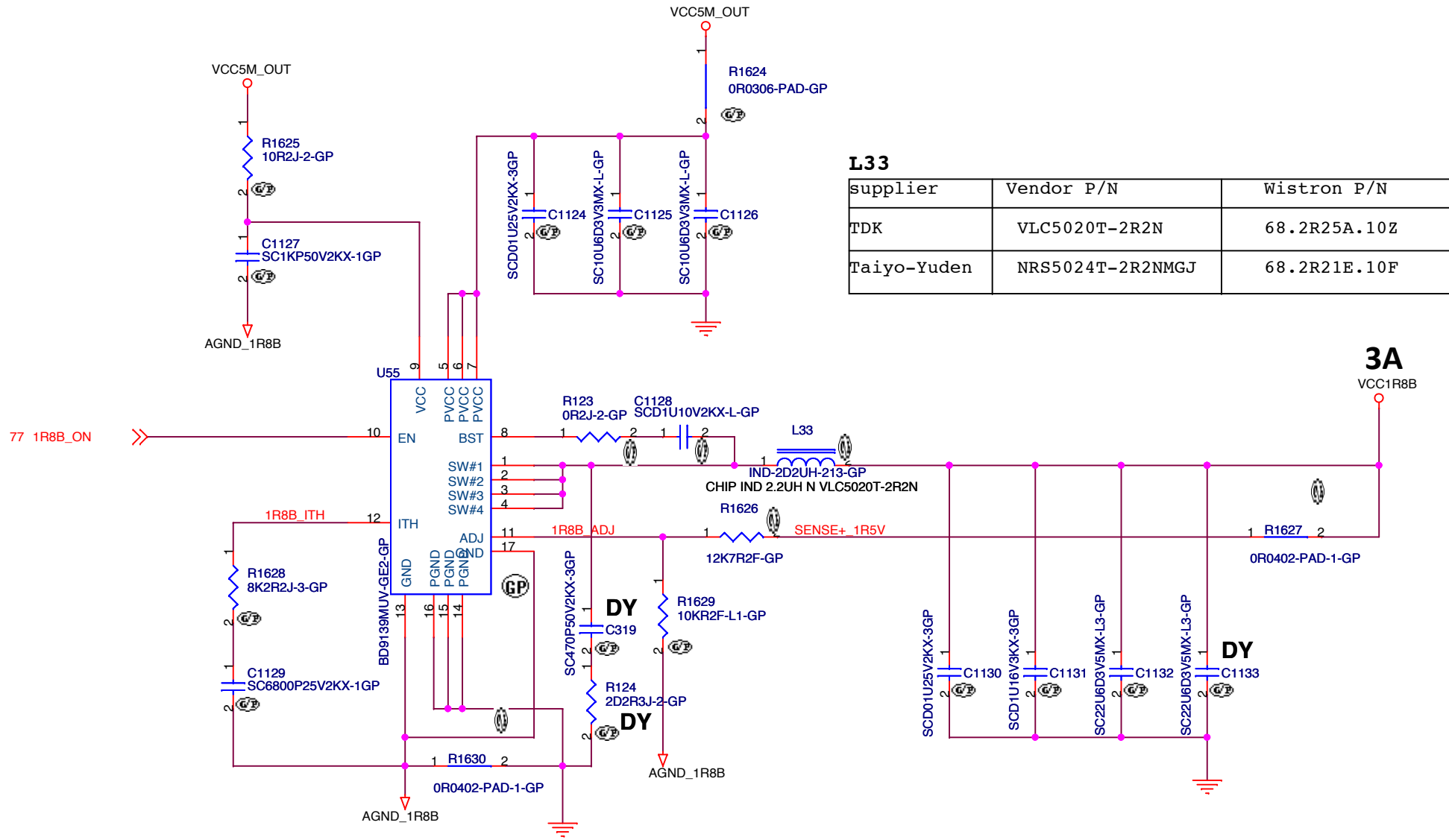
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### L33

supplier	Vendor P/N	Wistron P/N
TDK	VLC5020T-2R2N	68.2R25A.10Z
Taiyo-Yuden	NRS5024T-2R2NMGJ	68.2R21E.10F

<Core Design>

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Taipei Hsien 221, Taiwan, R.O.C.

Title

**DC-DC VCC1R8B**

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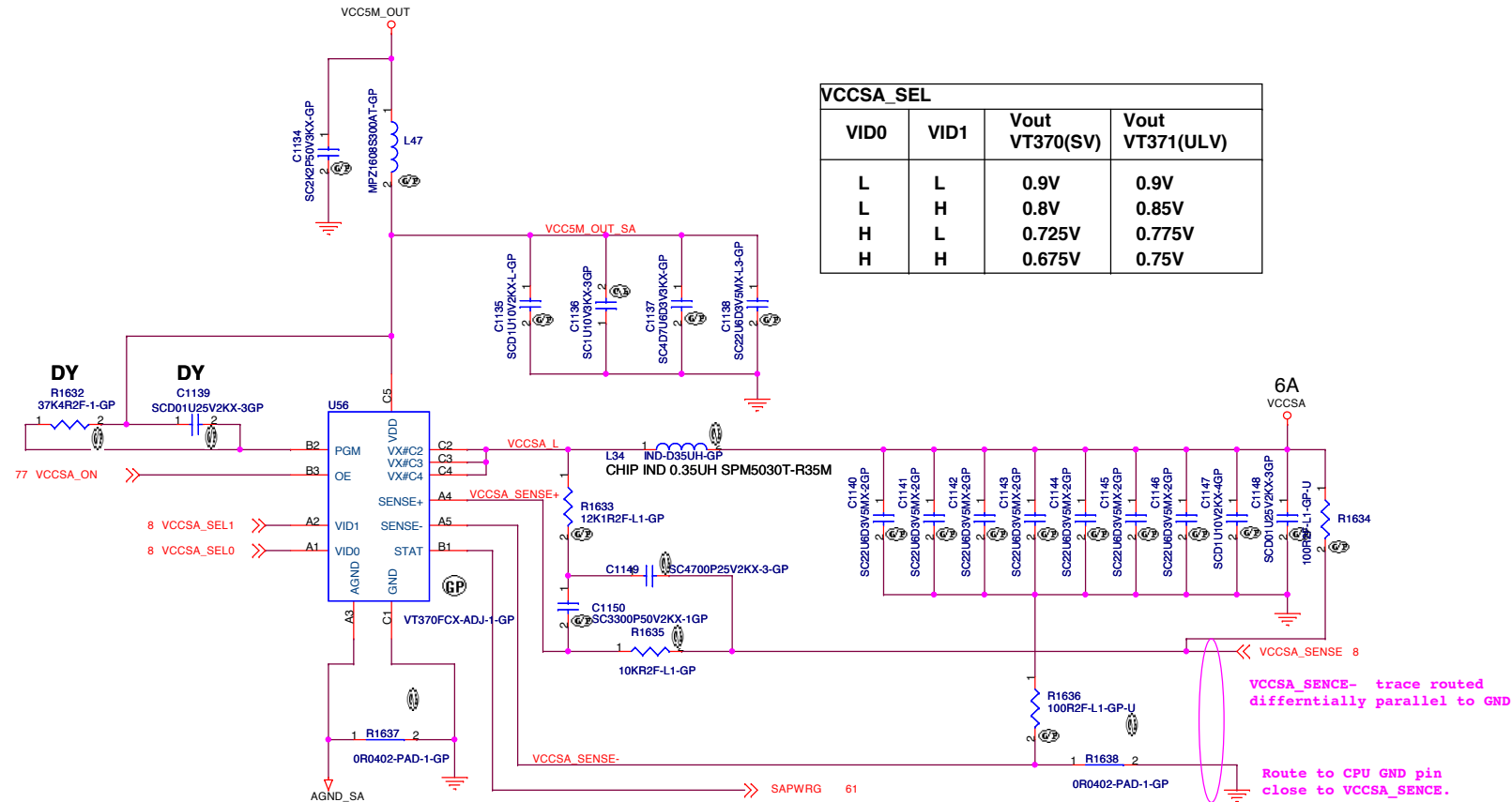
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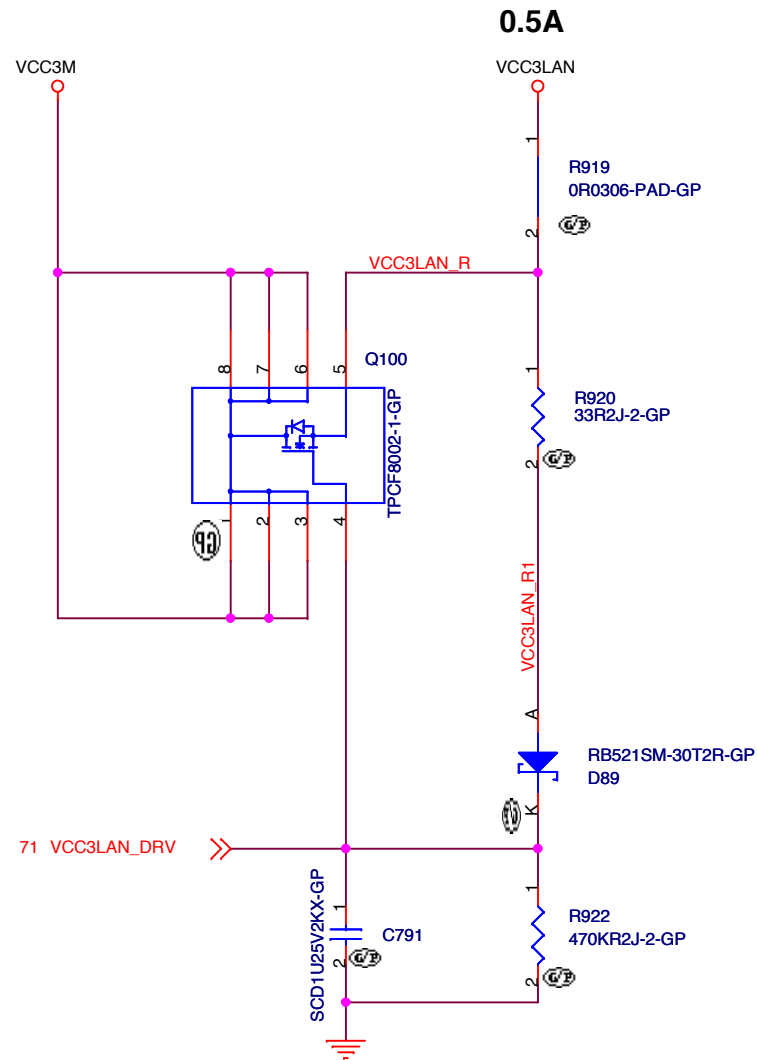
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-1

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Title

**LOAD SW LAN**

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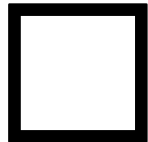
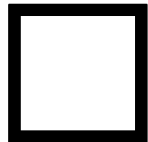
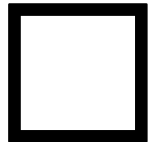
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D

C

B

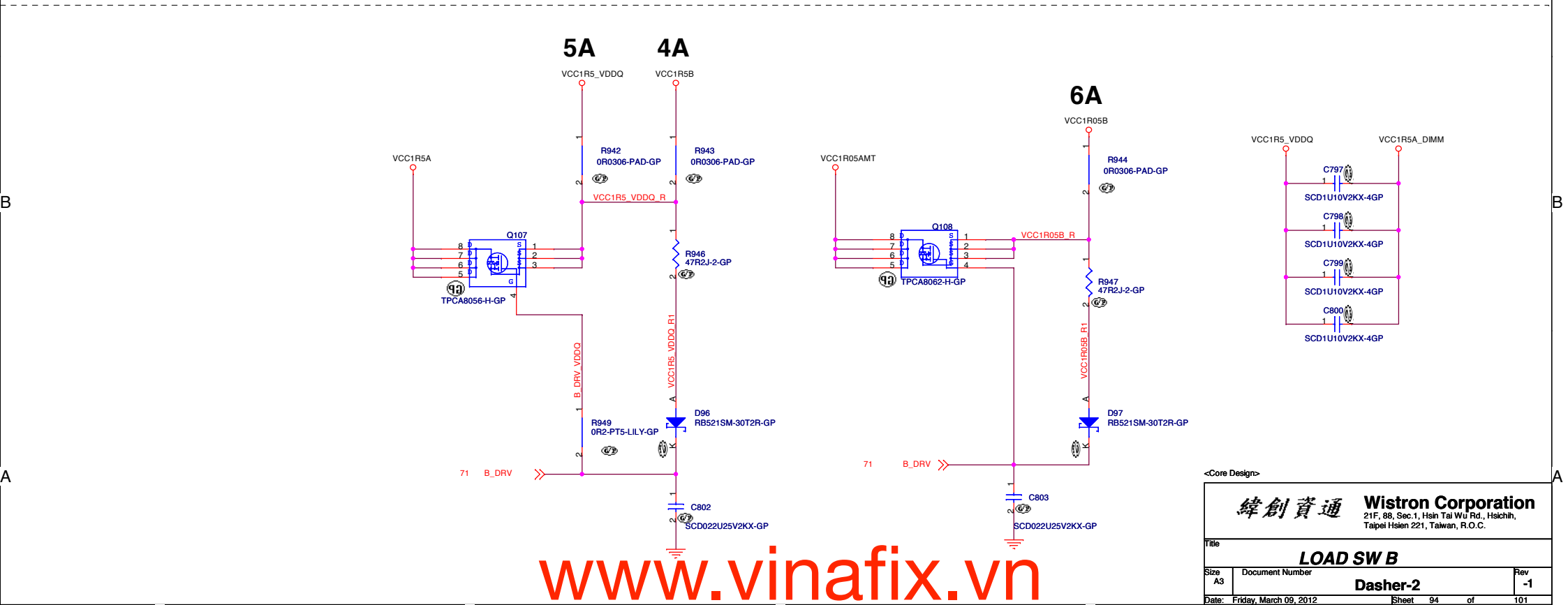
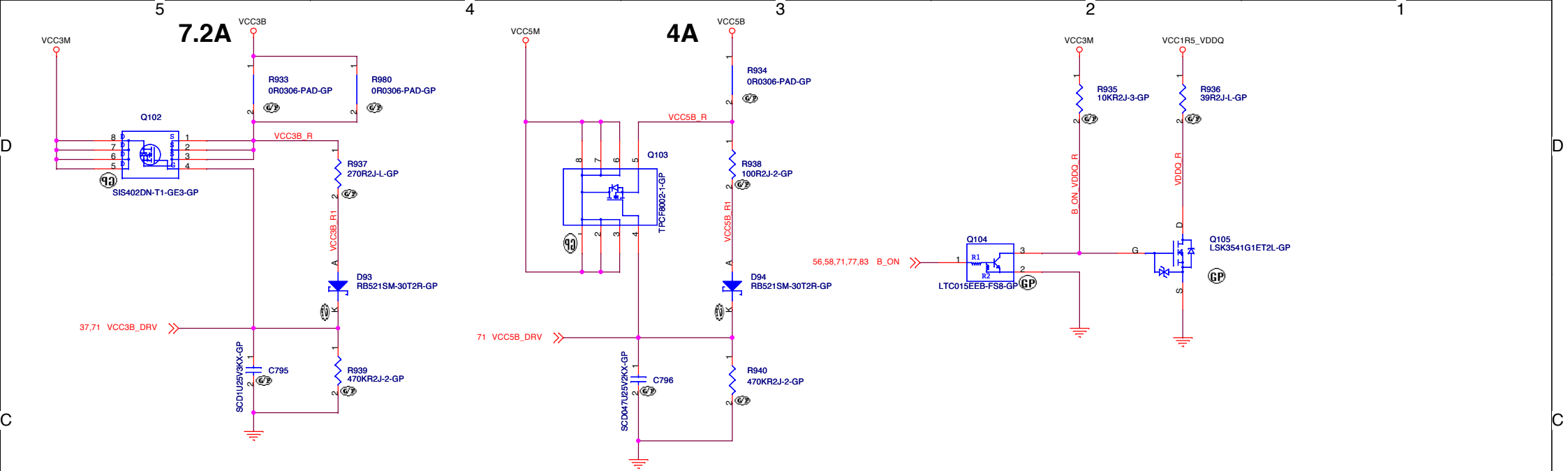
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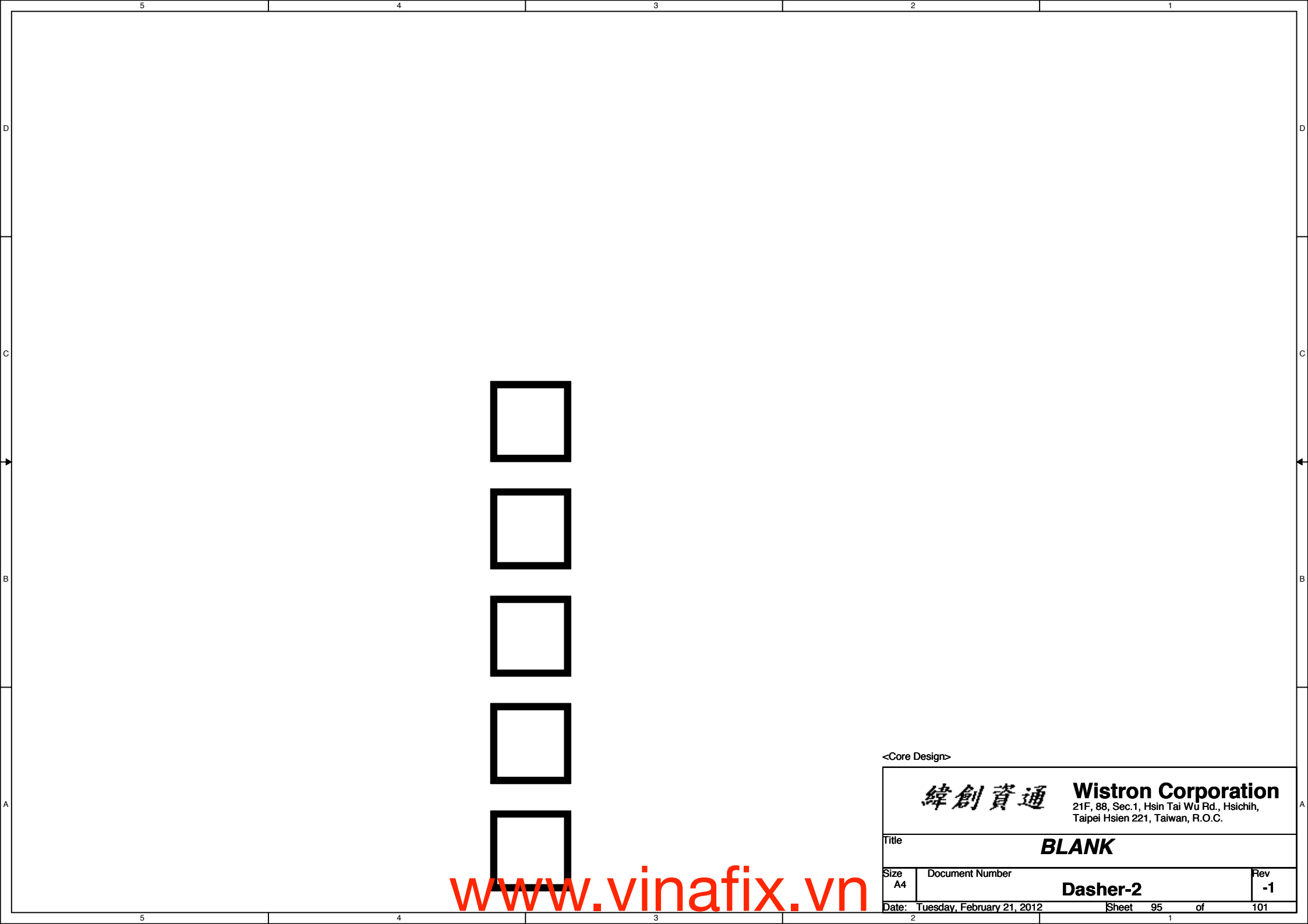
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Title

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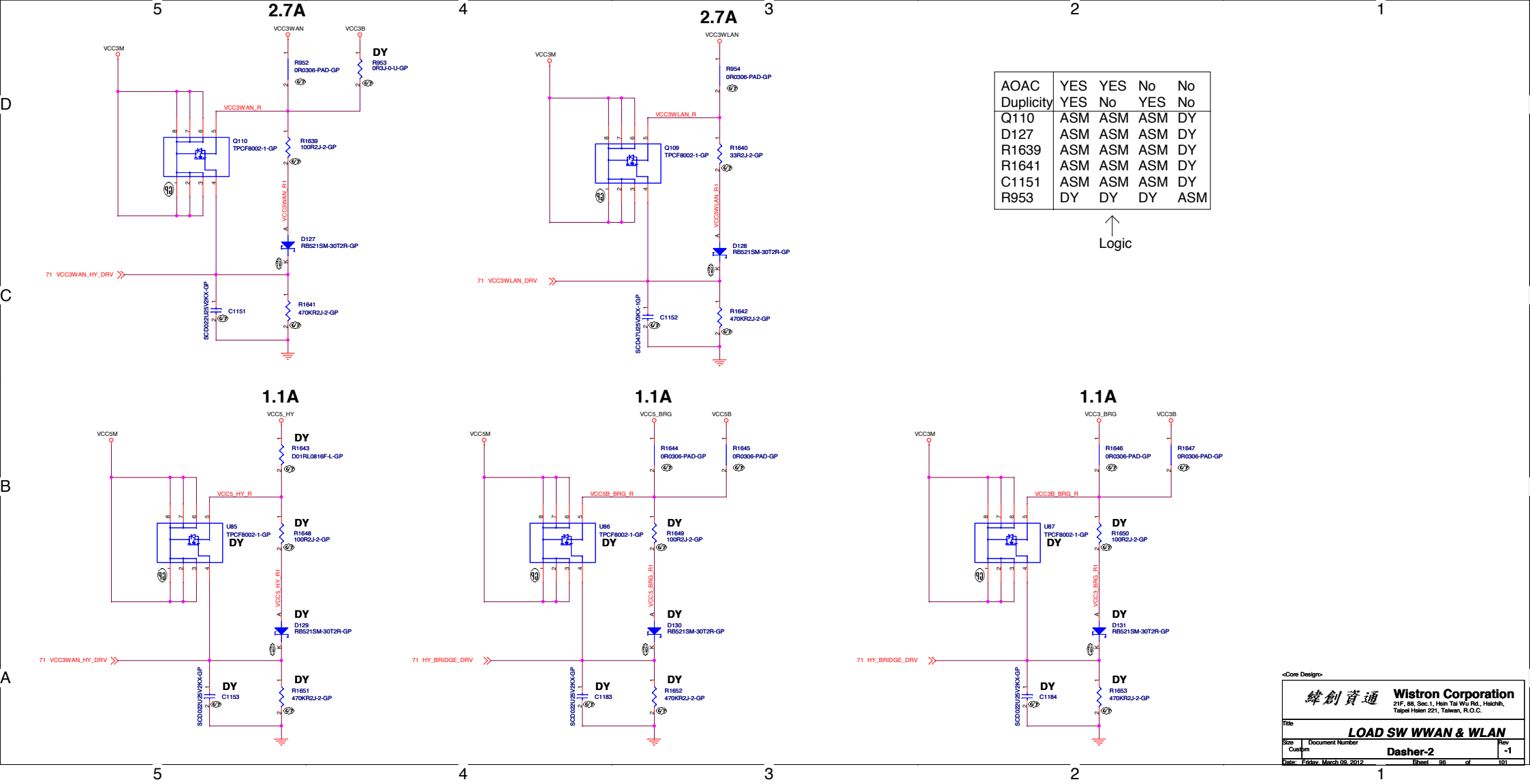
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**Dasher-2**

Rev  
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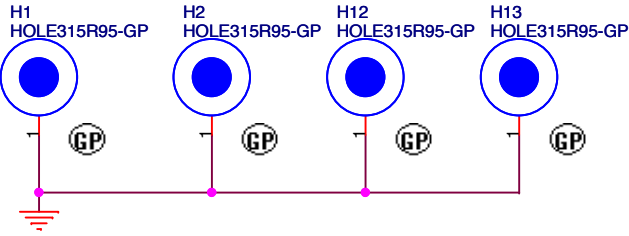
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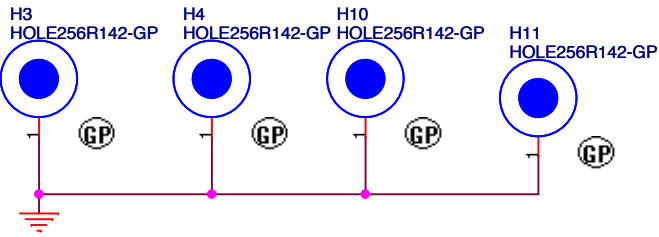




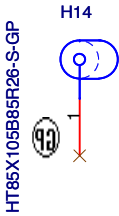
ZZ.00PAD.911



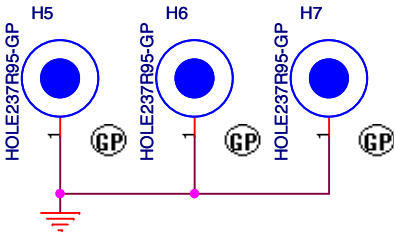
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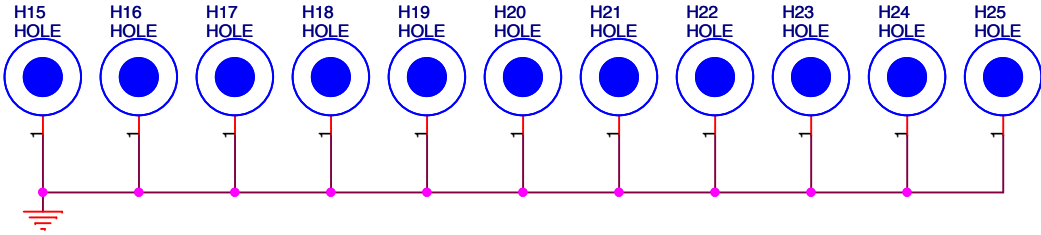
ZZ.SCREW.191



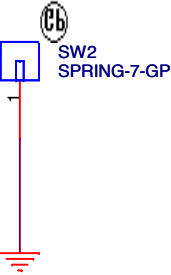
ZZ.00PAD.921



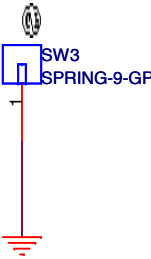
ZZ.OHOLE.XXX



34.49U26.001  
GND pad for docking connector



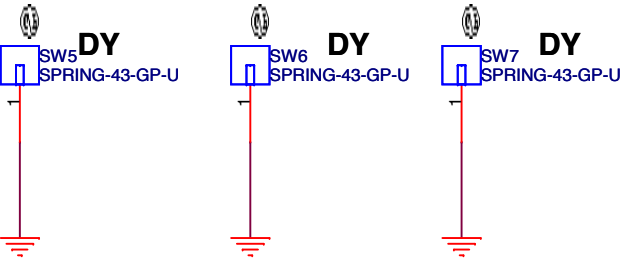
34.49U23.001  
GND pad for docking connector



34.15J03.001  
spring for EMI



34.15J03.001  
spring for RF (SW5~SW7)



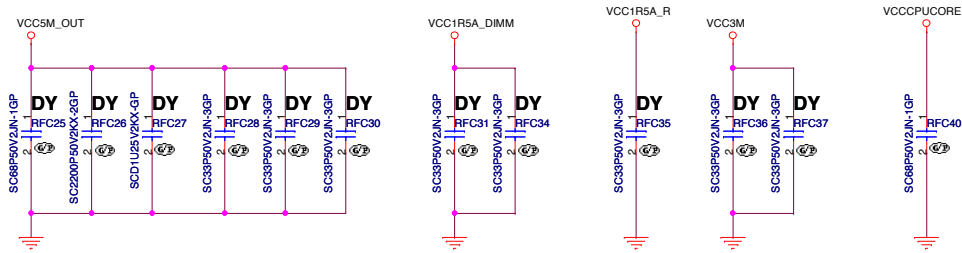
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Title <b>HOLES/GND/PADS</b>		
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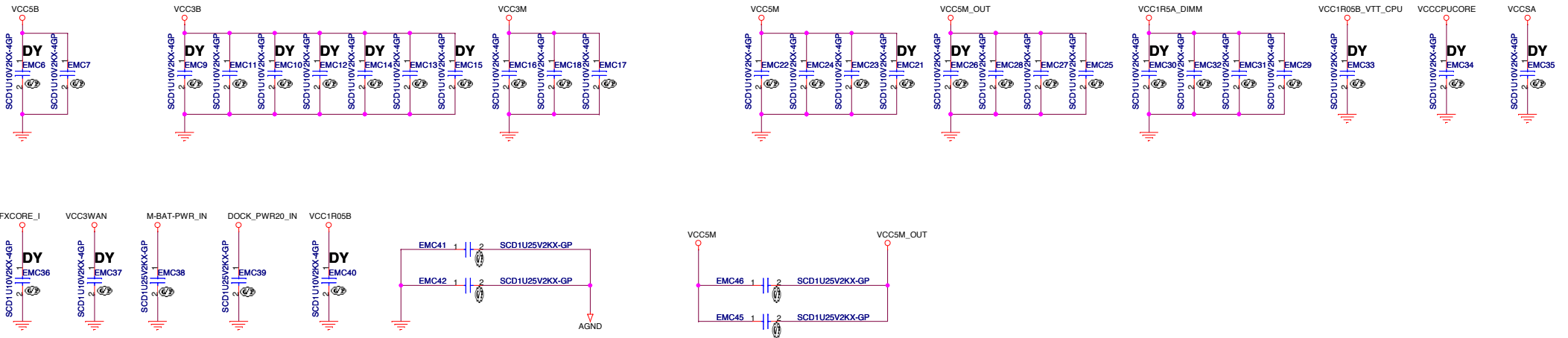


# RF decoupling caps

named as RFCxxx



## Long power trace EMI decoupling caps



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Title EMI DECOUPLING

Size Custom Document Number Dasher-2 Rev -1

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		SV		ULV	
Master IC		U48	VT1318M	VT1318M	
# of slave for CPU		2		1	
Slave for CPU		U47	VT1324S	VT1324S	
Inductor for CPU		U49	VT1324S	no stuff	
		L28	BPW10040	no stuff	
		L14	no stuff	MPCH0730LR12	
# of slave for GPU		1		1	
Slave for GPU		U50	VT1324S	VT1324S	
Inductor for GPU		L29	MPCH0730LR12	MPCH0730LR12	

			SV			ULV		
R_SEL[0]	pin 38	R1574	715	0.5%	280	1.0%		
R_SEL[1]	pin 37	R1575	887	0.5%	825	0.5%		
R_SEL[2]	pin 36	R1576	0	5.0%	0	5.0%		
R_SEL[3]	pin 35	R1577	196	0.5%	196	0.5%		
R_SEL[4]	pin 44	R1571	825	0.5%	825	0.5%		
R_SEL[5]	pin 32	R1579	475	1.0%	402	0.5%		
R_SEL[6]	pin 48	R1567	21.5	1.0%	21.5	1.0%		
R_REF	pin 34	R1578	20K	0.5%	20K	0.5%		
ROSC	pin 39	R1572	75K	1.0%	61.9K	1.0%		

				SV			ULV		
LL_R1_CPU	pin 15 - pin 16	R1599	10K	0.5%	10K	0.5%			
LL_R2_CPU	pin 16 - pin 17	R1600	20K	0.5%	7.5K	0.5%			
	pin 16 - pin 17	R1601	787	1.0%	453	1.0%			
LL_C1_CPU	pin 15 - pin 16	C1086	56pF		33pF				
LL_C2_CPU	pin 16 - pin 17	C1087	22pF		22pF				
LL_RLEAD_CPU	pin 15 - pin 16	R1595	30.1K	1.0%	15K	1.0%			
LL_CLEAD_CPU	pin 15 - pin 16	C1091	220pF		680pF				
LL_RLAG_CPU	pin 16 - pin 17	R1596	no stuff		no stuff				
LL_CLAG_CPU	pin 16 - pin 17	C1092	no stuff		no stuff				
RDES_CPU	pin 17 - pin 18	R1592	845	0.5%	487	0.5%			
RINT_CPU	pin 18 - pin 19	R1593	1.3K	1.0%	2.74K	1.0%			
CINT_CPU	pin 18 - pin 19	C1088	4700PF		3300pF				
RPH11	IPH1_1 - pin 18	R1598	499	1.0%	750	1.0%			
RPH12	IPH1_2 - pin 18	R1594	499	1.0%	no stuff	1.0%			
IPHF11_R	pin 11	R1582	1.96K	1.0%	1.96K	1.0%			
IPHF11_C	pin 11	C1076	10pF		no stuff				
IPHF12_R	pin 10	R1581	1.96K	1.0%	no stuff	1.0%			
IPHF12_C	pin 10	C1075	10pF		no stuff				
R_MRAMP1	pin 12	R1584	15.8K	1.0%	13K	1.0%			
R_MRAMP1_PU	pin 12	R1587	60.4K	1.0%	43.2K	1.0%			
R_PWM2	Pin 6	R4	no stuff		0	5.0%			
LL_R1_GPU	pin 24 - pin 23	R1609	10K	0.5%	10K	0.5%			
LL_R2_GPU	pin 23 - pin 22	R1610	7.5K	0.5%	7.87K	0.5%			
	pin 23 - pin 22	R1611	237	1.0%	0	5.0%			
LL_C1_GPU	pin 24 - pin 23	C1094	22pF		22pF				
LL_C2_GPU	pin 23 - pin 22	C1095	22pF		22pF				
LL_RLEAD_GPU	pin 24 - pin 23	R1606	30K	1.0%	30K	1.0%			
LL_CLEAD_GPU	pin 24 - pin 23	C1097	1200pF		1000pF				
LL_RLAG_GPU	pin 23 - pin 22	R1607	no stuff		no stuff				
LL_CLAG_GPU	pin 23 - pin 22	C1098	no stuff		no stuff				
RDES_GPU	pin 22 - pin 21	R1604	665	0.5%	665	0.5%			
RINT_GPU	pin 21 - pin 20	R1605	3.24K	1.0%	3.24K	1.0%			
CINT_GPU	pin 21 - pin 20	C1096	10nF		10nF				
RPH21	IPH2_1 - pin 21	R1608	1K	1.0%	1K	1.0%			
IPHF21_R	pin 28	R1580	1.96K	1.0%	1.96K	1.0%			
IPHF21_C	pin 28	C1077	no stuff		no stuff				
R_MRAMP2	pin 27	R1586	15.4K	1.0%	15.4K	1.0%			
R_MRAMP2_PU	pin 27	R1590	56.2K	1.0%	56.2K	1.0%			

			SV		ULV	
		C1090	4.7uF	no stuff		
		R1589	10 1.0%	no stuff		
		C1089	0.1uF	no stuff		
		C1082	0.22uF	no stuff		
		C1083	1uF	no stuff		
		C1080	1uF	no stuff		
		C1084	10uF	no stuff		
		C1079	10uF	no stuff		
		C1081	10uF	no stuff		
		C1085	10uF	no stuff		
		L50	MPZ1608S300A	no stuff		
		L51	MPZ1608S300A	no stuff		
		C1078	2200pF	no stuff		

Vcore Coutput

		SV		ULV	
C687	22uF	0805	22uF	0805	
C669	22uF	0603	22uF	0603	
C671	22uF	0603	22uF	0603	
C673	4.7uF	0603	22uF	0603	
C675	22uF	0603	22uF	0603	
C676	22uF	0603	22uF	0603	
C678	22uF	0603	22uF	0603	
C695	4.7uF	0603	22uF	0603	
C681	4.7uF	0603	22uF	0603	
C682	4.7uF	0603	22uF	0603	
C683	22uF	0603	22uF	0603	
C750	22uF	0603	22uF	0603	
C686	22uF	0603	22uF	0603	
C691	22uF	0603	22uF	0603	
C670	22uF	0603	22uF	0603	
C694	22uF	0603	22uF	0603	
C698	4.7uF	0603	22uF	0603	
C1112	22uF	0603	22uF	0603	
C1113	22uF	0603	22uF	0603	
C672	22uF	0603	22uF	0603	
C1157	22uF	0603	22uF	0603	
C674	22uF	0603	22uF	0603	
C1160	22uF	0603	22uF	0603	
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C1167	22uF	0603	22uF	0603	
C680	22uF	0603	22uF	0603	
C679	4.7uF	0603	10uF	0603	
C677	4.7uF	0603	10uF	0603	
C1156	4.7uF	0603	10uF	0603	
C684	10uF	0603	10uF	0603	
C693	4.7uF	0603	10uF	0603	
C1159	10uF	0603	10uF	0603	
C692	10uF	0603	10uF	0603	
C1158	4.7uF	0603	10uF	0603	
C1161	4.7uF	0603	10uF	0603	
C1162	4.7uF	0603	10uF	0603	
C690	N/A	0603	N/A	0603	
C1109	N/A	0603	N/A	0603	
C1110	N/A	0603	N/A	0603	
C1111	N/A	0603	N/A	0603	
C1163	N/A	0603	N/A	0603	
C1164	22uF	0603	N/A	0603	
C1165	N/A	0603	N/A	0603	
C1166	22uF	0603	N/A	0603	
C1168	N/A	0603	N/A	0603	
C1169	22uF	0603	N/A	0603	
C444	2.2uF	0402	2.2uF	0402	
C445	2.2uF	0402	2.2uF	0402	
C446	2.2uF	0402	2.2uF	0402	
C447	2.2uF	0402	2.2uF	0402	
C448	2.2uF	0402	2.2uF	0402	
C449	2.2uF	0402	2.2uF	0402	
C450	2.2uF	0402	N/A	0402	
C451	2.2uF	0402	2.2uF	0402	
C452	2.2uF	0402	2.2uF	0402	
C453	2.2uF	0402	2.2uF	0402	

		SV		ULV	
C477	2.2uF	0402	2.2uF	0402	
C486	2.2uF	0402	2.2uF	0402	
C518	2.2uF	0402	2.2uF	0402	
C520	2.2uF	0402	2.2uF	0402	
C521	2.2uF	0402	2.2uF	0402	
C522	2.2uF	0402	N/A	0402	
C533	2.2uF	0402	2.2uF	0402	
C830	2.2uF	0402	2.2uF	0402	
C831	2.2uF	0402	2.2uF	0402	
C832	2.2uF	0402	2.2uF	0402	
C1172	N/A	0402	N/A	0402	
C1173	2.2uF	0402	N/A	0402	
C1174	2.2uF	0402	N/A	0402	
C1175	2.2uF	0402	N/A	0402	
C1176	N/A	0402	N/A	0402	
C1177	N/A	0402	N/A	0402	
C1178	2.2uF	0402	2.2uF	0402	
C1179	2.2uF	0402	2.2uF	0402	
C1180	2.2uF	0402	2.2uF	0402	
C1181	2.2uF	0402	2.2uF	0402	
C1182	2.2uF	0402	2.2uF	0402	
C1185	2.2uF	0402	2.2uF	0402	
C1186	2.2uF	0402	2.2uF	0402	
C1187	2.2uF	0402	2.2uF	0402	
C1188	2.2uF	0402	2.2uF	0402	
C1189	2.2uF	0402	2.2uF	0402	
C1190	2.2uF	0402	2.2uF	0402	
C1191	2.2uF	0402	2.2uF	0402	
C1192	2.2uF	0402	2.2uF	0402	
C1193	2.2uF	0402	2.2uF	0402	
C1194	2.2uF	0402	2.2uF	0402	
C1195	2.2uF	0402	2.2uF	0402	
C1196	2.2uF	0402	2.2uF	0402	

<Core Design>

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Title		VT1318M TABLE	
Size	Document Number	Dasher-2	Rev
A2			-1
Date: Tuesday, March 06, 2012		Issue: 100	of 101

Capability												
Config	1	2	3	4	5	6	7	8	9	10	11	12
AOAC	Yes	Yes	Yes	Yes	No	No	No	No	Yes	No	Yes	No
Anti-Theft	Yes	Yes	No	No	Yes	Yes	No	No	Yes	Yes	No	No
External EEPROM	Yes	No	Yes	No	Yes	No	Yes	No	Yes	Yes	Yes	Yes
Duplicity	No	No	No	No	No	No	No	No	Yes	Yes	Yes	Yes
U66	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
U67	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
C864	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R978	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R929	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R901	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R930	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R1462	ASM	ASM	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
Q14	ASM	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
Q48	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R624	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R536	DY	DY	DY	DY	ASM	ASM	DY	DY	DY	DY	DY	DY
R539	DY	DY	DY	DY	ASM	ASM	DY	DY	DY	DY	DY	DY
U42	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
Q47	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
R617	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
C524	ASM	DY	ASM	DY	ASM	DY	ASM	DY	ASM	ASM	ASM	ASM
D111	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	DY	DY
R1463	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1464	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
U83	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
U84	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
C1020	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1466	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM
R1468	DY	DY	DY	DY	DY	DY	DY	DY	ASM	ASM	ASM	ASM

↑  
Logic

Duplicity	Y	Y	N	N
Battery Authentication	Y	N	Y	N
--> External EEPROM	Y	Y	Y	N

↑  
Logic